

INTRODUCTION:

- \* The gain of an amplifier does depend on the parameters of the device and circuit components, there exists an upper theoretical limit for the gain obtainable from single stage.
- \* The Voltage amplification & power gain & frequency response obtained with a single stage of amplification is usually not sufficient to meet the needs of either a composite electronic circuit or load device.

\* For Example:

A Speaker represents a heavy load in an audio amplifier system, and several amplifier stages may be required to "boost a signal" originating at a microphone or magnetic tape head to a level sufficient to provide a large amount of power to the speaker.

- \* We hear of pre-amplifiers, power amplifiers and output amplifiers, all of which constitute stages of amplification in such a system.

\* It may be emphasized here that a practical amplifier is always a multistage amplifier that may provide a higher voltage & current gain or both.

## CLASSIFICATION OF AMPLIFIERS:

- \* A circuit that increases the amplitude of the given i/p signal is an amplifier.
- \* Amplifiers can be classified as follows:
  - 1) Based on transistor configuration:
    - a) Common Emitter Amplifier
    - b) Common collector Amplifier
    - c) Common Base Amplifier
  - 2) Based on the Active device
    - a) BJT amplifier
    - b) FET amplifier
  - 3) Based on the Q-point (Operating Condition)
    - a) Class-A amplifier
    - b) Class B Amplifier
    - c) Class AB Amplifier
    - d) Class C Amplifier
  - 4) Based on the Number of Stages
    - a) Single stage amplifier
    - b) Multi stage amplifiers
  - 5) Based on the output
    - a) Voltage Amplifier
    - b) Power amplifier

6) Based on the Frequency Response

- Audio frequency (AF) Amplifier
- Intermediate frequency (IF) Amplifier
- Radio frequency (RF) Amplifier

7) Based on the Bandwidth

- Narrow band amplifier (normally RF amplifier)
- Wide band amplifier (normally Video amplifier)

### MULTISTAGE AMPLIFIER:

Definition: An Amplifier that produces Voltage, Current or power gain through the use of two or more stages is called multistage Amplifier.

\* It is to be noted that the output of the first stage makes the input for the second stage, the output of the second stage makes the input for third stage and so on.

\* A multistage amplifier can be represented by a block diagram, as shown.

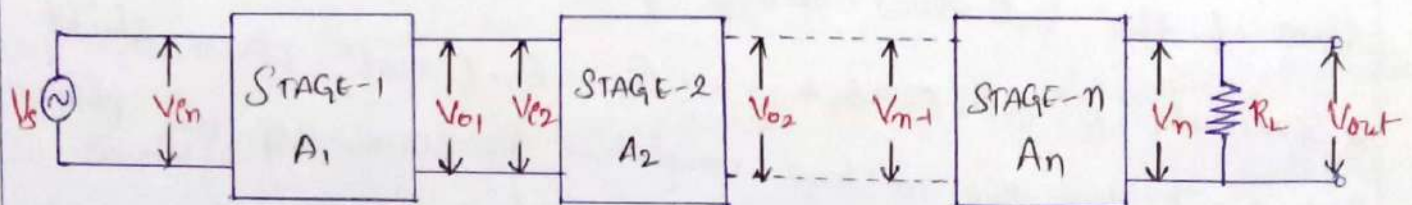


FIG: n-STAGE AMPLIFIER

\* The Signal Voltage  $V_s$  is applied to the input of 1<sup>st</sup> stage and final output is available at the output terminal of last stage.



\* Let  $A_{V1}, A_{V2}, A_{V3} \dots A_{Vn-1}, A_{Vn}$  are Voltage gains of individual stages.  $A_V$  be the overall Voltage gain.

\* By definition of Voltage gain

$$A_V = \frac{V_o}{V_i}, \quad A_{V1} = \frac{V_{o1}}{V_{in1}}, \quad A_{V2} = \frac{V_{o2}}{V_{i2}}, \dots, \quad A_{Vn-1} = \frac{V_{on-1}}{V_{in-1}}, \quad A_{Vn} = \frac{V_{on}}{V_{in}}$$

Here,  $A_V = \frac{V_{o1}}{V_{i1}} * \frac{V_{o2}}{V_{i2}} * \dots * \frac{V_{on-1}}{V_{in-1}} * \frac{V_{on}}{V_{in}}$

$$\boxed{A_V = A_{V1} * A_{V2} * \dots * A_{Vn-1} * A_{Vn}} \rightarrow (1)$$

\* Hence, Overall Voltage gain of n-stage amplifier is the product of Voltage gains of individual stage.

Taking logarithm on both sides for eq (1)

$$20 \log_{10} A_V = 20 \log_{10} [A_{V1} * A_{V2} * \dots * A_{Vn-1} * A_{Vn}]$$

$$20 \log_{10} A_V = 20 \log_{10} A_{V1} + 20 \log_{10} A_{V2} + \dots + 20 \log_{10} A_{Vn-1} + 20 \log_{10} A_{Vn}$$

$$A_V(\text{dB}) = A_{V1}(\text{dB}) + A_{V2}(\text{dB}) + \dots + A_{Vn-1}(\text{dB}) + A_{Vn}(\text{dB})$$

\* Hence, overall Voltage gain in dB of multistage amplifier is sum of the individual Voltage gain in dB.

\* Similarly  $\phi = \phi_1 + \phi_2 + \phi_3 + \dots + \phi_{n-1} + \phi_n$ . Overall phase shift introduced by the n-stage amplifier is sum of the phase shifts introduced by the individual stages.



\* In a multistage amplifier, the output of first stage is combined to the next stage through a coupling device. The process is known as Cascading.

\* The process of joining two amplifier stages using a coupling device is called Cascading.

### Need of Coupling:

→ To transfer the AC output of one stage to the i/p of next stage.

→ Block the dc to pass from one stage to the next stage i.e., to isolate dc conditions.

For an ideal coupling network the following requirements should be fulfilled.

- i. The direct currents should not pass through the coupling network.
- ii. AC signal waveform should transfer from one amplifier to next amplifier without distortion.
- iii. Some voltage loss of signal cannot be avoided in coupling network but this loss should be minimum just negligible.
- iv. Coupling network impedance should not be frequency dependent.

\* Unfortunately, there is no coupling network which fulfills all the demands.

## TYPES OF COUPLING NETWORKS:

(6)

\* There are three types of interstage coupling networks. They are,

1. R-C Coupling
2. Transformer Coupling
3. Direct Coupling.

### 1. R-C COUPLING:

\* The circuit diagram of RC Coupled Amplifier is as shown below.

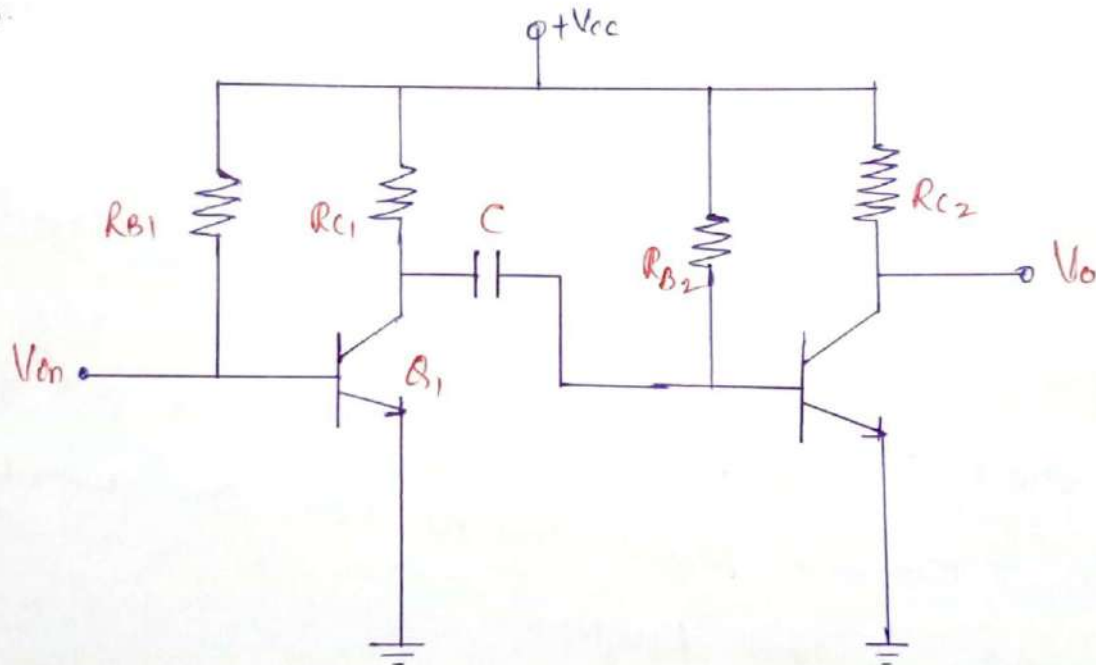


FIG: R-C COUPLED AMPLIFIER

\* In RC Coupling AC output of first stage is given to the input of second stage by a collector resistor and coupling capacitor.

\* Coupling Network consists of R and C components.

\* The coupling capacitor (C) isolates the DC conditions of one stage from the next stage.

\* The Coupling Capacitor ( $C_c$ ) isolates the DC Conditions of one stage acts as short circuit for AC signals and open circuit for DC Conditions.

\* The amplifier using this type of RC Coupling are called RC Coupled Amplifier.

#### APPLICATIONS:

1. Used in all Audio Small signal amplifiers
2. Used in tape recorders, Radio Receivers, TV receivers.

#### 2. TRANSFORMER COUPLING:

- \* The Coupling device in Coupling network is a transformer.
- \* In this method primary winding of the transformer acts as collector load and Secondary winding transfers ac output signal directly to the base of next stage.
- \* This type of Coupling increases the overall circuit gain and the level of interstage impedance matching.
- \* This is restricted to power amplifiers where efficiency and impedance matching are critical requirements.

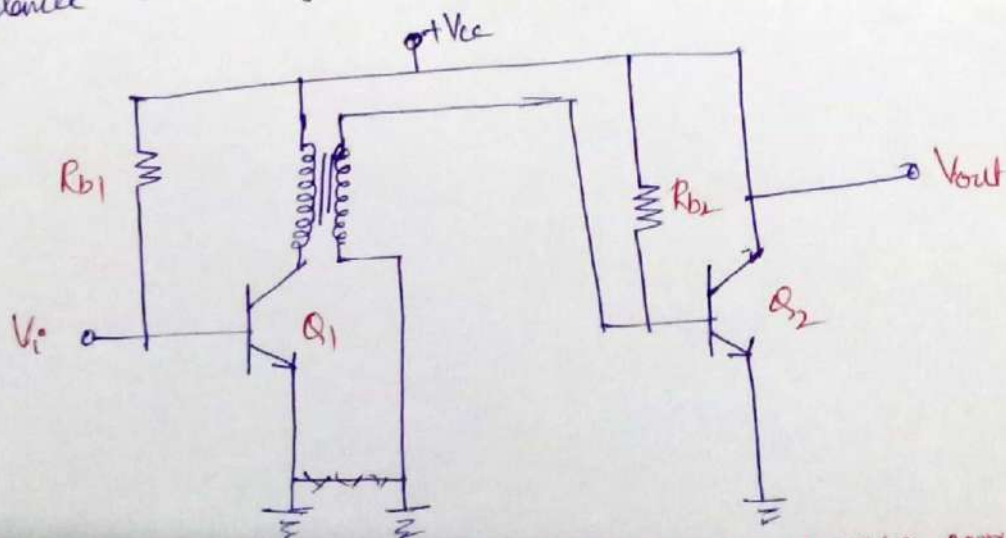


FIG: TRANSFORMER COUPLED AMPLIFIER



\* The amplifiers using Transformer Coupling are called Transformer Coupled Amplifiers. (2)

### 3. DIRECT COUPLING:

\* In this method ac o/p signal is directly fed to the input of next stage.

\* Here, difficulty is included in the Coupling network, biasing conditions are disturbed to avoid this special dc Voltage load, circuits are used to match the output dc loads.

\* It is used where amplifications of low frequency is to be done.

\* Coupling devices like capacitors, transformers are not used because of their size complexity.

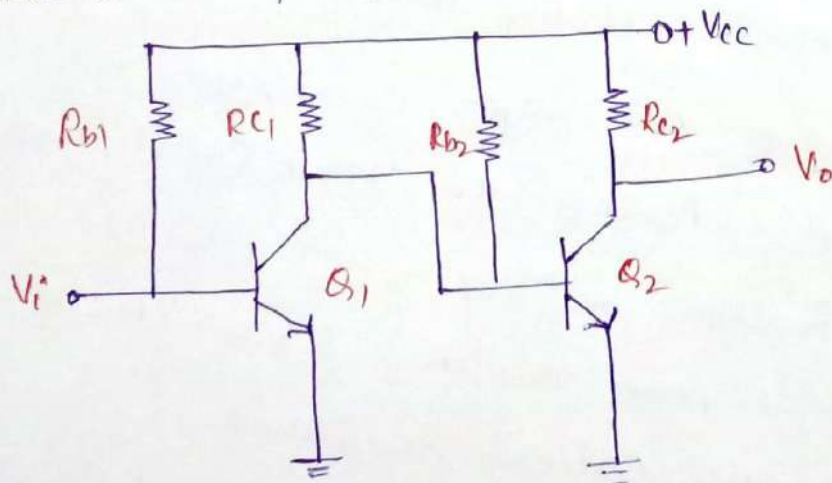
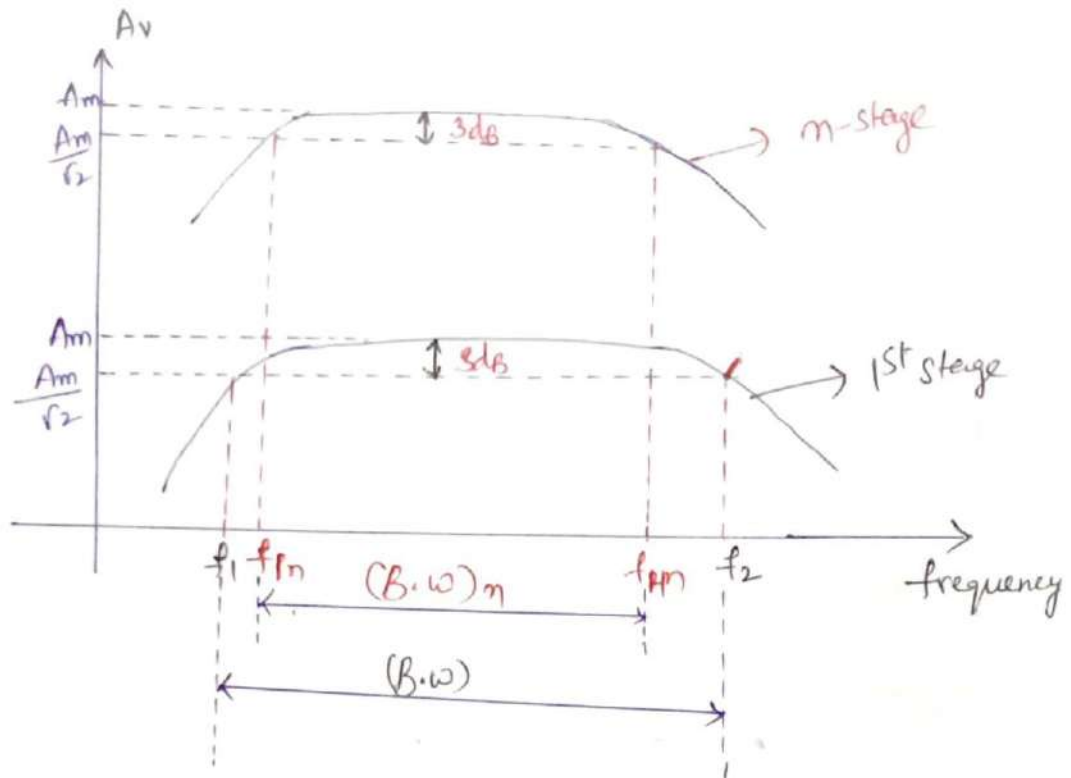


FIG: DIRECT COUPLED AMPLIFIER

## FREQUENCY RESPONSE OF MULTISTAGE AMPLIFIERS:

\* Frequency response of multistage amplifier is as shown below



\* For a single stage of amplifier Voltage gain at low frequencies and high frequencies are given as,

$$A_L = \frac{A_m}{1 - j\left(\frac{f_1}{f}\right)}, \quad A_H = \frac{A_m}{1 + j\left(\frac{f}{f_2}\right)}$$

∴ where  $f_1$  &  $f_2$  are lower and upper cut-off frequencies at which Voltage gain falls to  $1/\sqrt{2}$  of its maximum value.

$$|A_L| = \frac{A_m}{\sqrt{1 + \left[\frac{f_1}{f}\right]^2}} \quad \& \quad |A_H| = \frac{A_m}{\sqrt{1 + \left[\frac{f}{f_2}\right]^2}}$$

## OVERALL LOWER CUT-OFF FREQUENCY ( $f_{Ln}$ )

(10)

⇒ For n-stage cascade amplifiers

$$(A_L)^n = \left[ \frac{A_m}{1 - j\left[\frac{f_1}{f}\right]} \right]^n$$

$$\left| \frac{A_L}{A_m} \right|^n = \left[ \frac{1}{\sqrt{1 + \left[\frac{f_1}{f}\right]^2}} \right]^n \rightarrow \textcircled{1}$$

At  $f = f_{Ln}$

$$\left| \frac{A_L}{A_m} \right| = \left[ \frac{1}{\sqrt{2}} \right]^n \rightarrow \textcircled{2}$$

Equating  $\textcircled{1}$  &  $\textcircled{2}$

$$\left[ \frac{1}{\sqrt{2}} \right]^n = \left[ \frac{1}{\sqrt{1 + \left[\frac{f_1}{f}\right]^2}} \right]^n$$

$$\frac{1}{\sqrt{1 + \left[\frac{f_1}{f}\right]^2}} = \left[ \frac{1}{\sqrt{2}} \right]^{\frac{1}{n}}$$

$$\sqrt{1 + \left[\frac{f_1}{f}\right]^2} = [\sqrt{2}]^{\frac{1}{n}}$$

Squaring on both sides

$$1 + \left(\frac{f_1}{f_{Ln}}\right)^2 = 2^{\frac{1}{n}}$$

$$\left(\frac{f_1}{f_{Ln}}\right)^2 = 2^{\frac{1}{n}} - 1$$

$$\frac{f_1}{f_{Ln}} = \sqrt{2^{\frac{1}{n}} - 1}$$

$$\boxed{f_{Ln} = \frac{f_1}{\sqrt{2^{\frac{1}{n}} - 1}}}$$



## OVERALL UPPER CUT-OFF FREQUENCY $f_{un}$ :

(11)

$$\left| \frac{A_H}{A_m} \right|^n = \left[ \frac{1}{\sqrt{1 + \left[ \frac{f}{f_2} \right]^2}} \right]^n \rightarrow (1)$$

$$\text{At } f = f_{un} \Rightarrow \left| \frac{A_H}{A_m} \right|^n = \frac{1}{\sqrt{2}} \rightarrow (2)$$

Equating (1) & (2)

$$\frac{1}{\sqrt{2}} = \left[ \frac{1}{\sqrt{1 + \left[ \frac{f}{f_2} \right]^2}} \right]^n$$

$$\frac{1}{\sqrt{1 + \left[ \frac{f}{f_2} \right]^2}} = \left[ \frac{1}{\sqrt{2}} \right]^n$$

$$\sqrt{1 + \left[ \frac{f}{f_2} \right]^2} = [\sqrt{2}]^n$$

Squaring on both sides

$$1 + \left[ \frac{f}{f_2} \right]^2 = 2^n$$

$$\left[ \frac{f}{f_2} \right]^2 = 2^n - 1$$

$$\left[ \frac{f_{un}}{f_2} \right]^2 = \sqrt{2^n - 1}$$

$$\boxed{f_{un} = \frac{f_2}{\sqrt{2^n - 1}}}$$

\* The most popular cascade amplifier is formed by cascading several CE amplifier stages.

\* The n-stage CE amplifier is as shown below.

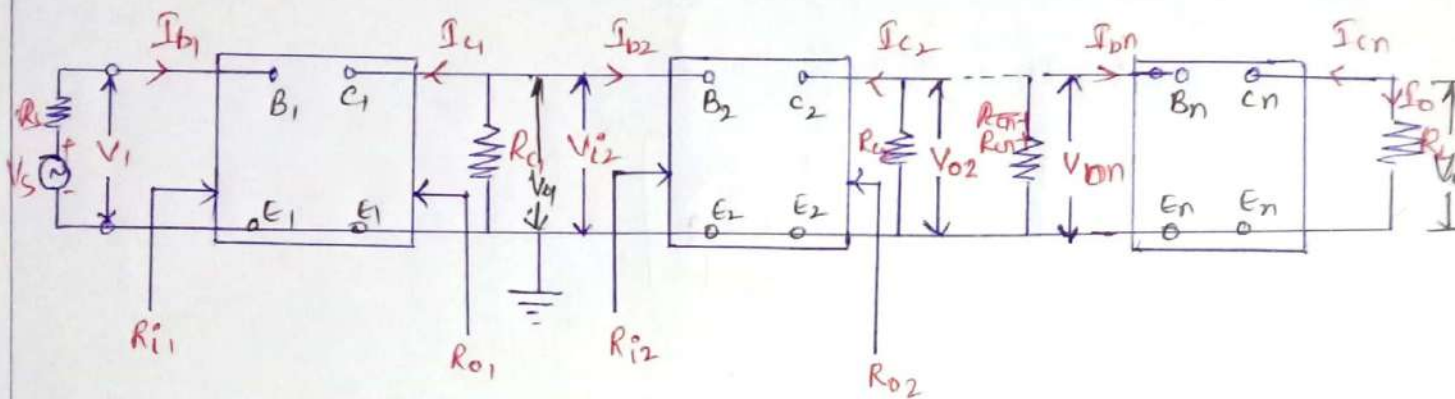


FIG: n-STAGE CE AMPLIFIER

### VOLTAGE GAIN:

\* In multistage amplifier the output voltage of first stage acts as the input voltage of second stage and so on. The voltage gain of the complete cascade amplifier is equal to the product of the voltage gains of the individual stages.

\* The voltage gain of first stage is  $A_{V1} = \frac{V_{o1}}{V_{i1}}$

\* The voltage gain of second stage is  $A_{V2} = \frac{V_{o2}}{V_{i2}}$

Similarly overall voltage gain  $A_V = \frac{V_o}{V_i}$

$$\frac{V_o}{V_i} = \frac{V_{o1}}{V_{i1}} \times \frac{V_{o2}}{V_{i2}} \dots \frac{V_{o(n-1)}}{V_{i(n-1)}} \times \frac{V_{on}}{V_{in}}$$

$$A_V = A_{V1} \times A_{V2} \dots A_{V(n-1)} \times A_{Vn}$$

$$\phi = \phi_1 + \phi_2 + \dots \phi_{n-1} + \phi_n$$

\* From above expressions, we can conclude that,

(13)

i) The magnitude of the resultant voltage gain equals to the product of the magnitudes of the voltage gains of the individual stages.

ii) The phase shift of the resultant voltage gain equals to the sum of the phase shifts of the individual stages.

\* The following figure shows a particular stage say the  $k^{th}$  stage of the  $n$  stage cascaded amplifier.

→ The voltage gain of the  $k^{th}$  stage is given by,

$$A_{VK} = \frac{A_{IK} R_{LK}}{R_{IK}}$$

\* Where  $R_{LK}$  is the effective load impedance at the collector of the  $k^{th}$  stage and  $R_{IK}$  is the input impedance of the  $k^{th}$  stage.

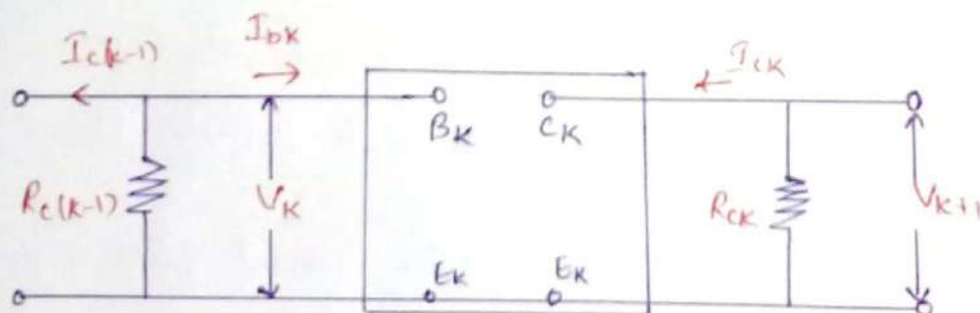


FIG:  $k^{th}$  STAGE OF A CASCADED AMPLIFIER

→ The Current gain  $A_{in} = \frac{-h_{fe}}{1 + h_{oe} R_{Ln}}$

$$R_{in} = h_{ie} + h_{fe} A_{in} R_{Ln}$$

Where  $R_{Ln}$  is the effective load impedance for the last stage and equals  $R_{Cn}$ .



$$\text{where } R_{L(n-1)} = \frac{R_{C(n-1)} * R_{in}}{R_{C(n-1)} + R_{in}}$$

(14)

$$A_{I(n-1)} = \frac{-h_{fe}}{1 + h_{oe} R_{L(n-1)}}$$

$R_{i(n-1)}$  can be found from  $R_{i(n-1)} = h_{ie} + h_{re} A_{I(n-1)} R_{L(n-1)}$

### CURRENT GAIN:

$\Rightarrow A_I$  is the current gain of the Complete  $n$ -stage amplifier

$$A_I = \frac{I_o}{I_{b1}} = \frac{-I_{cn}}{I_{b1}}$$

$$\frac{-I_{cn}}{I_{b1}} = \frac{-I_{c1}}{I_{b1}} * \frac{I_{c2}}{I_{c1}} \dots \dots \frac{I_{cn}}{I_{c(n-1)}}$$

$$A_I = A_{I1} * A'_{I2} \dots \dots A'_{In}$$

where  $A_{I1}$  is the base to collector current gain

$A'_{I2}, A'_{I3} \dots$  are the collector to collector current gains

\* for  $k^{th}$  stage the collector to collector current gain is given by,

$$A'_{Ik} = \frac{I_{ck}}{I_{c(k-1)}} \rightarrow \text{①} \quad A_{Ik} = \frac{I_{ck}}{I_{bk}}$$

$$A'_{Ik} = \frac{I_{ck}}{I_{bk}} * \frac{I_{c(k-1)}}{I_{c(k-1)}}$$

multiply and divide eq ① by  $I_{bk}$

$$A_{JK}' = \frac{I_{CK}}{I_{C(K-1)}} \times \frac{I_{BK}}{I_{BK}}$$

$$= \frac{I_{CK}}{I_{BK}} \times \frac{I_{BK}}{I_{C(K-1)}}$$

$$A_{JK}' = A_{JK} \times \frac{I_{BK}}{I_{C(K-1)}}$$

From the figure  $I_{BK} = \frac{R_{C(K-1)}}{R_{C(K-1)} + R_{inK}} \times I_{C(K-1)}$

$$\frac{I_{BK}}{I_{C(K-1)}} = \frac{R_{C(K-1)}}{R_{C(K-1)} + R_{inK}}$$

$$A_{JK}' = A_{JK} \times \frac{R_{C(K-1)}}{R_{C(K-1)} + R_{inK}}$$

Power gain :  $A_p = \frac{V_o}{V_i} \times \frac{I_o}{I_{b1}} = \frac{-V_o}{V_i} \frac{I_{cn}}{I_{b1}}$

$$A_p = A_v A_J$$

$$A_v = A_J \frac{R_{cn}}{R_{i1}}$$

$$A_p = A_J^2 \frac{R_{cn}}{R_{i1}}$$

## TWO STAGE RC COUPLED AMPLIFIER:

(16)

\* The two stage R-c coupled amplifier using Common Emitter Configuration is as shown.

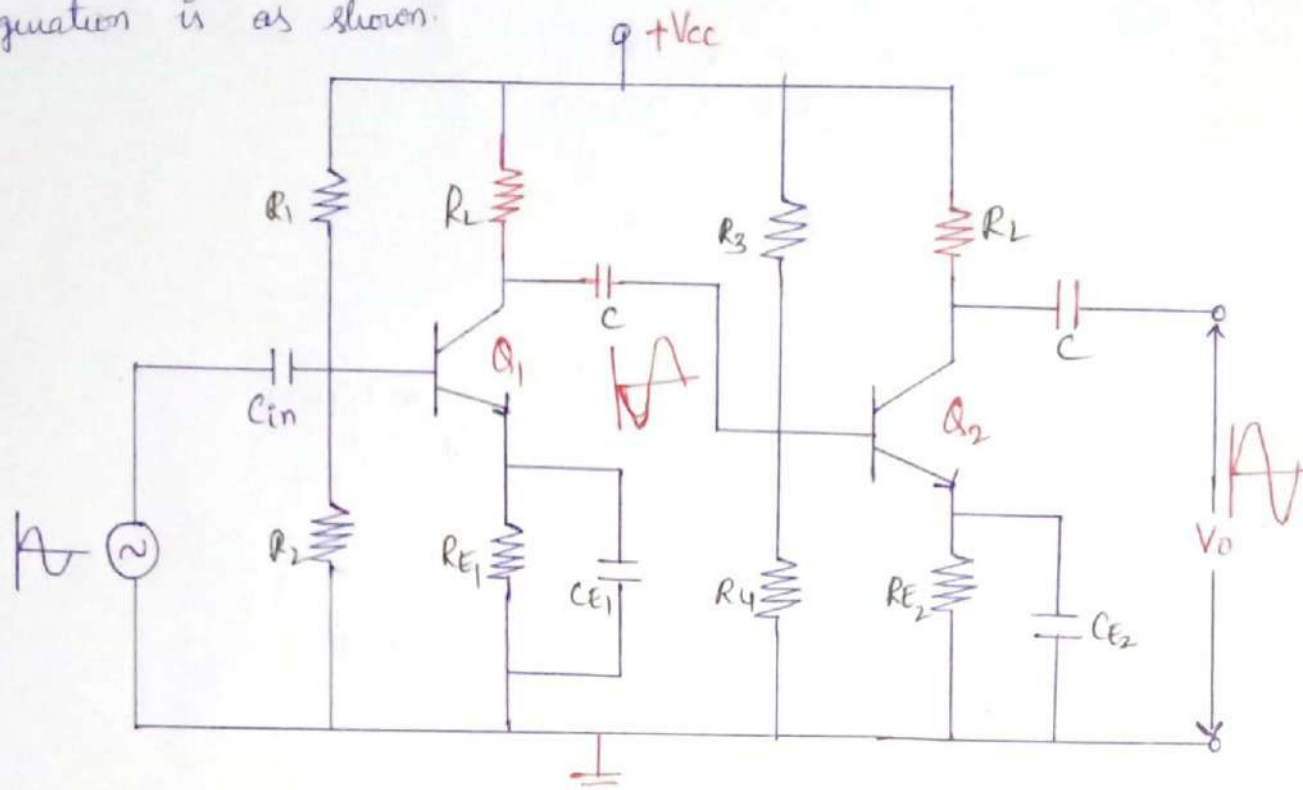


FIG: CIRCUIT DIAGRAM OF TWO STAGE RC COUPLED AMPLIFIER

\* The output of the first stage is coupled to the input of second stage through coupling capacitor  $C$  followed by a shunt connection of resistor. Therefore the amplifier is known as Resistance Capacitance Coupled Amplifier.

- \* The resistors  $R_1$ ,  $R_2$  and  $R_E$  form the biasing and stabilization network.
- \* The bypass capacitor  $C_E$  prevents the loss of amplification.
- \* The coupling capacitors are also known as Blocking capacitors which block DC and allow AC voltage.



### OPERATION:

- \* AC Signal is applied to the base of  $Q_1$  transistor, the amplified signal will develop across the collector of  $Q_1$  transistor.
- \* The amplified signal is connected to the base transistor through coupling capacitor 'C'.
- \* The 2nd stage can be used for further amplification of the input signal so the cascaded stages amplifies the signal and then the overall gain increases.
- \* The 1st stage output is out of phase with the input signal. This out of phase signal is fed to the i/p of the next stage.
- \* Therefore the overall output voltage of 2nd stage is in phase with the i/p signal with higher amplitude.

### FREQUENCY RESPONSE:

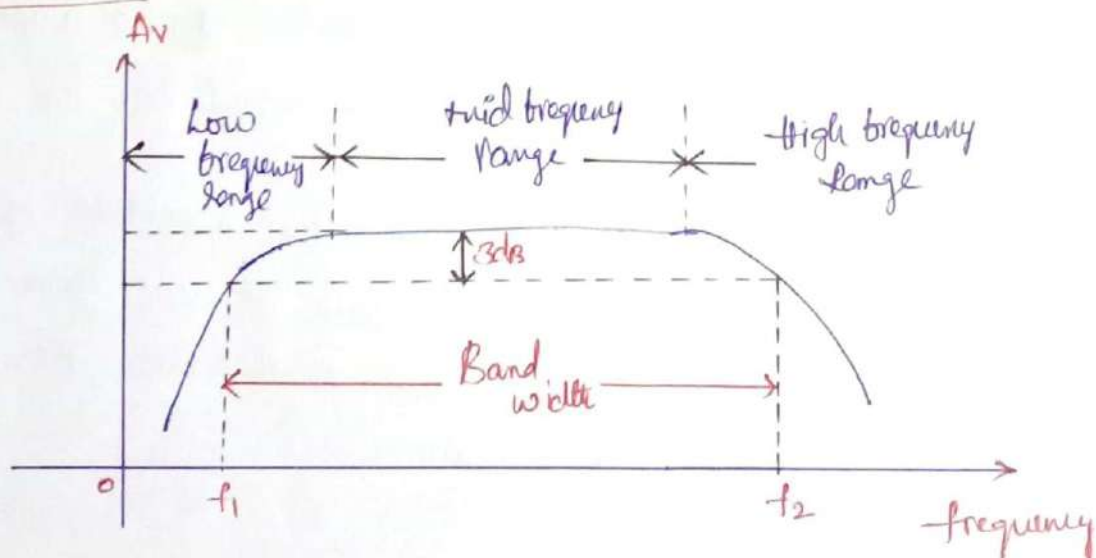


FIG: FREQUENCY RESPONSE OF TWO STAGE RC COUPLED AMPLIFIER

- \* It can be seen from the figure i.e., frequency response curve that the voltage gain is maximum and constant at mid frequencies.
- \* The voltage gain is low at lower and higher frequencies.

AT LOW FREQUENCIES:

\* At low frequencies, the reactance of the coupling capacitor will increase, so very small o/p voltage of the signal will be transfer from one stage to other stage. Due to large reactance, the capacitor  $C_c$  cannot parallel the emitter resistor  $R_E$ . Due to this 2 factors, the voltage gain will decrease at low frequencies.

AT HIGH FREQUENCIES:

\* At high frequencies, the reactance of coupling capacitor will be very small and it behaves as a short circuit but due to junction capacitance the output voltage will decrease at high frequency. So the gain can be reduced at high frequencies.

AT MID FREQUENCIES:

\* At mid frequencies, the voltage gain of the amplifier is constant. The effect of coupling capacitor to maintain the uniform voltage gain at mid frequencies.

\* As frequency increases in this range, the reactance of the capacitor decreases which tends to increase the gain at the same time.

\* The above two factors almost cancel each other resulting a uniform voltage gain at mid frequencies.

ADVANTAGES:

- It provides a good frequency response
- It is less expensive
- It is small in size
- Low complexity



DISADVANTAGES: It provides poor impedance matching.

(19)

APPLICATIONS:

1. Radio Receivers
2. Tape Recorders
3. TV Receivers

### ANALYSIS:

\* In the analysis of RC coupled amplifier, the following simplified assumptions are made.

- 1)  $h_{ie}$  is so small that the voltage source  $h_{ie}V_o$  can be neglected.
  - 2)  $V_{hoe}$  is so large that it can be considered as an open circuit.
  - 3) The reactance  $X_C$  for any given input frequency is so small that the parallel combination of  $R_2$  and  $X_C$  can be effectively considered as a short circuit.
  - 4) The bias resistors  $R_1$  and  $R_2$  are usually large as compared to  $h_{ie}$ .
- ⇒ With these assumptions, the simplified circuit is as follows,

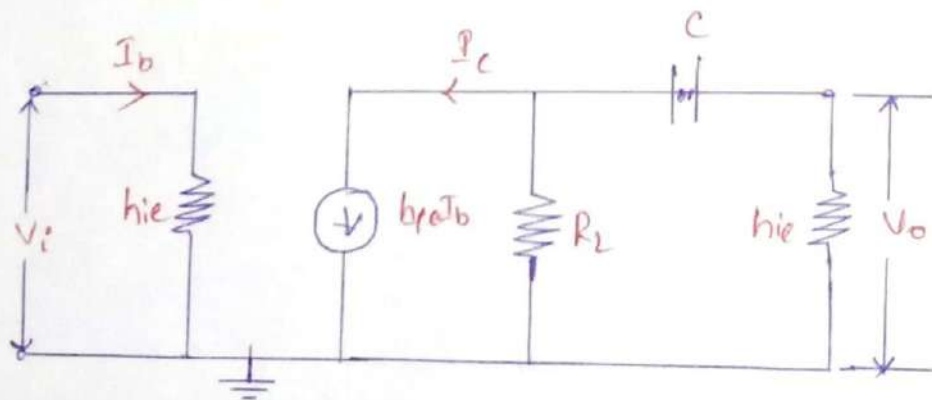


FIG: SIMPLIFIED EQUIVALENT CIRCUIT OF RC COUPLED AMPLIFIER

### MIDDLE FREQUENCY RANGE:

\* At mid frequencies, the impedance offered by coupling capacitor 'C' is so small, acts as an effective short circuit.



\* Hence at mid frequencies, the effect of coupling capacitor 'C' can be neglected. (20)

\* The thevenin's equivalent circuit is as follows.

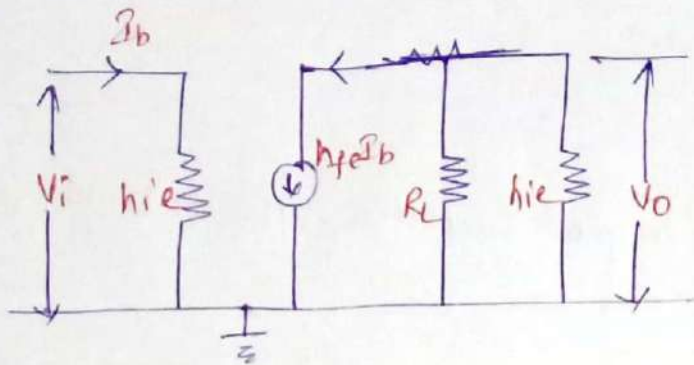


FIG: EQUIVALENT CIRCUIT AT MID FREQUENCY RANGE

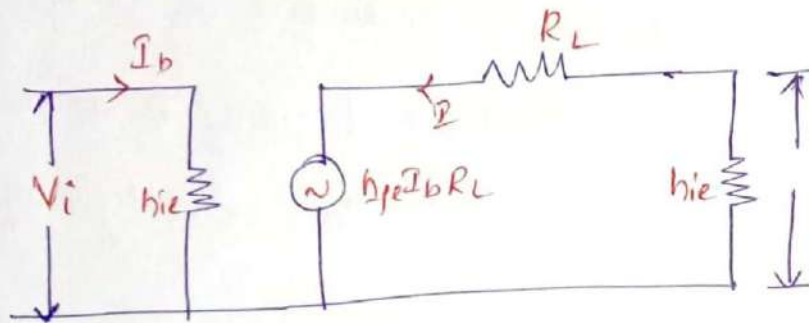


FIG: THEVENIN'S EQUIVALENT CIRCUIT

CURRENT GAIN:

By definition, Current gain  $(A_I)_m = \frac{I}{I_b} \rightarrow \textcircled{1}$

from the above figure  $I = \frac{-h_{fe} I_b R_L}{R_L + h_{ie}} \rightarrow \textcircled{2}$

Sub eq  $\textcircled{2}$  in eq  $\textcircled{1}$

$$(A_I)_m = \frac{-h_{fe} I_b R_L}{R_L + h_{ie}} \times \frac{1}{I_b}$$

$$A_{Im} = \frac{-h_{fe} R_L}{R_L + h_{ie}}$$

The magnitude of Current gain is given by  $|A_{Im}| = \frac{h_{fe} R_L}{R_L + h_{ie}}$

## VOLTAGE GAIN:

(21)

By definition  $A_{vm} = \frac{V_o}{V_i} \rightarrow \textcircled{1}$

The output voltage  $V_o = h_{fe} I$

$$V_o = h_{fe} \times \frac{-h_{fe} I_b R_L}{R_L + h_{ie}}$$

$$V_o = \frac{-h_{ie} h_{fe} I_b R_L}{R_L + h_{ie}} \rightarrow \textcircled{2}$$

The Input voltage  $V_i = h_{ie} I_b \rightarrow \textcircled{3}$

Sub eq  $\textcircled{2}$  & eq  $\textcircled{3}$  in eq  $\textcircled{1}$

$$A_{vm} = \frac{-h_{ie} h_{fe} I_b R_L}{R_L + h_{ie}} \times \frac{1}{h_{ie} I_b}$$

$$A_{vm} = \frac{-h_{fe} R_L}{R_L + h_{ie}}$$

\* The magnitude of voltage gain at mid-frequency is given by,

$$|A_{vm}| = \frac{h_{fe} R_L}{R_L + h_{ie}}$$

\* At mid frequency, the magnitude of current gain & voltage gain are equal i.e.,  $|A_{im}| = |A_{vm}|$

The negative sign shows the phase angle of  $180^\circ$ .

## LOW FREQUENCY RANGE:

(22)

\* In low frequency range the impedance offered by coupling capacitor is large. Hence it largely affects current amplification so it is included in the equivalent circuit

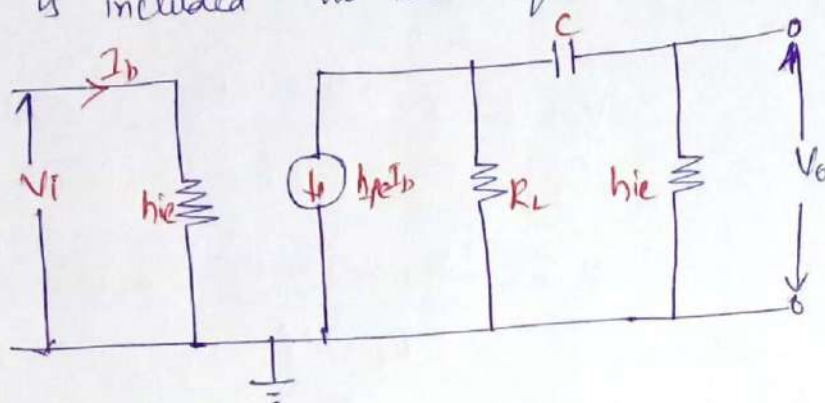


FIG: EQUIVALENT CIRCUIT

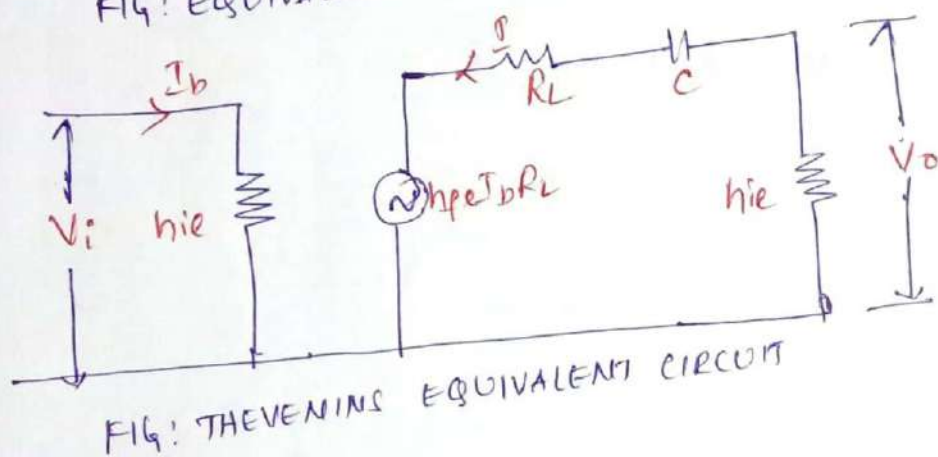


FIG: THEVENIN'S EQUIVALENT CIRCUIT

## CURRENT GAIN:

By definition, Current gain  $A_{IL} = \frac{I}{I_b}$

from the circuit 
$$I = \frac{-h_{fe} I_b R_L}{h_{ie} + R_L + \frac{1}{j\omega C}} = \frac{-h_{fe} I_b R_L}{h_{ie} + R_L - \frac{j}{\omega C}}$$

Current gain 
$$A_{IL} = \frac{-h_{fe} I_b R_L}{h_{ie} + R_L - \frac{j}{\omega C}} \times \frac{1}{I_b}$$

$$A_{IL} = \frac{-h_{fe} R_L}{h_{ie} + R_L - \frac{j}{\omega C}}$$

magnitude of current gain  $|A_{IL}| = \frac{h_{fe} R_L}{\sqrt{(R_L + h_{ie})^2 + \left(\frac{1}{\omega C}\right)^2}} \because \omega = 2\pi f$



### VOLTAGE GAIN:

(23)

By definition,  $A_{VL} = \frac{V_o}{V_i}$

Output Voltage  $V_o = h_{ie} \times I$

$$V_o = h_{ie} \times \frac{-h_{fe} I_b R_L}{R_L + h_{ie} - \frac{j}{\omega C}}$$

$$V_o = \frac{-h_{ie} h_{fe} I_b R_L}{R_L + h_{ie} - \frac{j}{\omega C}}$$

Input Voltage  $V_i = h_{ie} \times I_b$

$$A_{VL} = \frac{-h_{ie} h_{fe} I_b R_L}{R_L + h_{ie} - \frac{j}{\omega C}} \times \frac{1}{h_{ie} I_b}$$

$$A_{VL} = \frac{-h_{fe} R_L}{R_L + h_{ie} - \frac{j}{\omega C}} = \frac{-h_{fe} R_L}{R_L + h_{ie} - \frac{j}{2\pi f C}} \quad (\because \omega = 2\pi f)$$

\* Magnitude of Voltage gain is  $|A_{VL}| = \frac{h_{fe} R_L}{\sqrt{(R_L + h_{ie})^2 + \left(\frac{1}{2\pi f C}\right)^2}}$

\* From the above expression it is obvious that as frequency increases of the i/p Voltage ~~increases~~, the magnitude of the Voltage gain decreases and vice versa.

### HIGH FREQUENCY RANGE:

- \* In high frequency range, the reactance offered by Coupling Capacitor 'C' is very small and hence it can be considered as short circuited.
- \* In a bipolar transistor, there are two depletion regions across two P-N junctions. They behave like a dielectric media and hence give rise to two internal capacitances. At high frequency range the reactance of these capacitances ( $C_{be}$  &  $C_{bc}$ ) are considered.

- \* The  $C_{be}$  and  $C_{bc}$  may be replaced with a single Capacitance  $C_d$  across the i/p resistance  $h_{ie}$  of the transistor. (24)
- \* The values of short Capacitance  $C_d$  in the input circuit of the first stage is small because it depends on the o/p impedance of the first transistor.

\* But in the o/p circuit of the first stage  $C_d$  is increased by stray Capacitance of the wiring. Therefore the reactance  $1/\omega C_d$  will have appreciable shunting effect on  $R_L$  &  $h_{ie}$ .

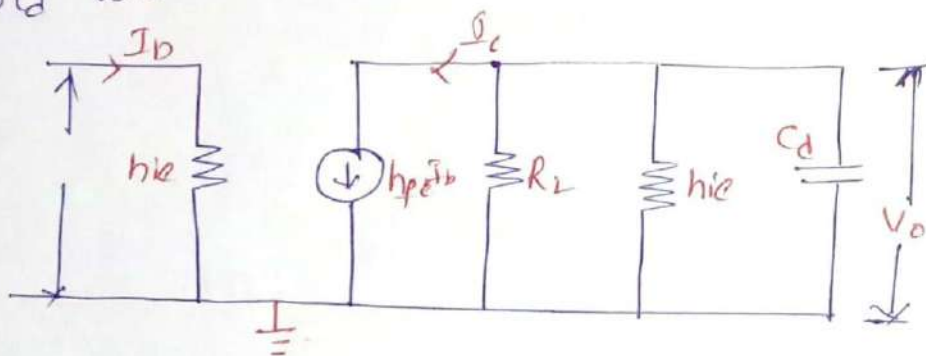


FIG: EQUIVALENT CIRCUIT AT HIGH FREQUENCIES

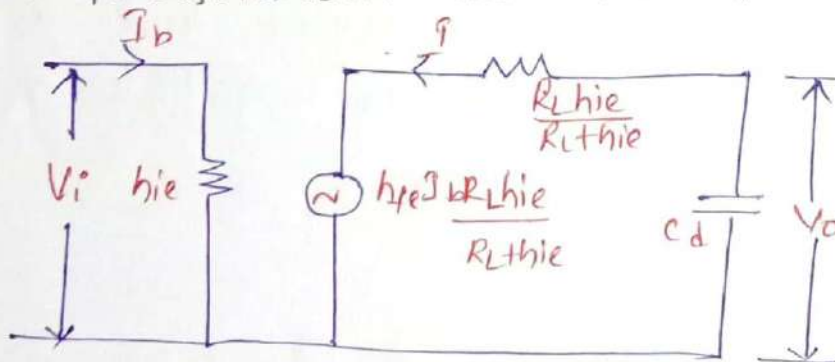


FIG: THEVENIN'S EQUIVALENT CIRCUIT

### CURRENT GAIN:

\* By definition  $A_{IH} = \frac{I}{I_b}$

from the above figure 
$$I = \frac{-h_{fe} I_b \frac{R_L h_{ie}}{R_L + h_{ie}}}{\frac{R_L h_{ie}}{R_L + h_{ie}} + \frac{1}{j\omega C_d}}$$

$$I = \frac{-h_{fe} I_b R_L h_{ie} / (R_L + h_{ie})}{(R_L + h_{ie}) \left[ R_L h_{ie} + \frac{1}{j\omega C_d} (R_L + h_{ie}) \right]}$$

$$I = \frac{-h_{fe} I_b R_L h_{ie}}{R_L h_{ie} + \frac{1}{j\omega C_d} (R_L + h_{ie})}$$

Current gain  $A_{IH} = \frac{-h_{fe} I_b R_L h_{ie}}{R_L h_{ie} + \frac{1}{j\omega C_d} (R_L + h_{ie})} \times \frac{1}{I_b}$

$$A_{IH} = \frac{-h_{fe} h_{ie} R_L}{R_L h_{ie} + \frac{1}{j\omega C_d} (R_L + h_{ie})}$$

### VOLTAGE GAIN:

By definition, Voltage gain  $A_{VH} = \frac{V_o}{V_i}$

output Voltage  $V_o = \frac{I}{j\omega C_d} \times I$

$$V_o = \frac{1}{j\omega C_d} \times \frac{-h_{fe} h_{ie} R_L I_b}{R_L h_{ie} + \frac{1}{j\omega C_d} (R_L + h_{ie})}$$

$$= \frac{1}{j\omega C_d} \times \frac{-h_{ie} h_{fe} R_L I_b}{R_L h_{ie} j\omega C_d + (R_L + h_{ie})}$$

$$V_o = \frac{-h_{ie} h_{fe} I_b R_L}{j\omega R_L h_{ie} C_d + (R_L + h_{ie})}$$

Input Voltage  $V_i = I_b \times h_{ie}$

$$A_{VH} = \frac{-h_{fe} h_{ie} I_b R_L}{(R_L + h_{ie}) + j\omega C_d R_L h_{ie}} \times \frac{1}{I_b \times h_{ie}}$$



$$A_{vH} = \frac{-h_{fe} R_L}{(R_L + h_{ie}) + j\omega C_d R_L h_{ie}} \quad (26)$$

The magnitude of voltage gain is given by,

$$|A_{vH}| = \frac{h_{fe} R_L}{\sqrt{(R_L + h_{ie})^2 + (\omega C_d R_L h_{ie})^2}} = \frac{h_{fe} R_L}{\sqrt{(R_L + h_{ie})^2 + (2\pi f C_d R_L h_{ie})^2}}$$

\* From above expression it is obvious that as the frequency 'f' of i/p voltage increases, the magnitude of voltage gain decreases.

### LOWER CUT-OFF FREQUENCY (A):

\* The lower cut-off frequency is defined as the frequency at which the magnitude of the voltage gain in the lower frequency range falls to  $1/\sqrt{2}$  or 0.707 of the magnitude of the gain in mid frequency range. Thus,

$$|A_{vL}| = \frac{|A_{vm}|}{\sqrt{2}} \quad \& \quad \frac{|A_{vL}|}{|A_{vm}|} = \frac{1}{\sqrt{2}} \rightarrow (1)$$

In R-C coupled amplifier

$$|A_{vL}| = \frac{h_{fe} R_L}{\sqrt{(h_{ie} + R_L)^2 + \left(\frac{1}{2\pi f C}\right)^2}} = \frac{h_{fe} R_L}{\sqrt{(h_{ie} + R_L)^2 \left[1 + \frac{1}{(2\pi f C (h_{ie} + R_L))^2}\right]}}$$

$$= \frac{h_{fe} R_L}{(h_{ie} + R_L) \sqrt{1 + \frac{1}{(2\pi f C (h_{ie} + R_L))^2}}}$$

$$= \frac{h_{fe} R_L}{h_{ie} + R_L} \times \frac{1}{\sqrt{1 + \frac{1}{(2\pi f C (h_{ie} + R_L))^2}}}$$

$$(\because |A_{vm}| = \frac{h_{fe} R_L}{h_{ie} + R_L})$$

$$= |A_{vm}| \times \frac{1}{\sqrt{1 + \frac{1}{(2\pi f C (h_{ie} + R_L))^2}}}$$

At  $f_1$

$$\frac{|A_{VL}|}{|A_{Vm}|} = \frac{1}{\sqrt{1 + \left[ \frac{1}{2\pi f_1 C(h_i + R_L)} \right]^2}} \rightarrow (2)$$

(27)

Equating (1) & (2) and At  $f_1$  be the lower cut-off frequency

$$\frac{1}{\sqrt{2}} = \frac{1}{\sqrt{1 + \left[ \frac{1}{2\pi f_1 C(h_i + R_L)} \right]^2}}$$

$$f_1 = \sqrt{1 + \left[ \frac{1}{2\pi f_1 C(h_i + R_L)} \right]^2}$$

$$2 = 1 + \left[ \frac{1}{2\pi f_1 C(h_i + R_L)} \right]^2$$

$$\left[ \frac{1}{2\pi f_1 C(h_i + R_L)} \right]^2 = 1$$

$$\frac{1}{2\pi f_1 C(h_i + R_L)} = 1$$

$$2\pi f_1 C(h_i + R_L) = 1$$

$$f_1 = \frac{1}{2\pi C(h_i + R_L)} \rightarrow (3)$$

Sub eq (3) in eq (2), we get

$$\boxed{|A_{VL}| = \frac{|A_{Vm}|}{\sqrt{1 + \left( \frac{f_1}{f} \right)^2}}}$$

## UPPER CUT-OFF FREQUENCY ( $f_2$ ):

(28)

\* The upper cut-off frequency is defined as the frequency at which the magnitude of the voltage gain in the high frequency range falls to  $1/\sqrt{2}$  or 0.707 of magnitude of the gain in the mid frequency range. Thus at  $f = f_2$

$$|A_{vH}| = \frac{|A_{vm}|}{\sqrt{2}} \Rightarrow \frac{|A_{vH}|}{|A_{vm}|} = \frac{1}{\sqrt{2}} \rightarrow (1)$$

$$\text{Here } |A_{vH}| = \frac{h_{fe} R_L}{\sqrt{(h_{ie} + R_L)^2 + (2\pi f C_d R_L h_{ie})^2}}$$
$$= \frac{h_{fe} R_L}{\sqrt{(h_{ie} + R_L)^2 \left[ 1 + \left[ \frac{1}{2\pi f C_d R_L h_{ie} (h_{ie} + R_L)} \right]^2 \right]}}$$

$$= \frac{h_{fe} R_L}{(h_{ie} + R_L) \sqrt{1 + \left[ \frac{1}{2\pi f C_d (h_{ie} + R_L) h_{ie}} \right]^2}}$$

$$= \frac{|A_{vm}|}{\sqrt{1 + \left[ \frac{1}{2\pi f C_d R_L h_{ie} (h_{ie} + R_L)} \right]^2}} \quad (\because A_{vm} = \frac{h_{fe} R_L}{h_{ie} + R_L})$$

$$\frac{|A_{vH}|}{|A_{vm}|} = \frac{1}{\sqrt{1 + \left[ \frac{1}{2\pi f C_d R_L h_{ie} (h_{ie} + R_L)} \right]^2}} \rightarrow (2)$$

Equating (1) & (2) and  $f_2$  is the upper cut-off frequency

$$\frac{1}{\sqrt{2}} = \frac{1}{\sqrt{1 + \left[ \frac{1}{2\pi f_2 C_d R_L h_{ie} (R_L + h_{ie})} \right]^2}}$$

$$f_2 = \sqrt{1 + \left[ \frac{1}{2\pi f_2 C_d R_L h_{ie} (h_{ie} + R_L)} \right]^2}$$



$$Q = \left[ 1 + \frac{1}{2\pi f_2 C_d R_L h_{ie} (h_{ie} + R_L)} \right]^2 \quad (29)$$

$$\left[ \frac{1}{2\pi f_2 C_d R_L h_{ie} (h_{ie} + R_L)} \right]^2 = 1$$

$$2\pi f_2 C_d R_L h_{ie} (h_{ie} + R_L) = 1$$

$$f_2 = \frac{1}{2\pi C_d R_L h_{ie} (h_{ie} + R_L)} \rightarrow (3)$$

Sub eq (3) in eq (2)

$$|A_{vH}| = \frac{A_{vm}}{\sqrt{1 + \left(\frac{f}{f_2}\right)^2}}$$

$$\phi_H = \tan^{-1} \left( \frac{f}{f_2} \right)$$

### DARLINGTON PAIR AMPLIFIER:

- \* A very popular connection of two bipolar junction transistors for operation as one "superbeta" transistor is the Darlington Connection.
- \* The Composite transistor acts as a single unit with a current gain that is the product of the current gains of the individual transistors.

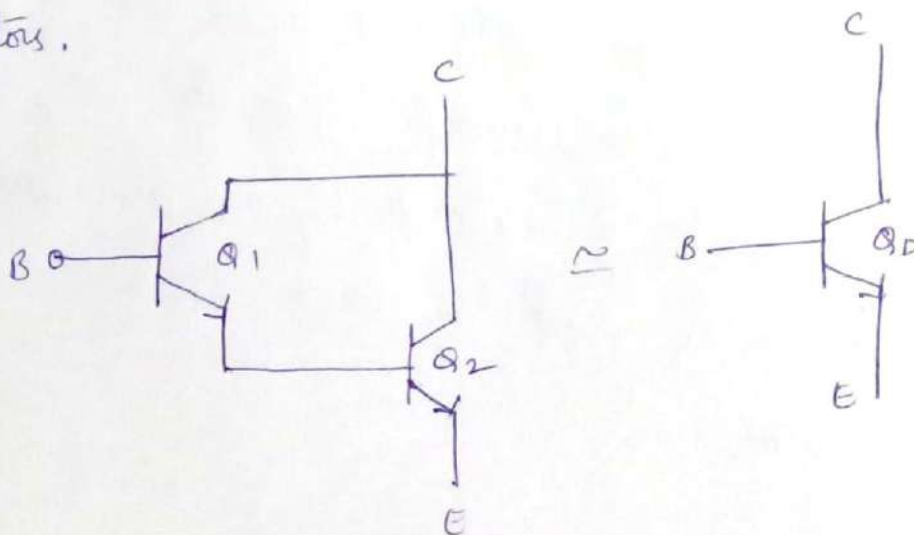


FIG: DARLINGTON TRANSISTOR CONNECTION AND SINGLE DARLINGTON TRANSISTOR.

\* If the two transistors are matched such that  $\beta_1 = \beta_2 = \beta$ , (80)  
the Darlington Connection provides a current gain of

$$\beta_D = \beta^2$$

\* When two transistors having high current gain are connected as a Darlington pair, the overall gain of the pair becomes very high.

### DC ANALYSIS:

\* A Darlington transistor offers very high current gain,  $\beta_D$  is used.

The base current is given by,  $I_B = \frac{V_{CC} - V_{BE}}{R_B + \beta_D R_E}$

The Emitter current is given by,  $I_E = (\beta_D + 1) I_B$   
 $= \beta_D I_B$

The dc voltages are given by,  $V_E = I_E R_E$

$$V_B = V_E + V_{BE}$$

### AC ANALYSIS:

\* The AC input signal is applied to the base of the Darlington transistor through capacitor  $C_1$ , with the ac output  $V_o$  obtained from the emitter through capacitor  $C_2$ .

\* The Darlington transistor is replaced by an ac equivalent circuit comprised of an input resistance  $r_i$  and an output current source  $\beta_D I_B$ .

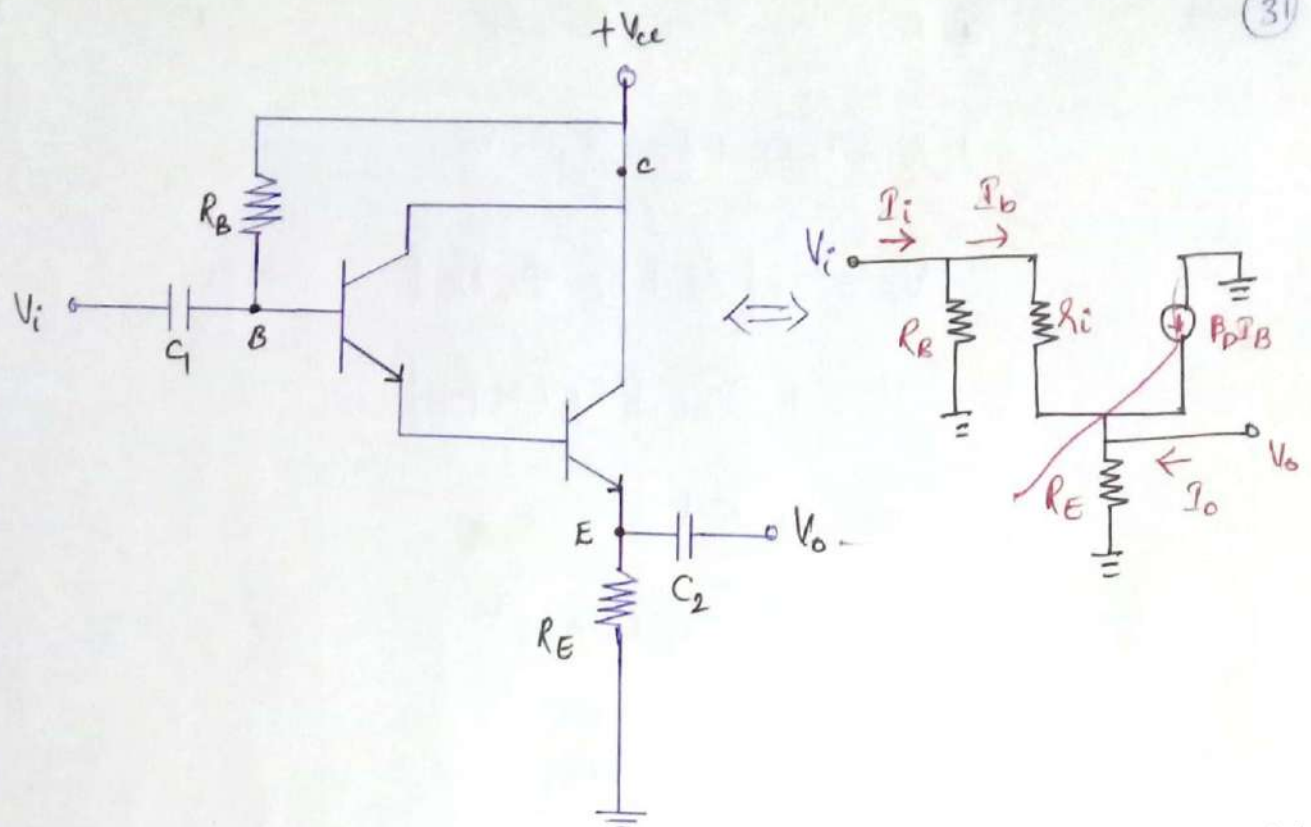
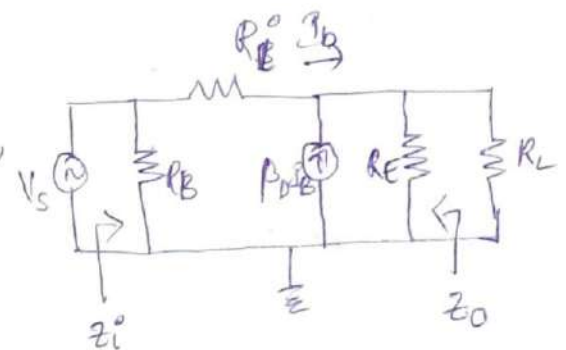


FIG: AC EQUIVALENT CIRCUIT OF DARLINGTON Emitter FOLLOWER

### AC INPUT IMPEDANCE:

\* The ac base current through  $R_i$  is,

$$I_b = \frac{V_i - V_o}{R_i} \rightarrow (1)$$



But output voltage is given by,

$$V_o = (I_b + \beta_D I_b) R_E \rightarrow (2)$$

∴ AC Input Impedance  $Z_i = \frac{V_i}{I_b}$

Substituting eq (2) in eq (1)

$$I_b = \frac{V_i - (I_b R_E + \beta_D I_b R_E)}{R_i}$$

$$I_b = \frac{V_i - I_b R_E - \beta_D I_b R_E}{R_i}$$



$$I_b R_i = V_i - I_b R_E - \beta_D I_b R_E$$

$$I_b R_i + I_b R_E + \beta_D I_b R_E = V_i$$

$$V_i = I_b [R_i + R_E + \beta_D R_E]$$

$$V_i = I_b [R_i + R_E (1 + \beta_D)]$$

Since  $\beta_D \gg 1$

$$V_i = I_b (R_i + \beta_D R_E)$$

$$\frac{V_i}{I_b} = R_i + \beta_D R_E$$

The ac impedance, looking into the circuit is

$$Z_i = R_B \parallel (R_i + \beta_D R_E)$$

AC CURRENT GAIN:

\* The ac output current through  $R_E$  is given as,

$$I_o = I_b + \beta_D I_b = (1 + \beta_D) I_b$$

$$\approx \beta_D I_b \quad (\because \beta_D \gg 1)$$

\* The transistor current is then

$$\frac{I_o}{I_b} = \beta_D$$

\* The ac current gain of the circuit is,

$$A_i = \frac{I_o}{I_i} \rightarrow \text{①}$$

Multiply numerator and denominator of eq① with  $I_b$ .

$$A_i = \frac{I_o}{I_i} \times \frac{I_b}{I_b} = \frac{I_o}{I_b} \times \frac{I_b}{I_i} \rightarrow (2)$$

Using the current divider rule in the ac equivalent circuit

$$I_b = I_i \times \frac{R_B}{(R_i + \beta_D R_E) + R_B}$$

$$\frac{I_b}{I_i} = \frac{R_B}{(R_i + \beta_D R_E) + R_B} \rightarrow (3)$$

Substituting eq (3) in eq (2)

$$A_i = \frac{I_o}{I_b} \times \frac{R_B}{(R_i + \beta_D R_E) + R_B}$$

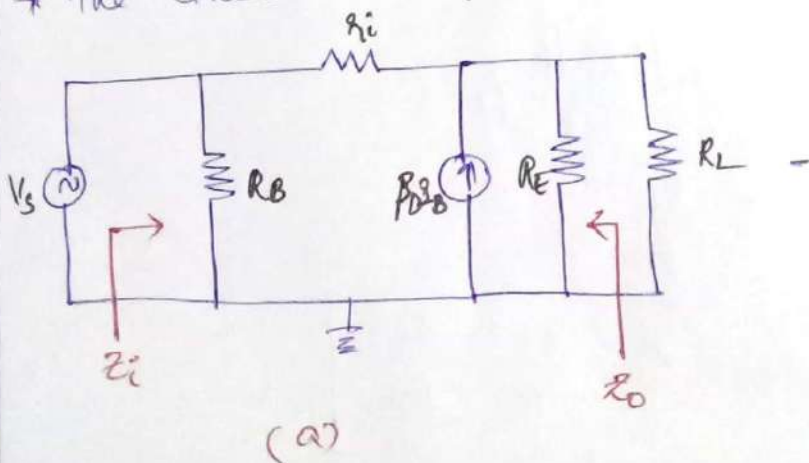
$$= \beta_D \times \frac{R_B}{(R_i + \beta_D R_E) + R_B} \quad (\because \beta_D = \frac{I_o}{I_b})$$

$$= \beta_D \times \frac{R_B}{\beta_D R_E + R_B}$$

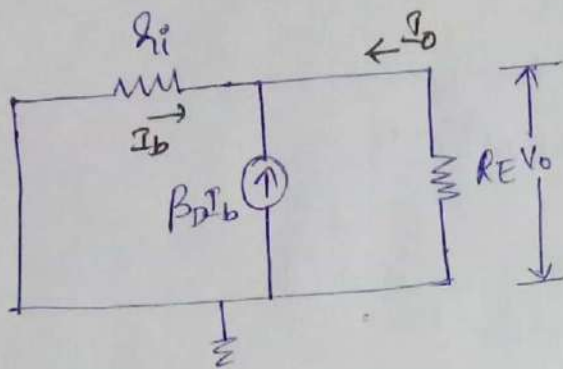
$\therefore$  The current gain is given by  $A_i = \frac{\beta_D R_B}{\beta_D R_E + R_B}$

### AC OUTPUT IMPEDANCE :

\* The circuit ~~to~~ output impedance is as shown below



\* The output impedance can be calculated by making  $V_s = 0$  then the equivalent circuit will be



from the above circuit,  $I_o = \frac{V_o}{R_E}$  Apply KCL then,

$$I_o = \frac{V_o}{R_E} + \frac{V_o}{r_i} - \beta_D I_b \quad \text{where } I_b = -\frac{V_o}{r_i}$$

$$= \frac{V_o}{R_E} + \frac{V_o}{r_i} - \beta_D \left( -\frac{V_o}{r_i} \right)$$

$$= \frac{V_o}{R_E} + \frac{V_o}{r_i} + \frac{V_o \beta_D}{r_i}$$

$$= V_o \left[ \frac{1}{R_E} + \frac{1}{r_i} + \frac{\beta_D}{r_i} \right]$$

$$\frac{V_o}{I_o} = \frac{1}{\frac{1}{R_E} + \frac{1}{r_i} + \frac{\beta_D}{r_i}}$$

$$= R_E \parallel r_i \parallel \frac{r_i}{\beta_D}$$

$$\approx \frac{r_i}{\beta_D}$$

$$\therefore \text{Output impedance } Z_o = \frac{V_o}{I_o} = \frac{r_i}{\beta_D}$$



## AC VOLTAGE GAIN:

(35)

\* The equivalent circuit to determine voltage gain

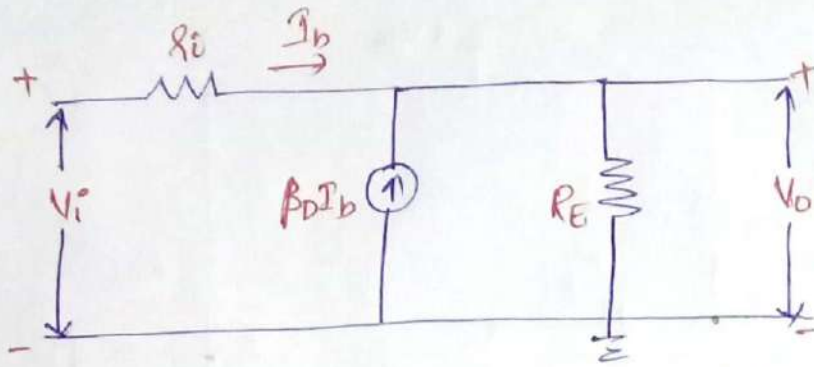


FIG: AC equivalent circuit to determine  $A_v$

From the circuit,  $V_o = (I_b + \beta_D I_b) R_E$

$$= I_b R_E + \beta_D I_b R_E$$

$$V_o = I_b (R_E + \beta_D R_E)$$

$$V_i = I_b R_i + (I_b + \beta_D I_b) R_E$$

$$V_i = I_b [R_i + R_E + \beta_D R_E]$$

$$\therefore \text{Voltage gain } A_v = \frac{V_o}{V_i}$$

$$= \frac{I_b (R_E + \beta_D R_E)}{I_b (R_i + R_E + \beta_D R_E)}$$

$$= \frac{R_E + \beta_D R_E}{R_i + R_E + \beta_D R_E}$$

$$(\because R_i \ll R_E + \beta_D R_E)$$

$$= \frac{R_E + \cancel{\beta_D R_E}}{R_E + \cancel{\beta_D R_E}} = 1$$

$$\boxed{A_v = 1}$$

The circuit diagram of emitter follower is shown in fig. 1.

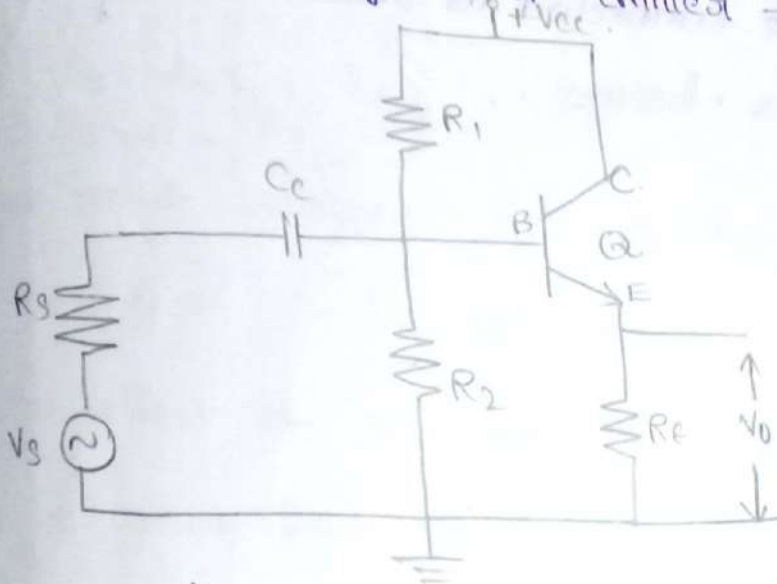


fig. 1: emitter follower (common collector amplifier).

Circuit description:

$V_{cc} \rightarrow$  Biasing voltage.

$R_1$  &  $R_2 \rightarrow$  Biasing resistors.

$R_e \rightarrow$  emitter resistor.

$R_3 \rightarrow$  source resistor

$V_S \rightarrow$  Source voltage

$Q \rightarrow$  npn transistor

$B, E, C \rightarrow$  Base, emitter & collector

$V_i \& V_o \rightarrow$  input & output voltage

(3) The input resistance of common collector amplifier (or) emitter follower is high generally.

i.e.  $R_i = h_{ie} + h_{fe} A_i R_E \Omega$

$$\approx h_{ie} + 1 \times A_i R_E$$

$$\therefore h_{ie} = h_{ie}$$

$$R_i = h_{ie} + A_i R_E$$

$$\& h_{fe} \approx 1$$

But current gain ( $A_i$ )  $\approx h_{fe} = 1 + h_{fe}$

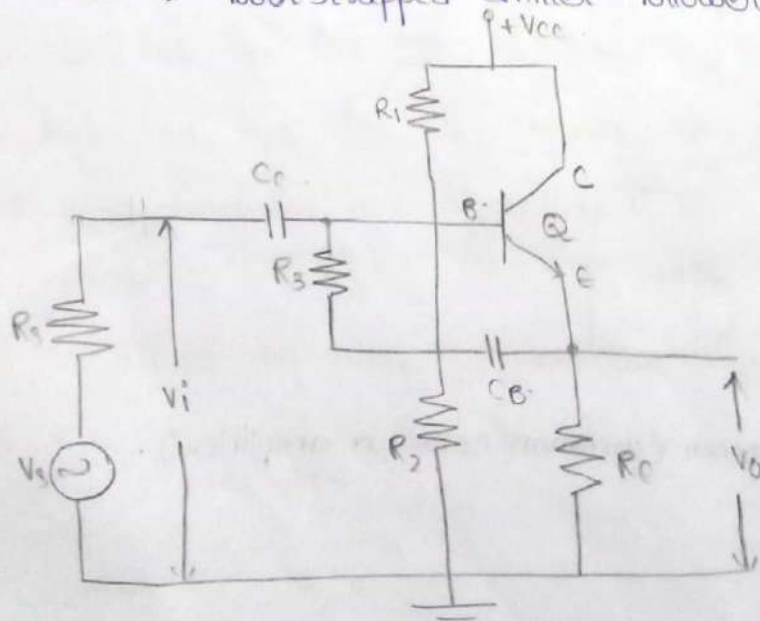
$$R_i = h_{ie} + (1 + h_{fe}) R_E$$

But practically biasing resistors  $R_1 \& R_2$  reduces the input resistance as  $R_i' = R_i \parallel R_b \Omega$  where

$$R_b = R_1 \parallel R_2 \Omega$$

$$\approx R_b \Omega$$

To overcome the above disadvantage i.e. decrease of input resistance due to biasing resistors for emitter follower and additional resistor  $R_3$  and capacitor  $C_B$  are connected as shown in fig(b) which is called as Bootstrapped emitter follower.



Fig(b): Boot ~~stopped~~ emitter follower.  
strapped.

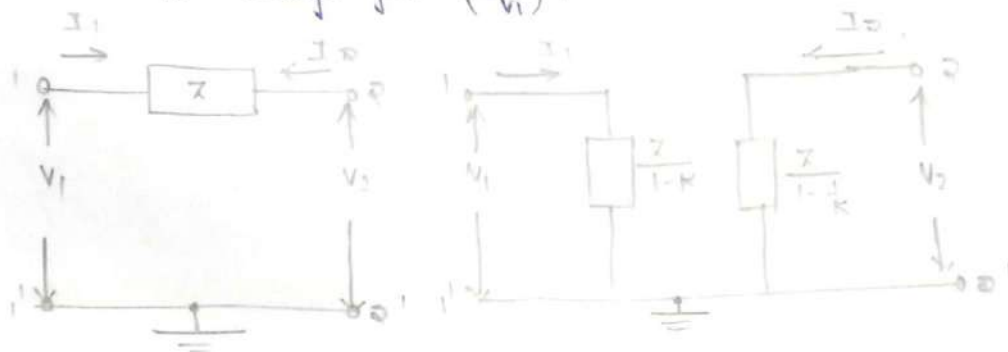


The top of  $R_3$  is connected to Base (B) (input) and the bottom of  $R_3$  is connected to emitter (E) (output) via capacitor  $C_B$ .

The value of capacitor  $C_B$  is selected such that it acts as short circuit for low frequency of operation.

Millers theorem states that if there is an impedance between two nodes can be replaced by two impedance  $\frac{Z}{1-K}$  &  $\frac{Z}{1-\frac{1}{K}}$  between input node and ground & output node and ground, where

$$K = \text{voltage gain } \left(\frac{V_2}{V_1}\right).$$



the voltage gain of  $C_C$  amplifier is  $A_v$  which is approximately unity i.e.  $A_v \approx 1$ .

$R_3$  is connected b/w input & output terminals.

Now, by millers theorem,

$$\text{effective input resistance } R_{in} = \frac{R_3}{1-A_v}$$

As  $A_v \approx 1$ ,  $R_{eff}$  is very high. Effective =  $R_{eff}$

$$\text{for ex: } \frac{R_3}{1-0.99}$$

$$\text{Now input resistance } R_i' = R_i \parallel R_{eff} \approx R_i$$

$$\text{i.e. } R_i' \approx R_i$$

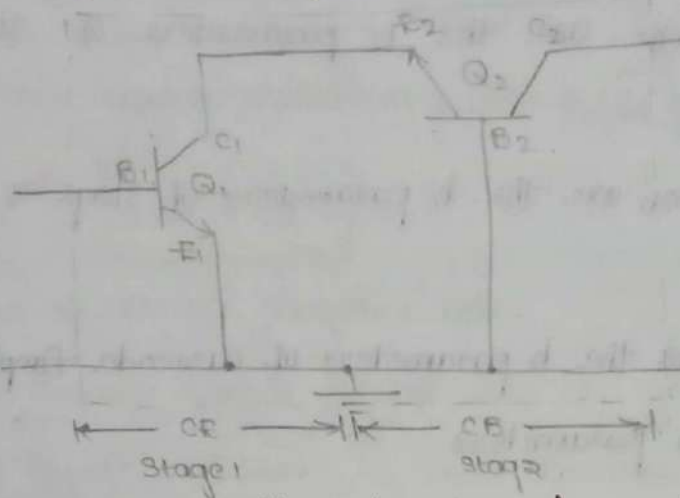
⇒ The effect of increasing the i/p resistance, when  $A_v$  approaches unity is called Boot strapping

⇒ The above term arises from the fact that  $A_v = \frac{V_o}{V_i} \approx 1$   
 $V_o \approx V_i$

⇒ It means if one end of the resistor  $R_3$  changes in voltage  $V_i$  then another end of  $R_3$  moves through the same change in voltage ( $V_o$ ).

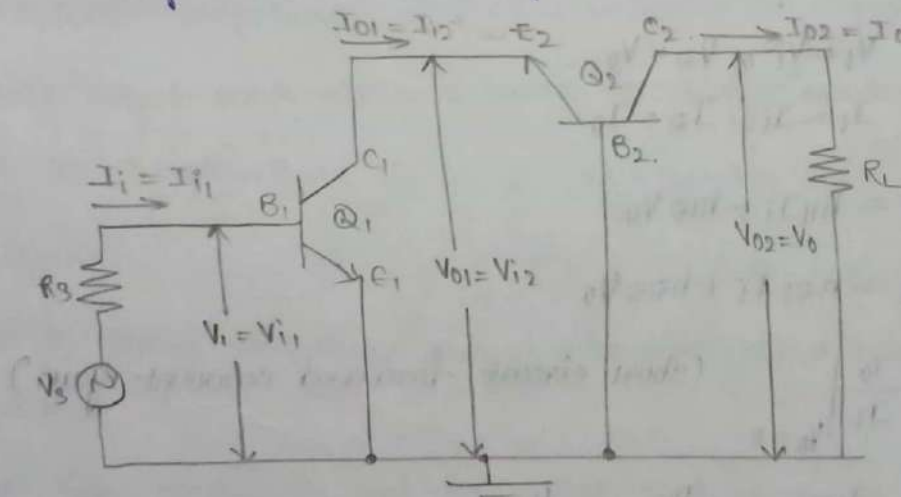
## Cascode Amplifier:

The circuit diagram of CE-CB configuration (cascode config) is shown in fig(a). In which it is observed that ~~whose~~ output of stage 1 ( $E_1$ ) given to stage 2 <sup>input</sup> ( $E_2$ )



fig(a): Cascode configuration (CE followed by CB).

Circuit diagram of cascode amplifier is shown in fig(b).



fig(b): cascode Amplifier.

### Circuit Description:

$Q_1$  &  $Q_2 \rightarrow$  npn transistors.

$E_1$  &  $E_2$ ,  $C_1$  &  $C_2$ ,  $B_1$  &  $B_2 \rightarrow$  emitter terminals, collector terminals, Base terminals of  $Q_1$  &  $Q_2$  respectively.

$R_L$ ,  $R_s$ ,  $V_s \rightarrow$  load resistance, source resistance, source voltage.

$V_{01}$ ,  $V_{02}$ ,  $V_0 \rightarrow$  output voltages of CE, CB & cascode.

$V_{i1}$ ,  $V_{i2}$ ,  $V_i \rightarrow$  input voltages of CE, CB & cascode.

from fig  $V_i = V_{i1}$ ,  $I_i = I_{i1}$ .

$$V_{o1} = V_{i2}, I_{o1} = I_{i2}$$

$$V_{o2} = V_o, I_{o2} = I_o$$

Analysis:

$\Rightarrow$  let  $h_{fe}, h_{oe}, h_{ie}, h_{oe}$  are the h parameters of stage 1

CE Amplifier

$\Rightarrow$  let  $h_{fb}, h_{ob}, h_{ib}, h_{ob}$  are the h parameters of stage 2 CB

Amplifier.

$\Rightarrow$   $h_{e1}, h_{e2}, h_{i1}, h_{i2}$  are the h parameters of cascode Amplifier.

By definition of h parameters

$$V_i = h_{i1} I_i + h_{i2} V_o$$

$$I_o = h_{o1} I_i + h_{o2} V_o$$

from fig:  $V_i = V_{i1}, V_o = V_o$

$$I_i = I_{i1}, I_o = I_{o1}$$

then  $V_i = h_{i1} I_i + h_{i2} V_o$

$$I_o = h_{o1} I_i + h_{o2} V_o$$

$$h_{o1} = \left. \frac{I_o}{I_i} \right|_{V_o=0} \quad (\text{short circuit forward current gain})$$

$$\begin{aligned} &\approx \frac{I_{o2}}{I_{i1}} = \frac{I_{o2}}{I_{i1}} \times \frac{I_{o1}}{I_{i1}} \\ &= h_{fb} \cdot h_{fe} \end{aligned}$$

$$\boxed{h_{o1} \approx h_{fe}} \quad (\because h_{fb} \approx 1)$$

Short circuit  $\frac{\text{input resistance}}{\text{output conductance}}$

$$h_{e2} = h_{i1} = \left. \frac{V_i}{I_i} \right|_{V_o=0} \quad (\text{or}) \approx h_{ie} \quad (\text{stage is CE})$$

$$\boxed{h_{i1} = h_{ie}}$$



Open circuit reverse voltage gain  $h_{12} = \left. \frac{V_i}{V_o} \right|_{I_i = 0}$

$$= \frac{V_{i1}}{V_{o2}} = \frac{V_{i1}}{V_{o1}} \times \frac{V_{o1}}{V_{o2}}$$

$$h_{12} = h_{oe} \cdot h_{ab}$$

Open circuit output conductance  $h_{22} = \left. \frac{I_o}{V_o} \right|_{I_i = 0}^{(V)} = h_{ob}$  (since stage 2 is CB)

$$h_{22} = h_{ob}$$

Advantages of cascode Amplifier are:

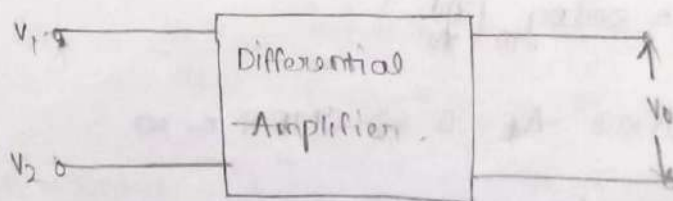
- (1) Overall current gain is current gain of single CE Amplifier
- (2) Overall input resistance is input resistance of single CE Amplifier
- (3) Overall reverse voltage gain is product of reverse voltage gain of both CB & CE Amplifiers.
- (4) Overall output conductance is same as output conductance of single CB Amplifier.

Applications:

- (1) Used in tuned Amplifier designed because of reduction in internal feedback
- (2) Has high bandwidth and less noise used as small signal amplifier
- (3) Used in RF Applications as video Amplifier.

Differential Amplifier:

Block diagram of a differential Amplifier is shown in fig (a),



fig(a): Block diagram.

An Amplifier which Amplifies the difference between two input Voltages is called as differential Amplifier.

Voltage gain of differential amplifier is

$$A_d = \frac{V_o}{V_1 - V_2} = \frac{V_o}{V_d}$$

Where,  $V_1, V_2 \rightarrow$  two input voltages

$V_o \rightarrow$  output voltage.

$V_1 - V_2 \rightarrow$  difference voltage.

$$V_o = A_d (V_1 - V_2).$$

For an ideal differential amplifier  $V_1 = V_2$ .

$$V_o = 0V.$$

But in a practical amplifier the output voltage also depends upon average signal (or) common mode signal.

$$\text{i.e. } V_c = \frac{V_1 + V_2}{2}$$

$$\text{Common mode voltage gain } A_c = \frac{V_o}{V_c} = \frac{V_o}{\left(\frac{V_1 + V_2}{2}\right)}$$

$$V_o = A_c V_c = A_c \left(\frac{V_1 + V_2}{2}\right)$$

Total output voltage  $V_o = A_d V_d + A_c V_c$ .

$$= A_d (V_1 - V_2) + A_c \left(\frac{V_1 + V_2}{2}\right)$$

CMRR (common mode rejection ratio) is defined as ratio of differential voltage gain ( $A_d$ ) to the common mode voltage gain ( $A_c$ ).

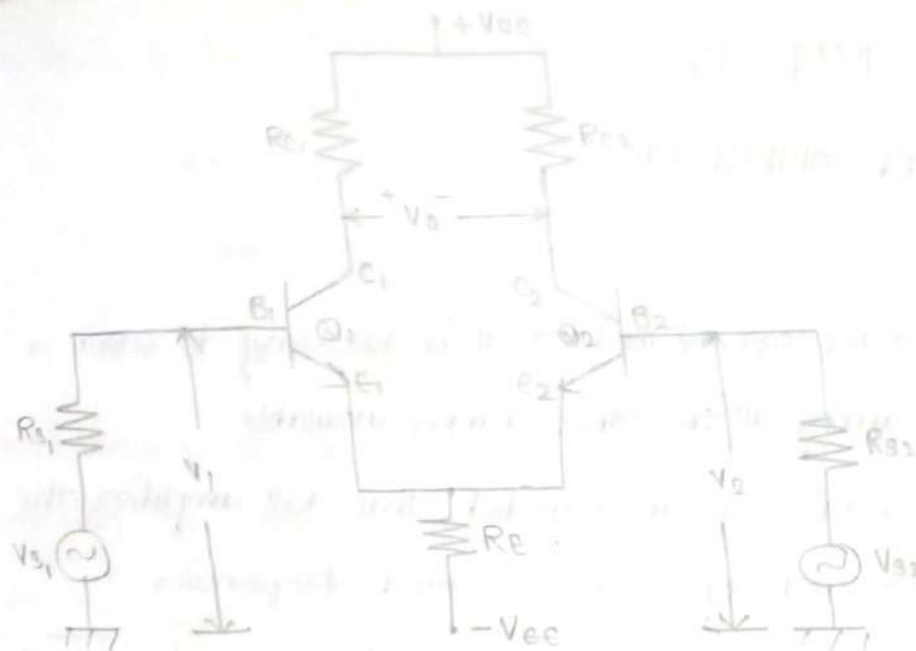
$$\text{CMRR} = \frac{A_d}{A_c}$$

$$\text{CMRR (dB)} = 20 \log_{10} \left( \frac{A_d}{A_c} \right)$$

For an ideal amplifier  $A_c = 0 \Rightarrow \text{CMRR} = \infty$

But in practical cases  $A_c \ll A_d$ .

The circuit diagram of differential amplifier by using BJT is shown in fig(b).



Fig(6): Emitter coupled differential Amplifier.

Circuit description:

$-V_{ee}, +V_{cc} \rightarrow$  positive biasing voltage.

$Q_1, Q_2 \rightarrow$  npn transistors.

$V_1 \& V_2 \rightarrow$  input voltages.

$R_E \rightarrow$  Emitter resistance.

$B_1, B_2, C_1, C_2, E_1, E_2 \rightarrow$  Base terminals, collector terminals,  
— Emitter terminals.

$R_{c1} \& R_{c2} \rightarrow$  collector resistors.

$R_{s1} \& R_{s2} \rightarrow$  source resistors.

$V_{s1} \& V_{s2} \rightarrow$  voltage sources.

Applications:

Used as basic input stage for Opamp (operation amplifier).

to provide balanced output.

$$-A_d = \frac{V_o}{V_d} = -\frac{h_{fe} R_L}{R_s + h_{ie}}$$

$$-A_c = \frac{V_o}{V_e} = -\frac{h_{fe} R_L}{R_s + h_{ie} + (1 + h_{fe}) 2R_E}$$

$$CMRR = 20 \log_{10} \left[ \frac{-A_d}{-A_c} \right] = 20 \log_{10} \left[ \frac{R_s + h_{ie} + (1 + h_{fe}) 2R_E}{R_s + h_{ie}} \right]$$



## ANALYSIS OF MULTISTAGE AMPLIFIER USING FET:

\* The circuit diagram for multistage amplifier using FET.

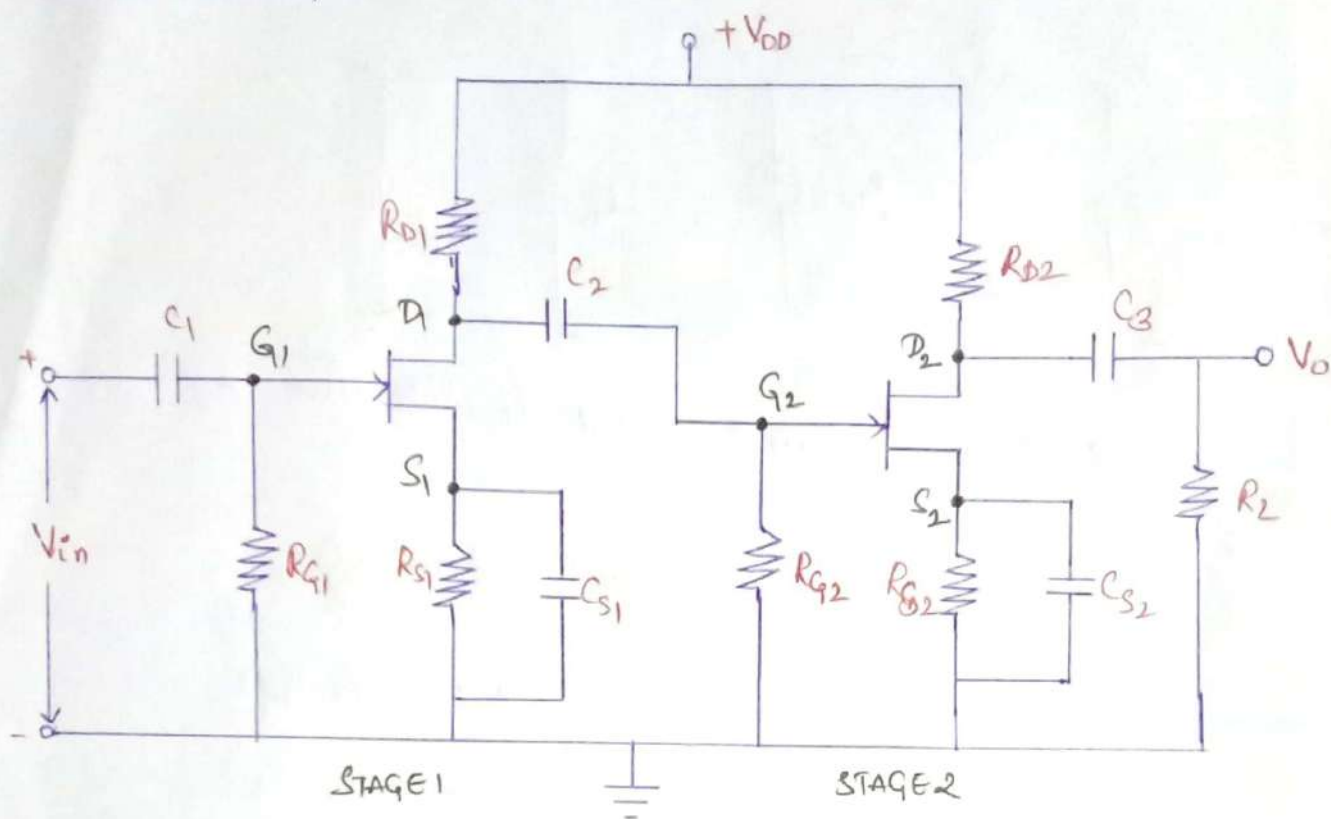


FIG: MULTISTAGE FET AMPLIFIER

\* The Overall gain of the multistage FET amplifier is given by the products of individual gains

$$\text{i.e., } A_v = A_{v1} * A_{v2}$$

$$= (-g_{m1} R_{D1}) * (-g_{m2} R_{D2})$$

$$= g_{m1} R_{D1} g_{m2} R_{D2}$$

\* The Input Impedance  $Z_{in} = R_{G1}$

\* The output impedance  $Z_{out} = R_{D2}$

\* The main function of cascading stages is to achieve the large overall Voltage gain.



# SMALL SIGNAL HIGH FREQUENCY TRANSISTOR AMPLIFIER MODELS

## Introduction:

- Hence a model was introduced in 1969 by L.J. Gia Colletto called as hybrid-IT model (because of its shape) (or) Gia Colletto model (because of the inventor)

Hand-drawn circuit diagram of a BJT common-emitter amplifier model. The circuit includes a base-emitter junction with voltage  $V_{be}$ , a base resistor  $R_{bb}$ , a base-emitter capacitance  $C_{be}$ , a base-emitter conductance  $g_{m'b'e}$ , a collector resistor  $R_{ce}$ , a collector-emitter capacitance  $C_{ce}$ , and a collector-emitter conductance  $g_{m'c'e}$ . The collector current is labeled  $i_c$ . The diagram is annotated with various handwritten notes and values.

Hybrid TV model of CG and Travel for roy below

where  $i$  = emitter, collector

$B, E, C \rightarrow$  Base, emitter, collector  
 $B' \rightarrow$  physically inaccessible virtual base

$\beta$   $\rightarrow$  Physically  $\beta$  is virtual  $\beta$

$r_{bb'}$   $\rightarrow$  Resistance b/n actual base and virtual base (or) i.e., bulk resistance of base spreading resistance

$r_{be} \approx r_{be} + (1 + \beta) r_{e1} \rightarrow$  Resistance b/w virtual base and emitter



$r_{b'c}(or) r_{\pi} \rightarrow$  Resistance b/n virtual base and collector

$r_{ce} \rightarrow$  Resistance b/n collector and emitter

$C_e(or) C_{\pi} \rightarrow$  Diffusion capacitance b/n forward biased emitter base junction

$C_c(or) C_{\mu} \rightarrow$  Transition capacitance b/n reverse biased collector base junction

$g_m v_{b'e} \rightarrow$  Voltage dependent current source

$g_m \rightarrow$  transconductance (or) mutual conductance

$v_{b'e} \rightarrow$  Voltage b/n virtual base and emitter

$v_{ce} \rightarrow$  voltage b/n collector and emitter

$i_b \rightarrow$  base current

$i_c \rightarrow$  collector current

Assume that all parameters in this model are independent of frequency and are constant under given biased condition. Typical values of seven hybrid parameters at room temperature and  $I_c = 1.3 \text{ mA}$  are as follows

$$g_m = 50 \text{ mS}$$

$$r_{bb'} = 100 \Omega$$

$$r_{b'e}(or) r_{\pi} = 1 \text{ K}\Omega$$

$$r_{b'c}(or) r_{\mu} = 1 \text{ M}\Omega$$

$$r_{ce} = 80 \text{ K}\Omega$$

$$C_e(or) C_{\pi} = 100 \text{ pF}$$

$$C_c(or) C_{\mu} = 3 \text{ pF}$$

The advantage of hybrid  $\pi$  model is it is valid for low, medium and high frequencies but it is used only for high frequencies because of complexity in analysis it can't be used for low and medium frequencies.

$\text{h-parameter model (hybrid model)}$  is used only for low and medium frequencies as it fails at high frequencies.



Derivation of hybrid- $\pi$  parameter in terms of h-parameters:  
 consider H-parameter model of CE transistor as shown in figure (a).

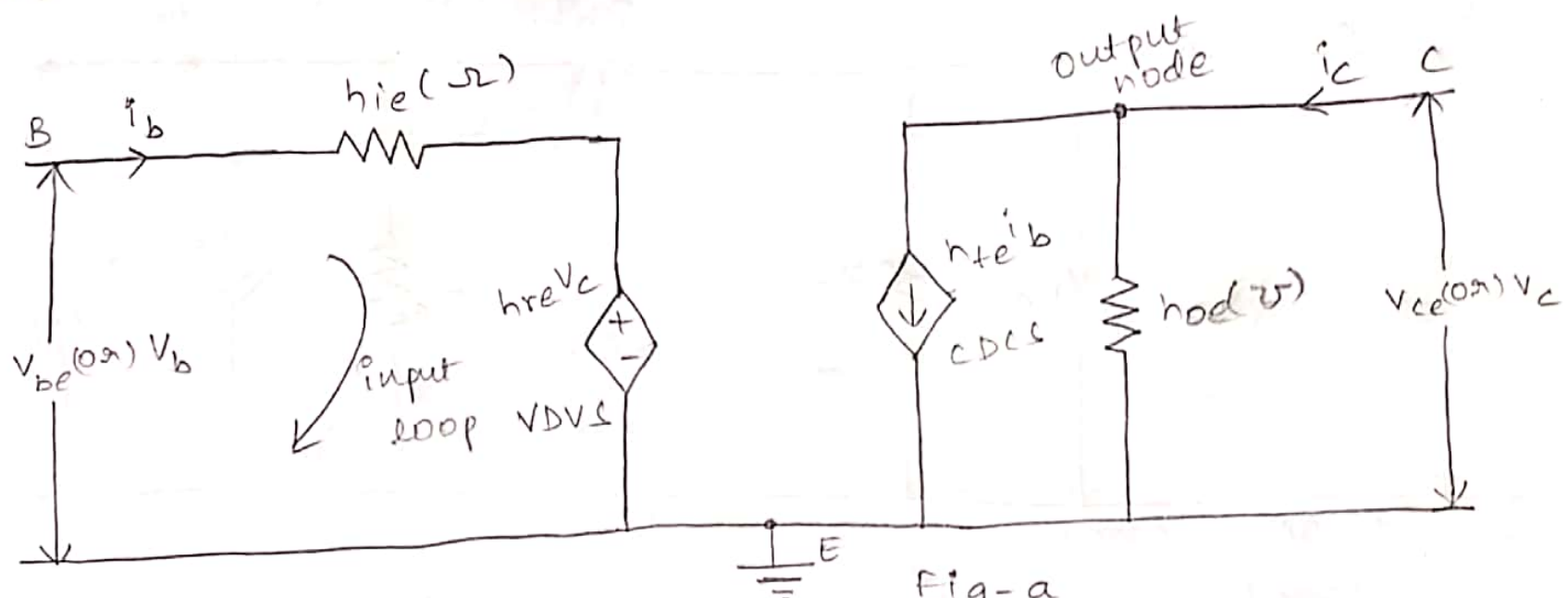


Fig-a

h-parameter model of CE transistor  
 By applying KVL to input loop & KCL to output node

$$V_b = h_{ie} i_b + h_{re} V_c$$

$$i_c = h_{fe} i_b + h_{oe} V_c$$

$$h_{ie} = \left. \frac{V_b}{i_b} \right|_{V_c=0} \quad \text{short circuit input impedance}$$

$$h_{fe} = \left. \frac{i_c}{i_b} \right|_{V_c=0} \quad \text{short circuit forward current gain}$$

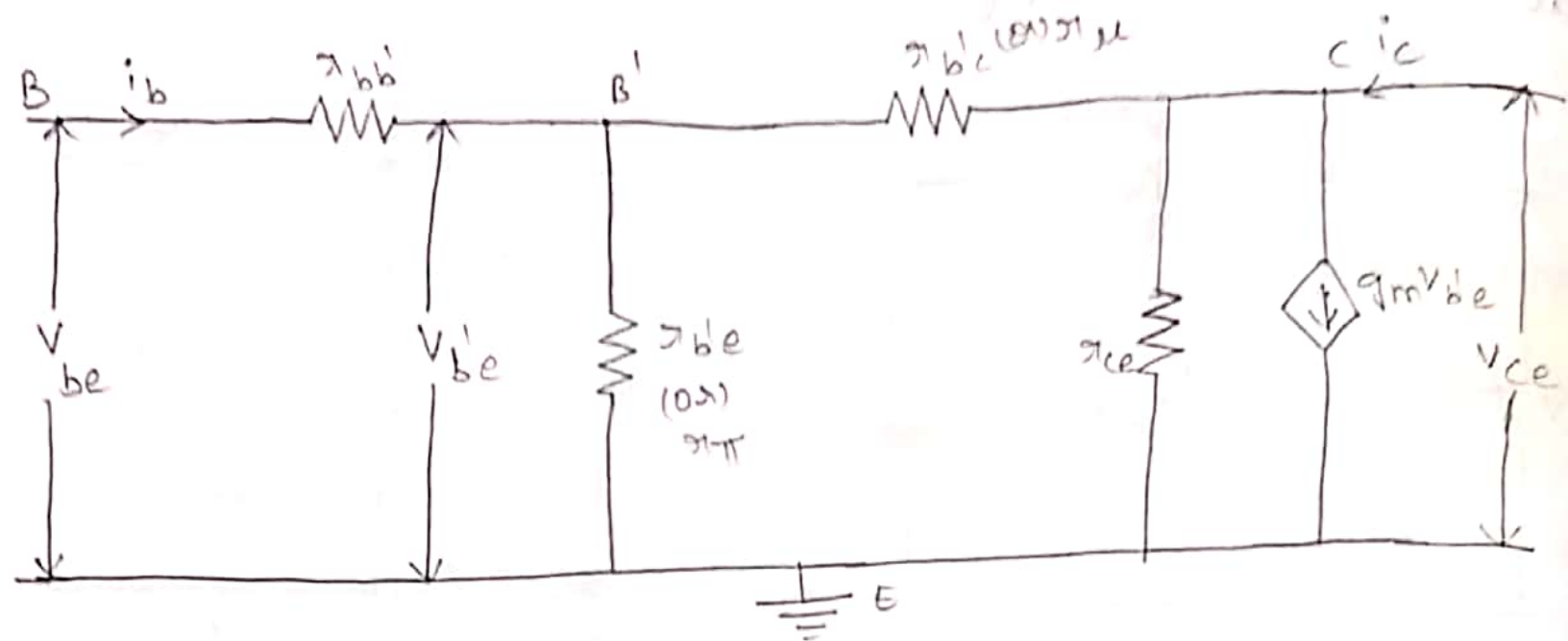
$$h_{re} = \left. \frac{V_b}{V_c} \right|_{i_b=0} \quad \text{open circuit reverse voltage gain}$$

$$h_{oe} = \left. \frac{i_c}{V_c} \right|_{i_b=0} \quad \text{open circuit output conductance}$$

As above all four parameters are defined differently, they are called as Hybrid parameters or h-parameters.

consider hybrid- $\pi$  model for CE transistor at low frequencies as shown in fig-b. It is observed at reactance of capacitor is inversely proportional to frequency  
 i.e.,  $X_C = \frac{1}{\omega C} = \frac{1}{2\pi f C}$

At low frequencies, as capacitive reactance is very high,  $C_E$  &  $C_C$  capacitors are treated as open circuit in fig-6



Hybrid- $\pi$  model of CE transistor at low frequency

Where seven hybrid- $\pi$  parameters are

$g_m \rightarrow$  Trans conductance

$r_{ce} \rightarrow$  Resistance between collector and emitter

$r_{b'c}$  (or)  $r_{\mu} \rightarrow$  Resistance b/n virtual base and collector

$r_{b'e}$  (or)  $r_{\pi} \rightarrow$  Resistance b/n virtual base and emitter

$V_{b'e} \rightarrow$  Voltage b/n virtual base and emitter

$r_{bb'}$   $\rightarrow$  Resistance b/n actual base and virtual base

$V_{be} \rightarrow$  Voltage b/n actual base and emitter.

$V_{ce} \rightarrow$  voltage b/n collector and emitter

$C_E$  (or)  $C_{\pi} \rightarrow$  Diffusion capacitance b/n forward biased emitter base junction

$C_C$  (or)  $C_{\mu} \rightarrow$  Transition capacitance b/n reverse biased collector base junction

Trans conductance (or) mutual conductance  $g_m (r)$ :

By definition

$$g_m = \left. \frac{\partial I_C}{\partial V_{B'E}} \right|_{V_{CE} = \text{constant}}$$



For npr transistor

$$I_C = \alpha I_E + I_{C0}$$

$$\frac{\partial I_C}{\partial V_{B'E}} = \alpha \frac{\partial I_E}{\partial V_{B'E}} + 0$$

$$\frac{\partial I_C}{\partial V_{B'E}} = \alpha \frac{\partial I_E}{\partial V_{B'E}}$$

$$\frac{\partial I_C}{\partial V_{B'E}} = \alpha \frac{\partial I_E}{\partial V_E}$$

$$= \frac{\alpha}{\left( \frac{\partial V_E}{\partial I_E} \right)}$$

=  $\frac{\alpha}{r_e}$  where  $r_e$  is dynamic resistance of forward biased emitter diode.

$$= \frac{\alpha I_E}{V_T}$$

$$\approx \frac{|I_C|}{V_T}$$

Where  $V_T = \frac{kT}{q} = \frac{T}{11600} = 26 \text{ mV (at room temperature)}$

$$g_m = \frac{|I_C|}{V_T} (\Omega)$$

It is observed at  $g_m$  is directly proportional to collector current ( $I_C$ ) and inversely proportional to thermal voltage ( $V_T$ ) and hence temperature ( $T$ ).

Resistance b/n virtual base ( $B'$ ) and emitter ( $E$ )  $r_{be}(\Omega)$ :  
Consider short circuit forward current gain

$$h_f = \left. \frac{I_C}{I_B} \right|_{V_C=0}$$

From fig-(b) with  $V_C=0$  most of the current  $I_B$  flows through  $r_{be}$  because  $r_{be}$  is short circuited and  $r_{be} \ll r_{bc}$ .



Now  $V_{be} = i_b r_{be}$

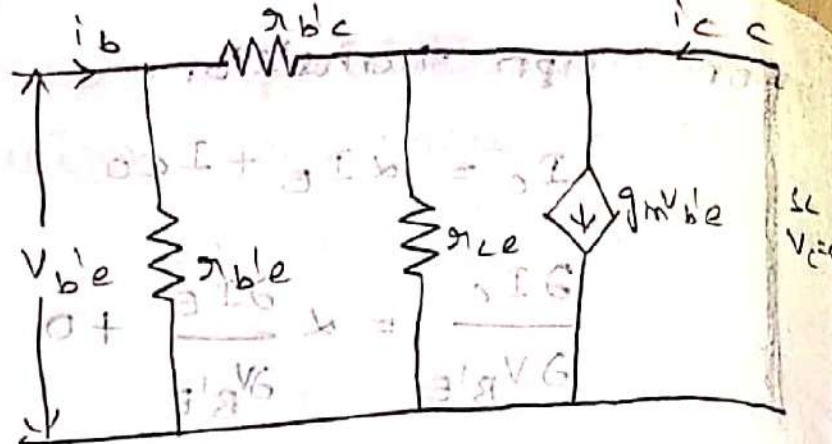
From Fig-(b) with short circuit  $i_c = g_m V_{be}$

$$= g_m i_b r_{be}$$

$$\frac{i_c}{i_b} = g_m r_{be}$$

$$h_{fe} = g_m r_{be}$$

$$r_{be} = \frac{h_{fe}}{g_m} \Omega$$



Resistance between virtual base and collector  $h_{re}$  consider open circuit reverse voltage gain

$$h_{re} = \frac{V_b}{V_c} \Big|_{i_b=0}$$

from Fig(b)  $i_b = 0$

$$V_{be} = \left[ \frac{V_c}{r_{be} + r_{bc}} \right] r_{be}$$

$$\frac{V_{be}}{V_c} = \frac{r_{be}}{r_{be} + r_{bc}} = \frac{r_{be}}{r_{be} + r_{bc}}$$

$$h_{re} = \frac{r_{be}}{r_{bc}}$$

$$r_{bc} = \frac{r_{be}}{h_{re}}$$

Resistance b/n actual base & virtual base:  $r_{bb'}$

Consider short circuit input impedances

$$h_{ie} = \left. \frac{V_b}{I_b} \right|_{V_c=0} \quad (\Omega)$$

with  $r_c = 0$ ,  $r_{b'e}$  &  $r_{b'c}$  are in parallel, since  $r_{ce}$

is short circuit

Hence

$$r_{b'e} \parallel r_{b'c} = \frac{r_{b'e} \cdot r_{b'c}}{r_{b'e} + r_{b'c}} = r_{b'e} \quad [r_{b'e} \ll r_{b'c}]$$

Now,

$$V_b = I_b (r_{bb'} + r_{b'e})$$

$$\frac{V_b}{I_b} = r_{bb'} + r_{b'e}$$

$$h_{ie} = r_{bb'} + r_{b'e}$$

$$r_{bb'} = h_{ie} - r_{b'e}$$

relation b/n collector and emitter:  $r_{ce}$

Consider open circuit output conductances

$$h_{oe} = \left. \frac{I_c}{V_c} \right|_{V_b=0}$$

from  $r_{ib}(b)$  By KCL

$$I_c = g_m V_{b'e} + \frac{V_c}{r_{ce}} + \frac{V_c}{r_{b'c}}$$

$$= g_m V_b + \frac{V_c}{r_{ce}} + \frac{V_c}{r_{b'c}}$$

$$I_c = g_m h_{re} \frac{V_c}{r_{ce}} + \frac{V_c}{r_{b'c}} \quad (\because h_{re} = \frac{V_b}{V_c} \text{ \& } r_{b'e} \ll r_{b'c})$$

$$I_c = V_c \left[ g_m h_{re} + \frac{1}{r_{ce}} + \frac{1}{r_{b'c}} \right]$$

$$\frac{I_c}{V_c} = g_m h_{re} + g_{ce} + g_{b'c}$$

$$h_{oe} = \frac{h_{fe}}{\beta} \times \frac{r_{b'e}}{r_{b'c}} + g_{ce} + g_{b'c}$$

$$h_{oe} = h_{fe} g_{b'c} + g_{ce} + g_{b'c}$$

$$h_{oe} = (1 + h_{fe}) g_{b'c} + g_{ce}$$

$$g_{ce} = h_{oe} - (1 + h_{fe}) g_{b'c} \quad (w)$$

$$r_{ce} = \frac{1}{g_{ce}} \quad \Omega$$



## Transition capacitance: ( $C_c$ )

It is measured b/n reverse biased collector base junction in  $C_b$  (common base) configuration and is given by manufacturer over the data sheet as  $C_{ob}$ . Therefore  $C_c(\omega) C_\mu = C_{ob}$

## Diffusion capacitance ( $C_e$ ):

It is measured b/n forward biased emitter base junction in  $C_b$  configuration and is given by  $C_e(\omega) C_\pi$

$$C_e(\omega) C_\pi = \frac{g_m}{2\pi f_T}$$

where  $f_T$  is the frequency at which short circuit current gain falls to unity, i.e.,  $h_{fe} = 1$

## Conclusions:

$$1. g_m = \frac{|I_C|}{V_T} (\Omega) \text{ where } V_T = \frac{T}{11600}$$

$$2. r_{b'e} = \frac{h_{fe}}{g_m} (\Omega)$$

$$3. r_{b'c} = \frac{r_{b'e}}{h_{fe}}$$

$$4. r_{b'b} = h_{ie} - r_{b'e}$$

$$5. r_{ce} = \frac{1}{g_{ce}}$$

$$6. C_c(\omega) C_\mu = C_{ob}$$

$$7. C_e(\omega) C_\pi = \frac{g_m}{2\pi f_T}$$



\* The following low frequency parameters for given transistor  $I_c = 5\text{mA}$ ,  $V_{CE} = 10\text{V}$  at room temperature,  $h_{ie} = 600\Omega$ ,  $h_{fe} = 100$ ,  $h_{re} = 10^{-4}$ ,  $h_{oe} = 20\mu\text{S}$  at the same operating point  $f_T = 500\text{MHz}$   $C_{ob} = 3\text{pF}$  calculating values of Hybrid- $\pi$  parameters.

Given data

collector current ( $I_c$ ) = 5mA

Voltage b/n collector and emitter ( $V_{CE}$ ) = 10V

$h_{ie}$  = short circuit input impedance = 600 $\Omega$

Short circuit forward current gain ( $h_{fe}$ ) = 100

Open circuit reverse voltage gain ( $h_{re}$ ) =  $10^{-4}$

Open circuit output conductance ( $h_{oe}$ ) = 20 $\mu\text{S}$

Frequency at room temperature ( $f_T$ ) = 500MHz

Transition capacitance ( $C_{ob}$ ) = 3pF

$$g_m = \frac{|I_c|}{V_T}$$

$$V_T = \frac{T}{11600}$$

$$= \frac{5 \times 10^{-3}}{26 \times 10^{-3}} = 0.1923 \text{ S} = 192 \text{ mS}$$

$$r_{b'e} = \frac{h_{fe}}{g_m}$$

$$= \frac{100}{0.1923} = 520\Omega$$

$$r_{b'c} = \frac{r_{b'e}}{h_{re}}$$

$$= \frac{520}{10^{-4}} = 5.2 \text{ M}\Omega$$

$$r_{bb} = \frac{h_{ie} - r_{b'e}}{1} = 600 - 520 = 80\Omega$$

$$r_{bb} = h_{ie} - r_{b'e} (\Omega)$$

$$= 600 - 520 = 80\Omega$$



$$r_{ce} = \frac{1}{g_{ce}}$$

$$g_{ce} = h_{oe} - (1 + h_{fe}) g_{b'c}$$

$$g_{b'c} = \frac{1}{r_{b'c}} = \frac{1}{5.2 \times 10^{-6}} = 192307.6923 = 0.1923$$

$$g_{ce} = (20 \times 10^{-6}) - (1 + 100)(0.1923 \times 10^{-6})$$

$$= -19423076.92 = -0.19418$$

$$r_{ce} = \frac{1}{-0.19418} = -5.1498$$

Transition capacitance  $C_c(\omega)$   $C_u = C_{ob}$

$$\rightarrow C_c(\omega) C_u = 8pt$$

Diffusion capacitance  $C_c(\omega) C_{\pi} = \frac{g_m}{2\pi f_T}$

$$C_{\pi} = \frac{0.1923}{2 \times 3.14 \times 500 \times 10^3}$$

$$C_{\pi} = \frac{9615}{157} = 61.242F$$

\* The following low frequency parameters for a given  $I_c = 5mA$ ,  $V_{ce} = 8V$  at room temperature  $h_{ie} = 1k\Omega$ ,  $h_{fe} = 100$ ,  $h_{re} = 10^{-4}$ ,  $h_{oe} = 4 \times 10^{-5} A/V$  at the same operating point,  $f_T = 10MHz$ ,  $C_{ob} = 2pt$ . Calculate the values of hybrid- $\pi$  parameters (or) high frequency parameters.

Given

Collector current  $I_c = 5mA$

Voltage b/n collector and emitter  $V_{ce} = 8V$

Short circuit input impedance  $h_{ie} = 1k\Omega$

Short circuit forward current gain  $h_{fe} = 100$

Open circuit reverse voltage gain  $h_{re} = 10^{-4}$

Open circuit output conductance  $h_{oe} = 4 \times 10^{-5}$

$$f_T = 10MHz$$

$$C_{ob} = 2pt = 2 \times 10^{-12} F$$

(i) Trans conductance  $g_m = \frac{|I_c|}{V_T} = \frac{5 \times 10^{-3}}{26 \times 10^{-3}} = 0.19232$

(ii) Resistance b/n virtual base and emitter

$$r_{b'e} = \frac{h_{ie}}{g_m}$$

$$r_{b'e} = 5.1498 - r_{ce}$$

$$r_{b'e} = \frac{100}{0.1923} = 520.02$$

(iii) Resistance b/n virtual base & collector

$$r_{b'c} = \frac{r_{b'e} \beta}{1 + \beta}$$

$$r_{b'c} = \frac{100}{0.1923} = 520.02 \quad \frac{520.02}{101} = 5.1487$$

(iv) Resistance b/n actual base & virtual base

$$r_{bb'} = h_{ie} - r_{b'e}$$

$$r_{bb'} = 1 \times 10^3 - 520.02$$

$$= 479.98$$

(v) Resistance b/n collector & emitter

$$r_{ce} = \frac{1}{g_{ce}}$$

$$g_{ce} = h_{oe} - (1 + h_{fe}) g_{b'c}$$

$$= h_{oe} - (1 + h_{fe}) \frac{1}{r_{b'c}}$$

$$g_{ce} = 4 \times 10^{-5} - (1 + 100) \frac{1}{5200200}$$

$$= 4 \times 10^{-5} - (101) \frac{1}{5200200}$$

$$g_{ce} = 2.0577 \times 10^{-5}$$

$$\Rightarrow r_{ce} = \frac{1}{2.0577 \times 10^{-5}} = 48597.94$$

(vi) Transmission capacitance  $C_c$  OR  $C_u = C_{ob} = 2 \text{ pF}$

(vii) Diffusion capacitance  $C_c$  OR  $C_T = \frac{I_m}{2\pi f_T}$

$$= \frac{0.1923}{2 \times 3.14 \times 10 \times 10^3}$$

$$= \frac{1923}{628}$$

$$= 3.0621$$



4) Common Emitter Amplifier with short circuit load ( $R_L = 0 \Omega$ ) at high frequencies:

→ Consider a single stage CE amplifier with short circuit load (by short circuiting b/n collector and emitter to make  $R_L = 0 \Omega$ ) as shown in fig-(a).

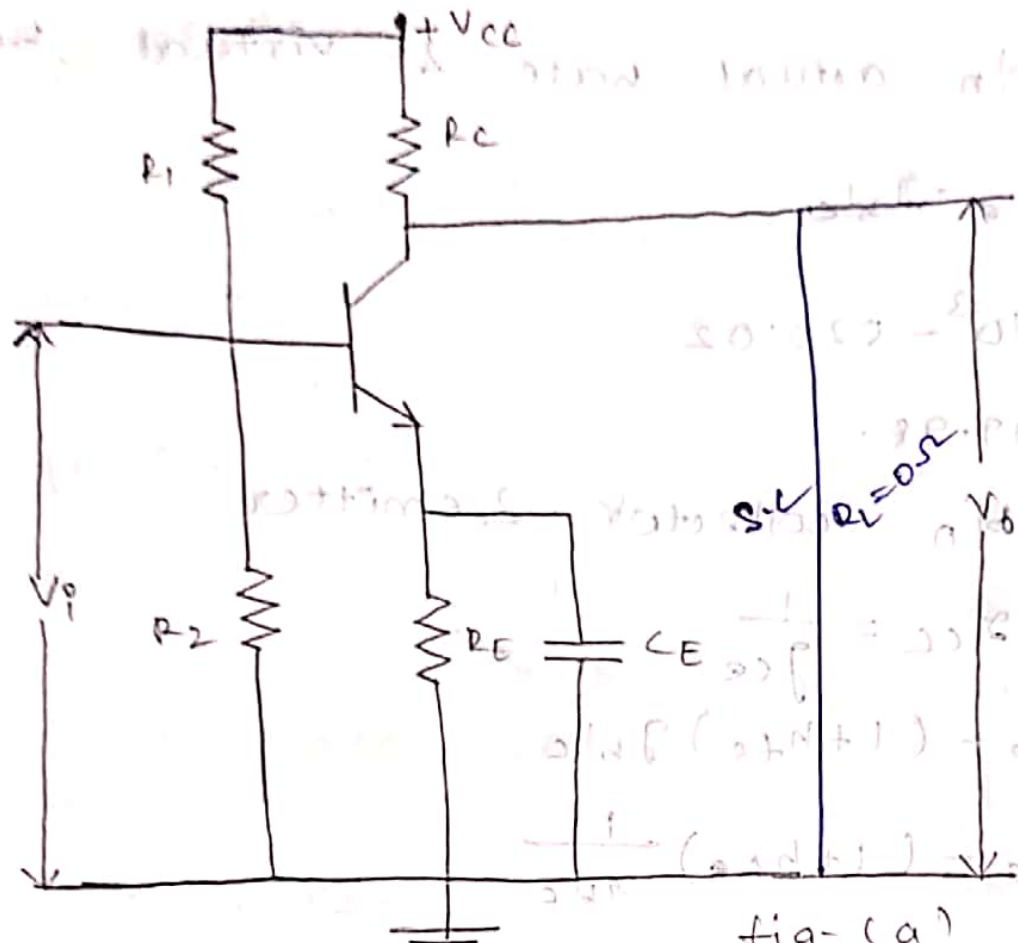


fig-(a)

CE amplifier at high frequencies by using hybrid- $\pi$  model at high frequencies is shown in fig.

→ Small signal equivalent circuit of CE amplifier:

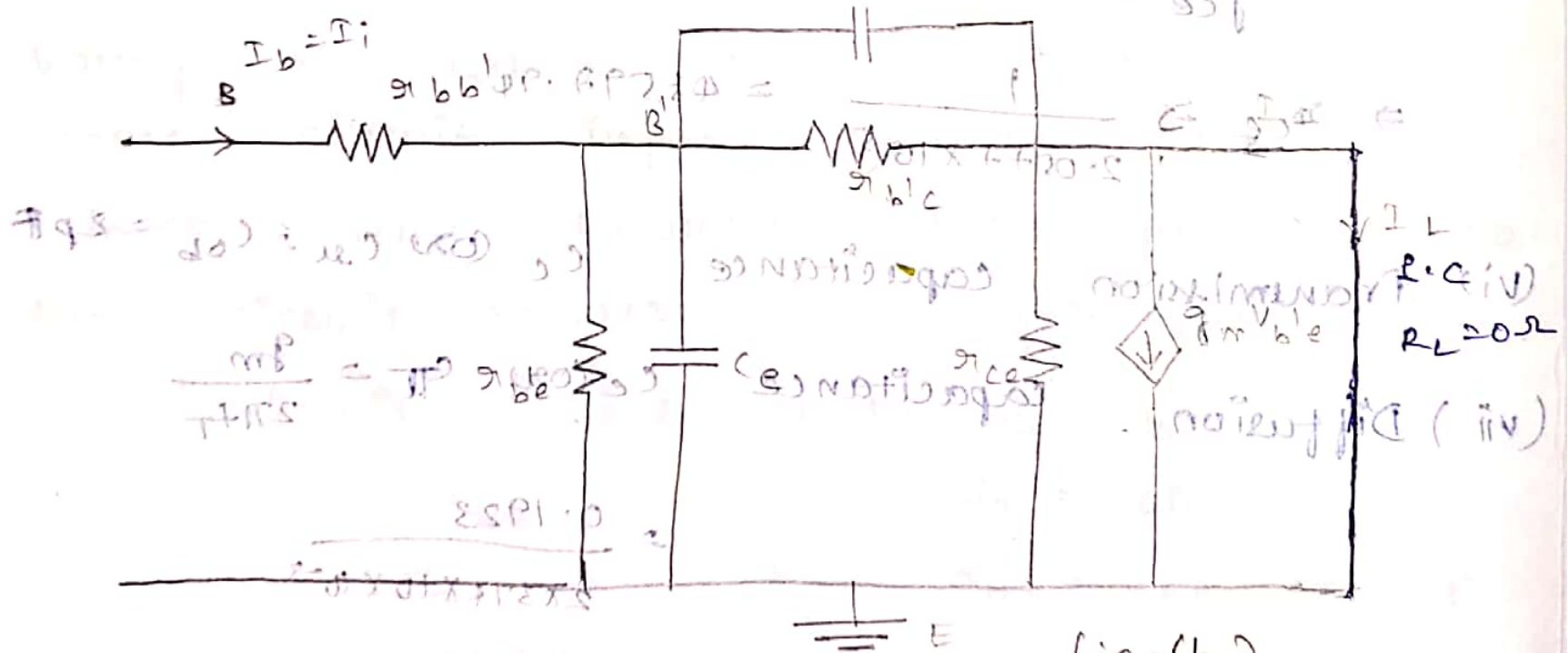


fig-(b)

Approximations:

→  $r_{ce}$  disappears because it is short circuited

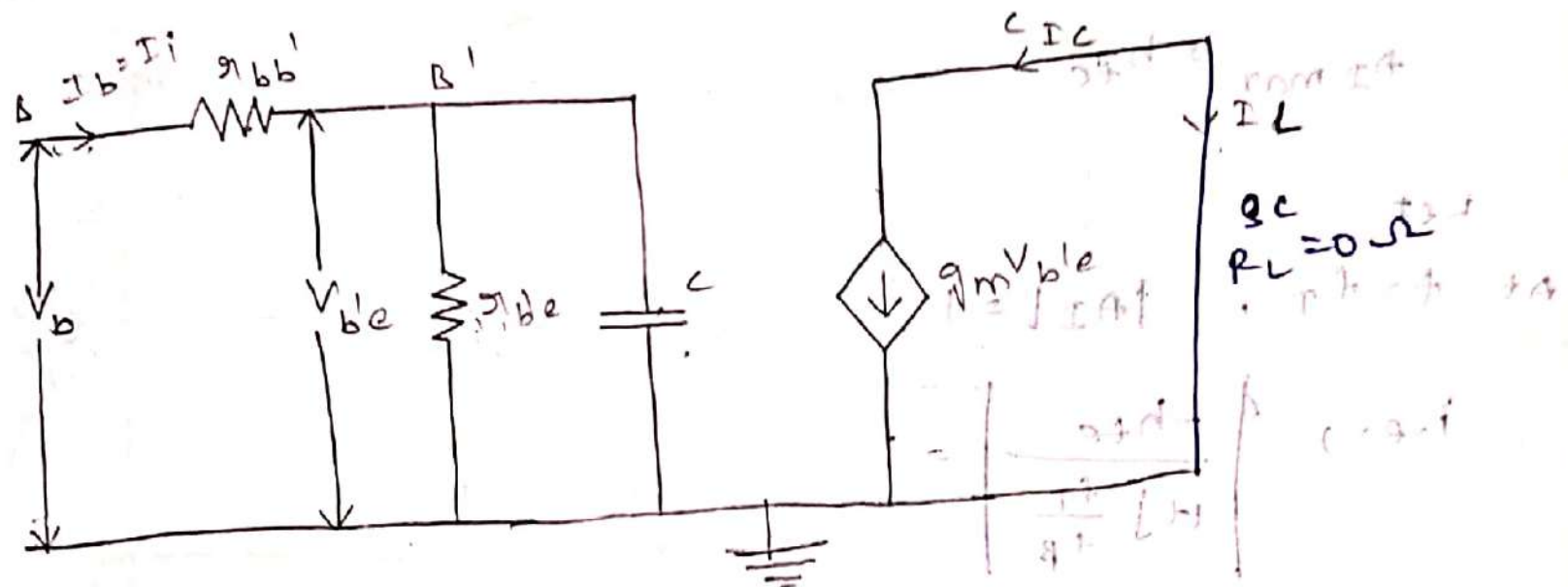
→  $r_{be}$  &  $r_{b'c}$  are in parallel because of short circuit i.e.,  $r_{be} || r_{b'c} \approx r_{be}$  ( $\because r_{be} \ll r_{b'c}$ )

hence  $r_{b'c}$  disappears.



$\rightarrow C_e$  &  $C_c$  are in parallel because of short circuit  
 at this node voltage, etc.  $= C_e + C_c$   
 $\rightarrow$  current to be delivered from input node to output node can be neglected.

With the above four approximations, approximate equivalent circuit is shown in fig. (c)



By definition,

short circuit current gain

$$A_I = \frac{I_L}{I_i} = \frac{-I_c}{I_b} = \frac{-g_m V_{be}}{V_{be} / (r_{be} \parallel \frac{1}{j\omega C_e})}$$

$$A_I = \frac{-g_m}{r_{be} \parallel \frac{1}{j\omega C_e}} = \frac{-g_m r_{be}}{1 + j\omega r_{be} C_e}$$

$$= \frac{-h_{fe}}{1 + j 2\pi f r_{be} C_e} \quad \left[ \begin{aligned} &h_{fe} = g_m r_{be} = \frac{h_{fe}}{g_m} \\ &\omega = 2\pi f \end{aligned} \right]$$

where  $f_p = \frac{1}{2\pi r_{be} C_e}$

At  $f = f_p$

$$|A_I| = \left| \frac{-h_{fe}}{1 + j \frac{f}{f_p}} \right| = \left| \frac{-h_{fe}}{1 + j} \right| = \frac{h_{fe}}{\sqrt{2}} = \frac{h_{fe}}{1.414}$$

Hence  $f_p$  is the frequency at which short circuit current gain falls to  $\frac{1}{\sqrt{2}}$  of its maximum value. Also short circuit gain falls to below 3dB from its maximum value in dB.

At  $f=0$  very small

$$|A_I| \approx \left| \frac{-h_{fe}}{1+j(0)} \right| \approx h_{fe}$$

$$A_{I \text{ max}} = h_{fe}$$

Let

$$\text{At } f = f_T, |A_I| = 1$$

$$\text{i.e., } \left| \frac{-h_{fe}}{1+j \frac{f_T}{f_p}} \right| = 1$$

$$\frac{h_{fe}}{\sqrt{1 + \left( \frac{f_T}{f_p} \right)^2}} = 1$$

Squaring on both sides

$$\frac{h_{fe}^2}{1 + \frac{f_T^2}{f_p^2}} = 1$$

$$h_{fe}^2 = 1 + \frac{f_T^2}{f_p^2}$$

$$h_{fe}^2 - 1 = \frac{f_T^2}{f_p^2}$$

$$h_{fe}^2 \approx \frac{f_T^2}{f_p^2}$$

$$\left( \frac{f_T}{f_p} \right) \left[ \frac{h_{fe}^2}{f_p^2} \gg 1 \right]$$

Square root on both sides

$$h_{fe} = \frac{f_T}{f_p}$$

$$f_T = h_{fe} f_p$$

$$f_T = h_{fe} \frac{1}{2\pi \times 10^6 \times C} \frac{g_m}{\beta}$$

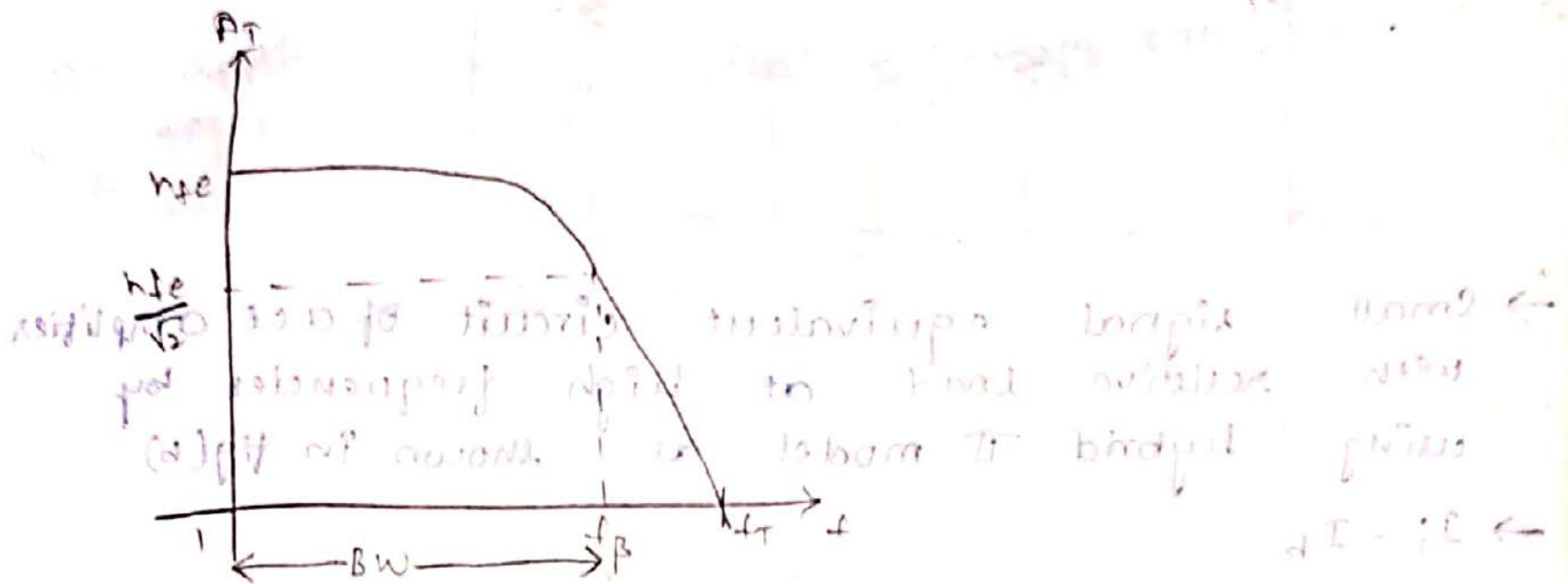


$$f_T = \frac{g_m}{2\pi(C_c + C_e)} \quad \text{where } g_m = \frac{h_{fe}}{r_m}$$

bandwidth configuration  $\approx \frac{g_m}{2\pi C_e}$   $[\because C_c \gg C_e]$

$$f_T = \frac{g_m}{2\pi C_e}$$

Frequency response of CE amplifier with short circuit load is shown in figure



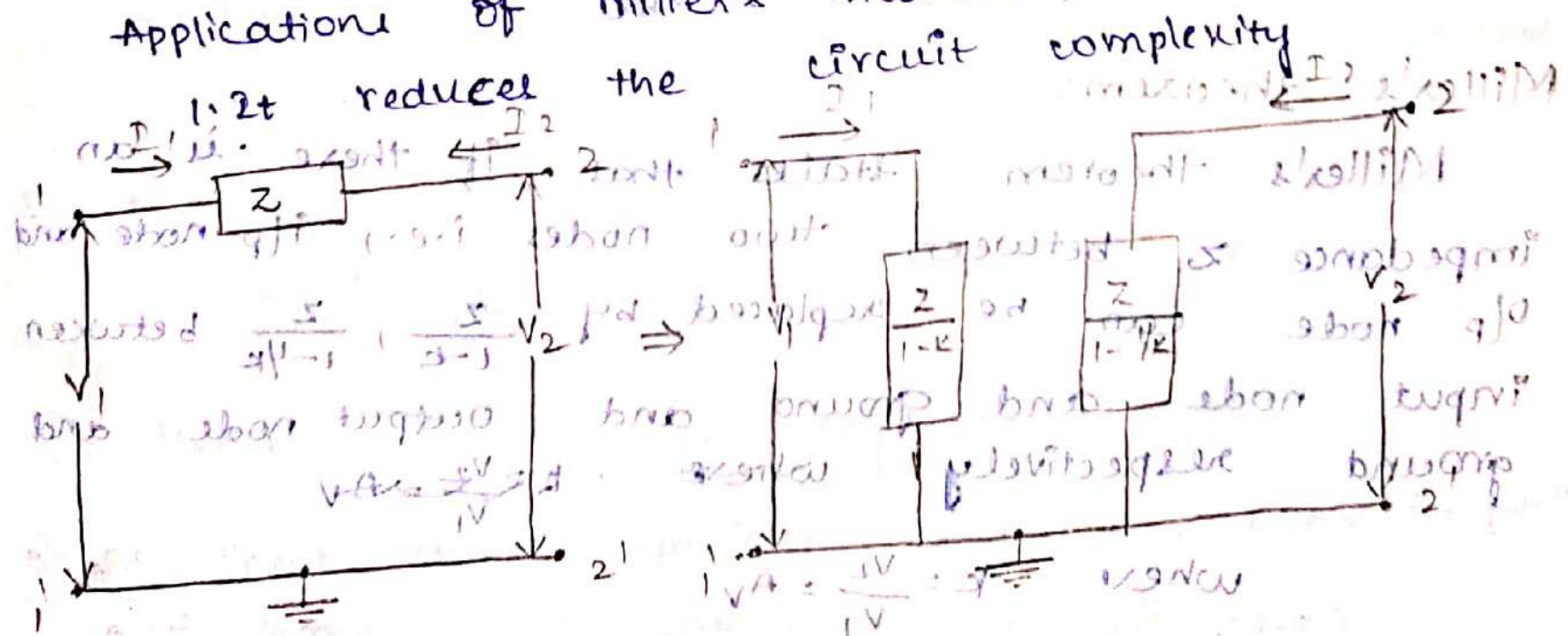
gain bandwidth product is

$$|A_T| \times BW = h_{fe} \cdot f_{\beta} = f_T$$

Miller's theorem:

Miller's theorem states that if there is an impedance  $Z$  between two nodes i.e., i/p node and o/p node can be replaced by two impedances  $\frac{Z}{1-k}$  and  $\frac{Z}{1-1/k}$  between input node and ground, output node and ground respectively where  $k = \frac{V_2}{V_1} = A_V$

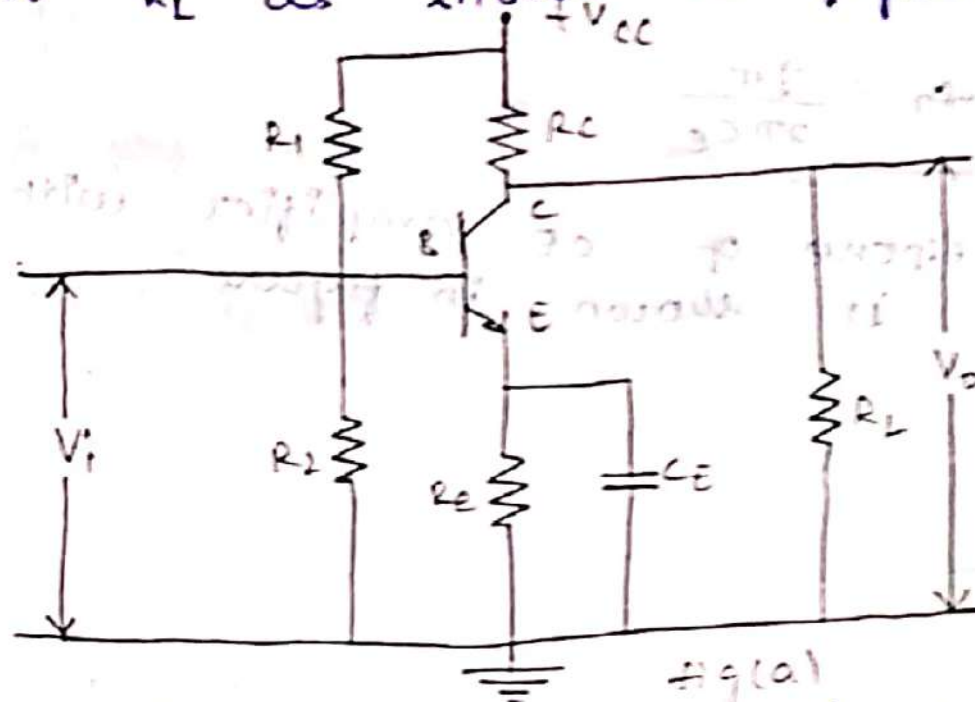
Applications of miller's theorem are





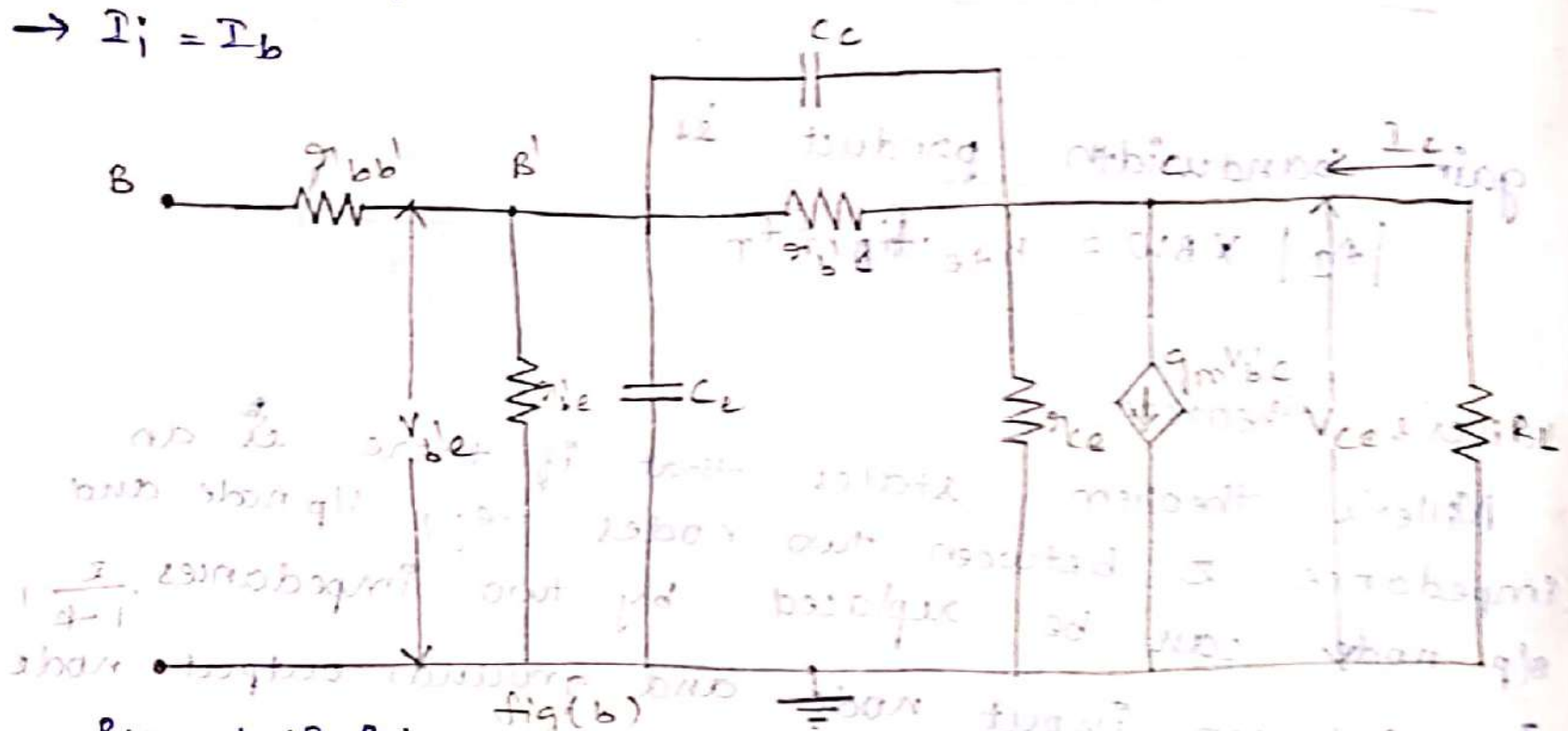
#### 4) Common Emitter (CE) Amplifier with resistive load ( $R_L$ ) at high frequencies:-

Consider a single stage CE amplifier with load resistor  $R_L$  as shown in figure (a)



→ Small signal equivalent circuit of a CE amplifier with resistive load at high frequencies by using hybrid  $\pi$  model as shown in fig(b)

$$\rightarrow I_i = I_b$$



By definition

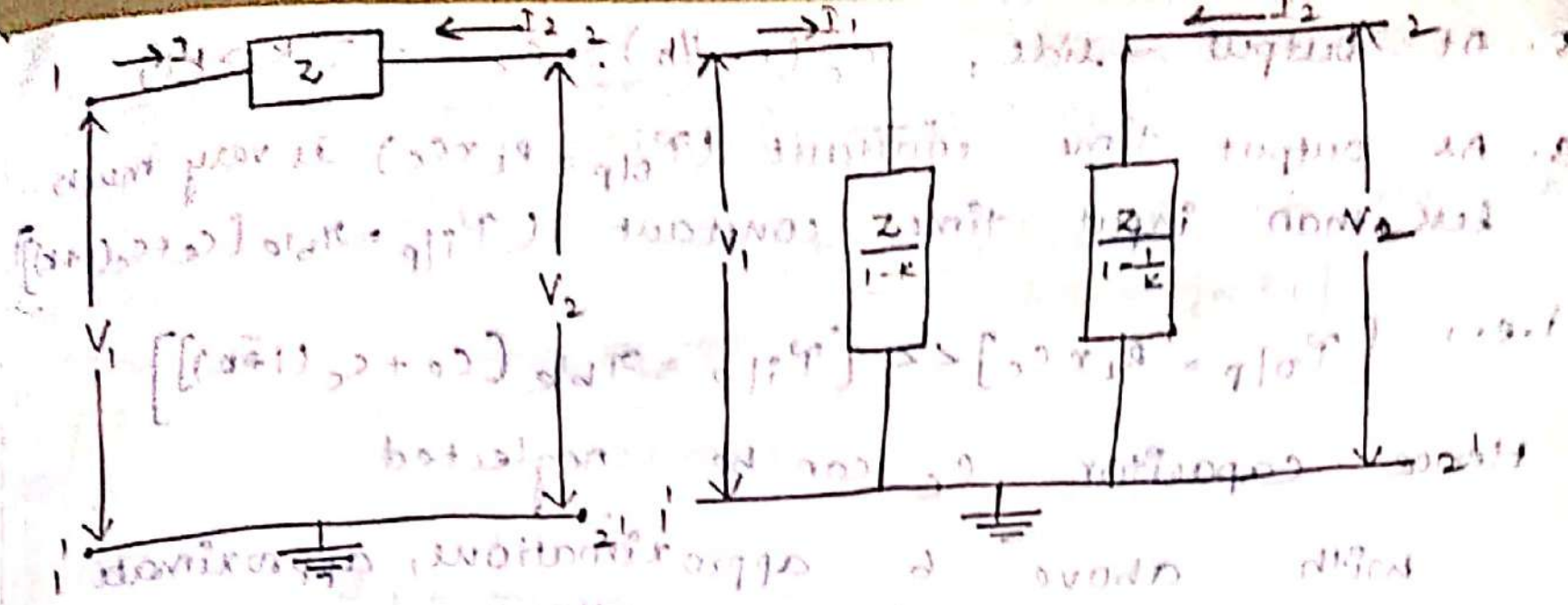
$$\text{Voltage gain } (A_V) = K = \frac{V_{ce}}{V_{be}}$$

Miller's theorem:

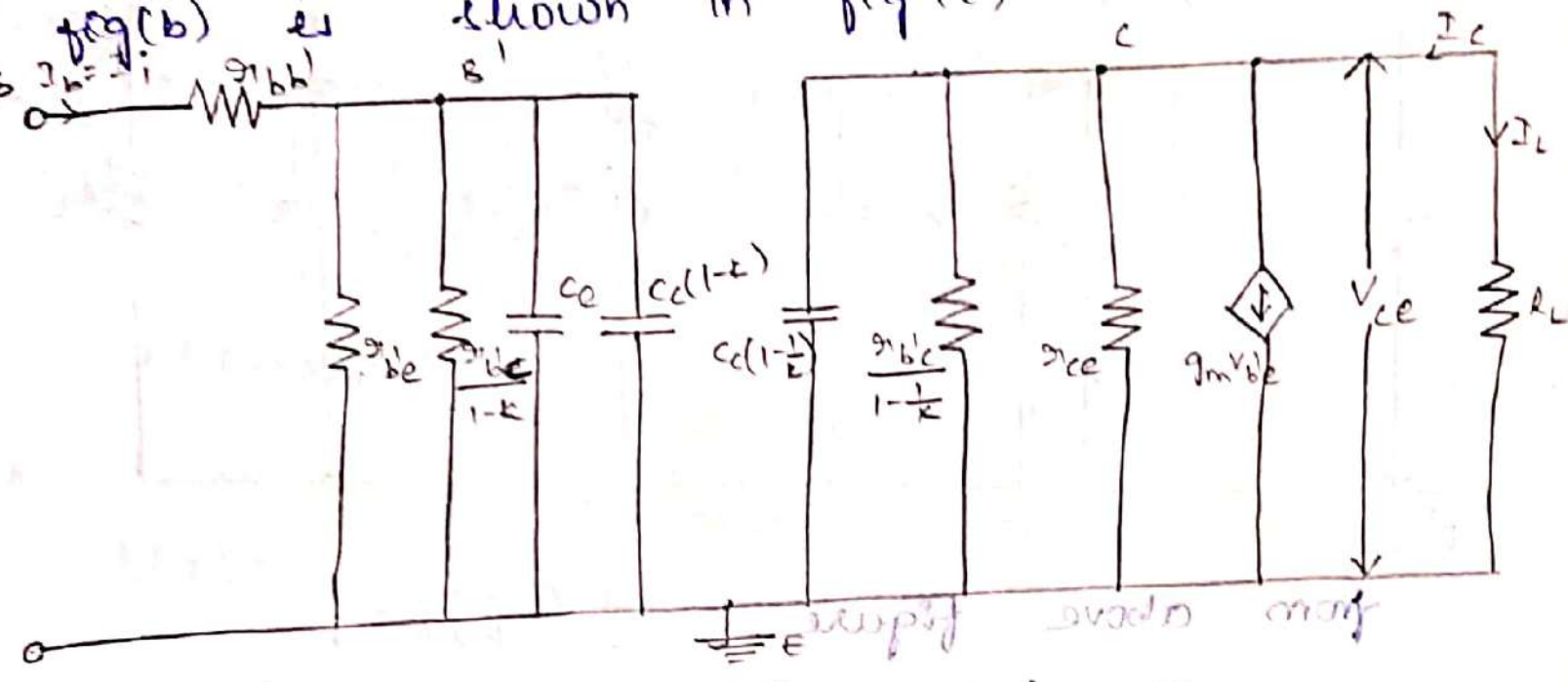
Miller's theorem states that if there is an impedance  $Z$  between two nodes i.e., i/p node and o/p node can be replaced by  $\frac{Z}{1-K}$ ,  $\frac{Z}{1-1/K}$  between input node and ground and output node and ground respectively where  $K = \frac{V_o}{V_i} = A_V$

$$\text{where } K = \frac{V_2}{V_1} = A_V$$





On application of miller's theorem equivalent circuit of fig (b) is shown in fig (c)



Where  $k = \frac{V_{ce}}{V_{be}}$

Approximations:

1. At  $\frac{1}{1-k}$  output side  $\frac{r_{bc}}{1-1/k}$  is approximately  $r_{bc}$  ( $k \gg 1$ )

$$\frac{r_{bc}}{1-1/k} \approx r_{bc} = r_L$$

2. At output side three resistors are in parallel i.e.  $r_{bc} \parallel r_{ce} \parallel R_L \approx R_L$  [ $\because r_{bc} \gg r_{ce} \gg R_L$ ] hence  $r_{bc}$  and  $r_{ce}$  can be neglected

3. At input side

$$r_{be} \parallel \frac{r_{bc}}{1-k} \approx r_{be} \quad [\because r_{be} \ll \frac{r_{bc}}{1-k}]$$

Hence  $\frac{r_{bc}}{1-k}$  can be neglected

At input side, capacitors  $c_e$  and  $c_c(1-k)$  are in parallel and hence total capacitance,  $C = c_e \parallel c_c(1-k)$   
 $C \approx c_e + c_c(1-k)$



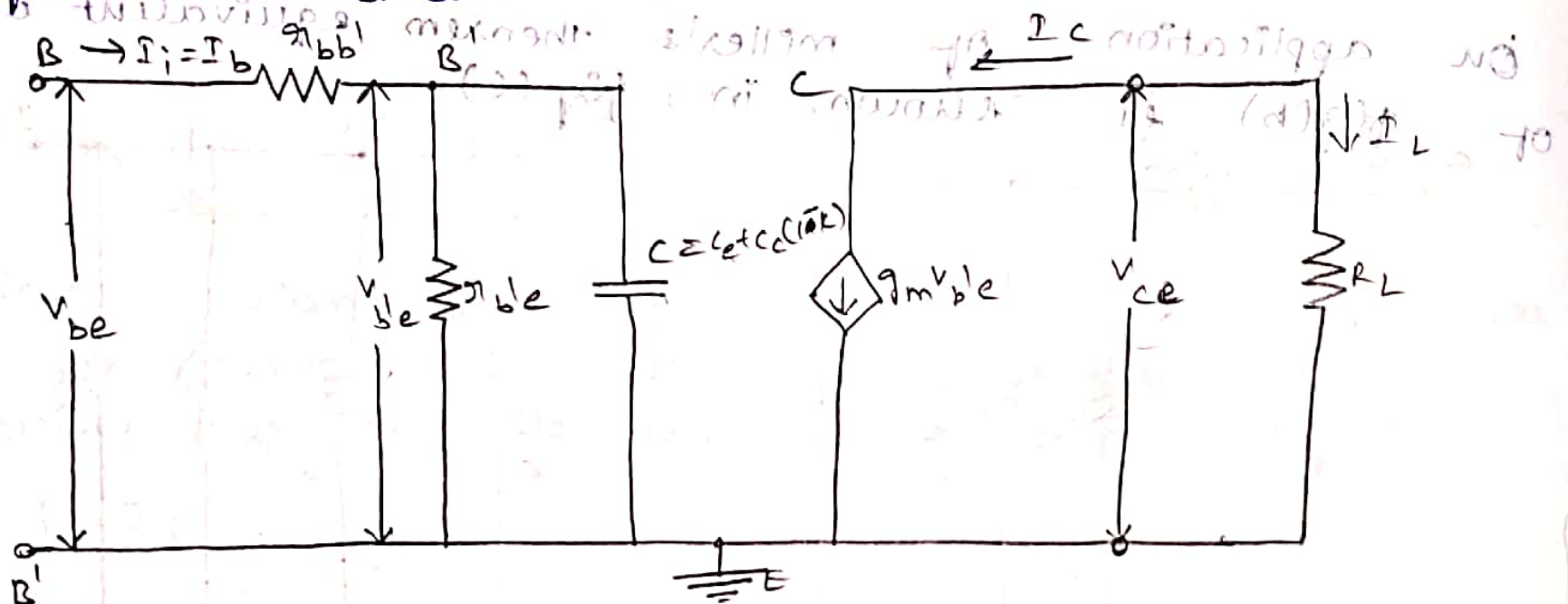
5. At output side,  $C_c(1 - \beta) \approx C_c$  [ $\because \beta \gg 1$ ]

6. As output time constant ( $\tau_{o/p} = R_L \times C_c$ ) is very much less than input time constant ( $\tau_{i/p} = r_{be} [C_e + C_c(1 + \beta)]$ )

i.e.,  $[\tau_{o/p} = R_L \times C_c] \ll [\tau_{i/p} = r_{be} [C_e + C_c(1 + \beta)]]$

Hence capacitor  $C_c$  can be neglected

With above 6 approximations, approximate equivalent circuit is as follows



from above figure

$$V_{ce} = (-g_m V_{be}) R_L$$

Voltage gain  $\Rightarrow \frac{V_{ce}}{V_{be}} = -g_m R_L \Rightarrow A_V = K = -g_m R_L$

current gain with resistive load  $A_I = \frac{I_L}{I_i} = \frac{I_L}{I_b}$

$$I_L = -g_m V_{be}$$

$$I_i = \frac{V_{be}}{r_{be} \parallel \frac{1}{j\omega C}}$$

$$A_I = \frac{I_L}{I_i} = \frac{-g_m V_{be}}{\frac{V_{be}}{r_{be} \parallel \frac{1}{j\omega C}}} = \frac{-g_m r_{be} \parallel \frac{1}{j\omega C}}{1}$$

$$A_I = \frac{-g_m r_{be}}{1 + j\omega r_{be} C}$$

$$A_I = \frac{-g_m r_{be}}{1 + j\omega r_{be} C}$$



$$A_v = \frac{-h_{fe}}{1 + j2\pi f r_{be} [C_e + C_c(1 + g_m R_L)]}$$

$$\therefore [h_{fe} = g_m r_{be}, \omega = 2\pi f, C_e + C_c(1 + g_m R_L)]$$

$$A_v = -g_m R_L$$

$$A_v = \frac{-h_{fe}}{1 + j\left(\frac{f}{f_H}\right)} \quad \text{where}$$

$$f_H = \frac{1}{2\pi r_{be} [C_e + C_c(1 + g_m R_L)]}$$

from frequency response, at very low frequencies,

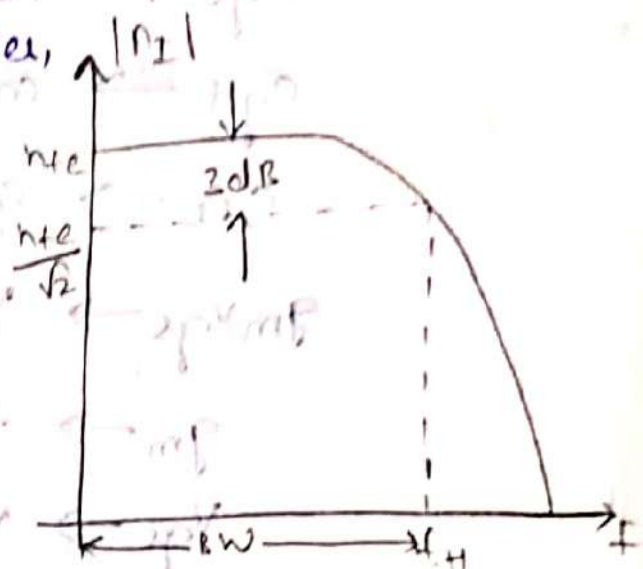
$$|A_v| = \left| \frac{-h_{fe}}{1 + j(0)} \right|$$

$$\Rightarrow A_{v \max} = h_{fe}$$

$$\text{At } f = f_H$$

$$|A_v| = \left| \frac{-h_{fe}}{1 + j\left(\frac{f}{f_H}\right)} \right| = \left| \frac{-h_{fe}}{1 + j(1)} \right|$$

$$|A_v| = \left| \frac{-h_{fe}}{\sqrt{2}} \right| = \frac{h_{fe}}{\sqrt{2}}$$



from frequency response,  $f_H$  is the frequency at which CE current gain with relative load falls to  $\frac{1}{\sqrt{2}}$  of its maximum value (or) falls below 3dB of its maximum value in dB.

\* current gain bandwidth product  $A_v \times BW = |A_v|_{\max} \times f_H$

$$= h_{fe} \times \frac{1}{2\pi r_{be} [C_e + C_c(1 + g_m R_L)]}$$

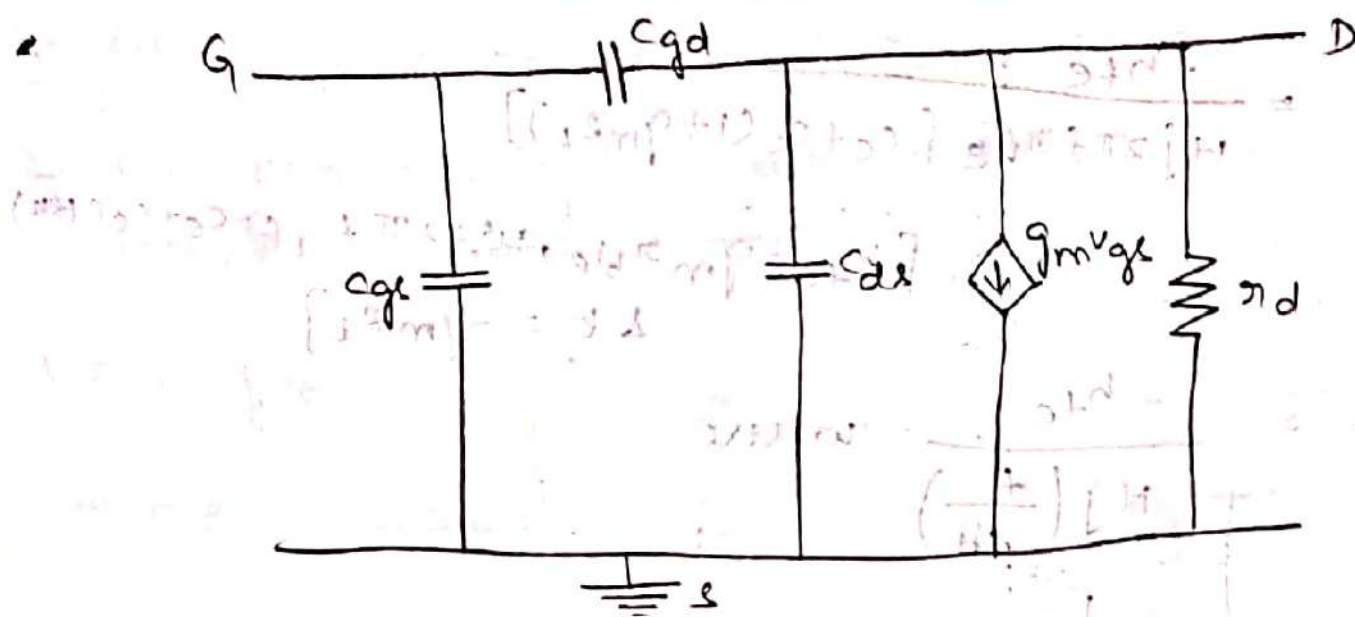
$$= \frac{g_m}{2\pi [C_e + C_c(1 + g_m R_L)]} \quad \left[ \because r_{be} = \frac{h_{fe}}{g_m} \right]$$

Hybrid  $\pi$  model for CS FET:

In low frequency model of J-FET works for frequencies below 1MHz, for frequencies greater than 1MHz the response of JFET will be limited by internal parasitic capacitance of JFET.

The hybrid  $\pi$ -model for common source (CS) FET is shown in fig(a).





parameters of hybrid  $\pi$  FET model are:  
 where G, D, S are gate, drain, source.

$C_{gs} \rightarrow$  capacitance b/n gate and source

$C_{gd} \rightarrow$  capacitance b/n gate & drain

$C_{de} \rightarrow$  capacitance b/n drain & source

$g_m V_{gs} \rightarrow$  voltage dependent current source

$g_m \rightarrow$  trans conductance

$V_{gs} \rightarrow$  voltage b/n gate & source

$r_d \rightarrow$  internal drain resistance

$A_v = g_m r_d \rightarrow$  Amplification factor

The three capacitors [ $C_{gs}$ ,  $C_{gd}$ ,  $C_{de}$ ] are indirectly given over the data sheet of JFET as

$$C_{gd} = C_{iss} - C_{fs}$$

$$C_{gs} = C_{iss} - C_{re}$$

$$C_{de} = C_{os} - C_{re}$$

where  $C_{iss}$  = common source input capacitance

$C_{fs}$  = forward transfer capacitance

$C_{re}$  = reverse transfer capacitance

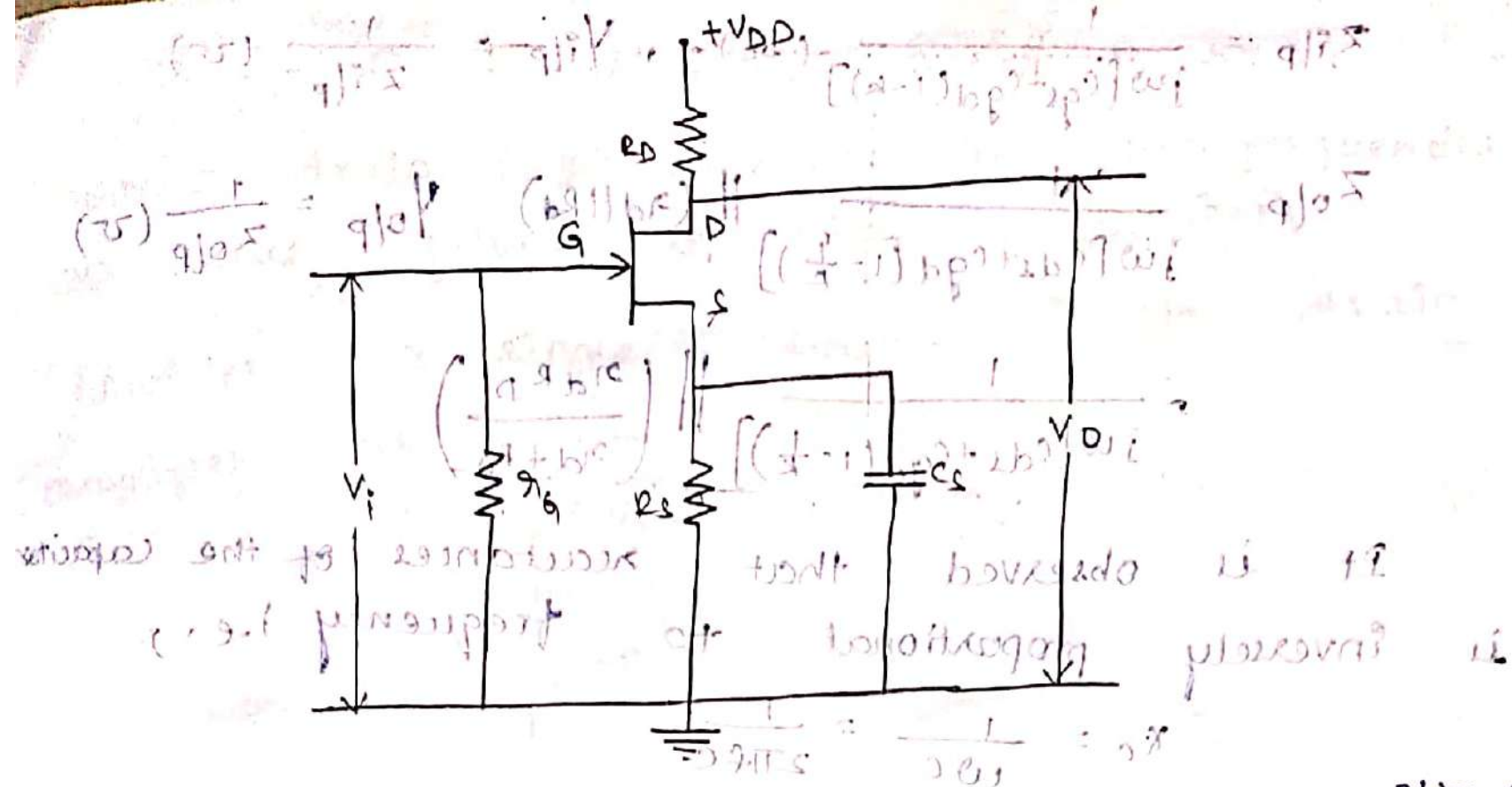
$C_{os}$  = O/P capacitance

Common source amplifier at high frequencies

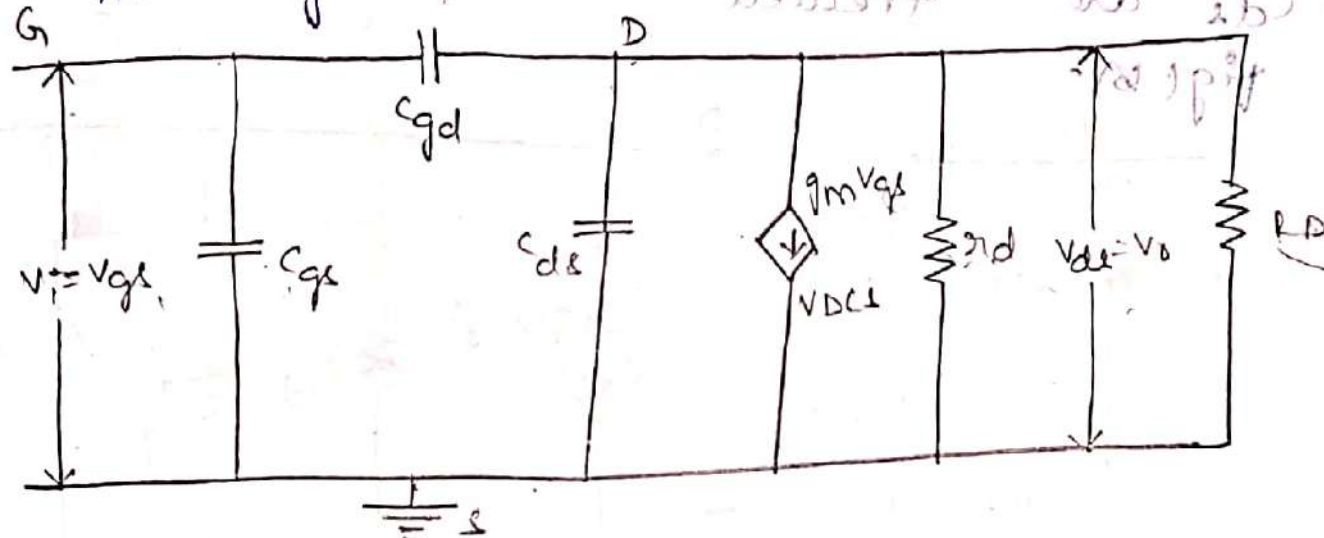
Consider a single stage C.S. amplifier as

shown in fig(a).





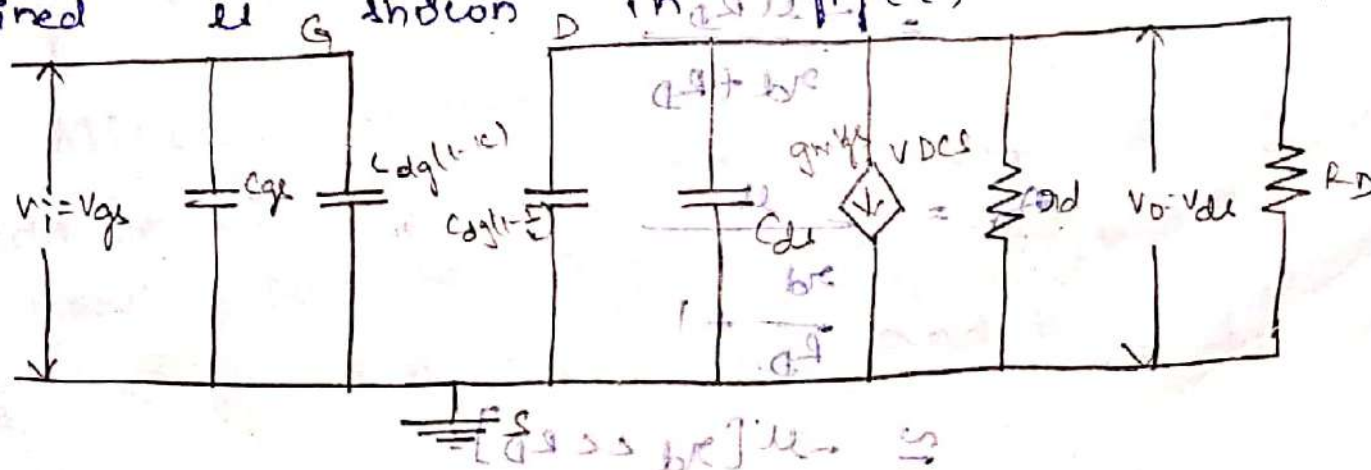
Small signal equivalent circuit of CS amplifier at high frequencies is shown in Fig (b) using hybrid- $\pi$  model is shown in Fig (b).



From figure  $k \approx AV = \frac{V_o}{V_i} = \frac{V_{ds}}{V_{gs}}$

voltage gain of CE amplifiers. Miller's theorem states that if there is impedance between two nodes (i.e., input node & output node), can be replaced by two impedances  $\frac{Z}{1-k}$  &  $\frac{Z}{1-1/k}$  b/w input node and ground, & output node and ground respectively as shown below where  $k = \text{voltage gain}$ .

After applying Miller's theorem to Fig (b), the circuit obtained is shown in Fig (c).





$$Z_{i/p} = \frac{1}{j\omega [C_{gs} + C_{gd}(1-k)]} (\Omega) \quad , \quad V_{i/p} = \frac{1}{Z_{i/p}} (V)$$

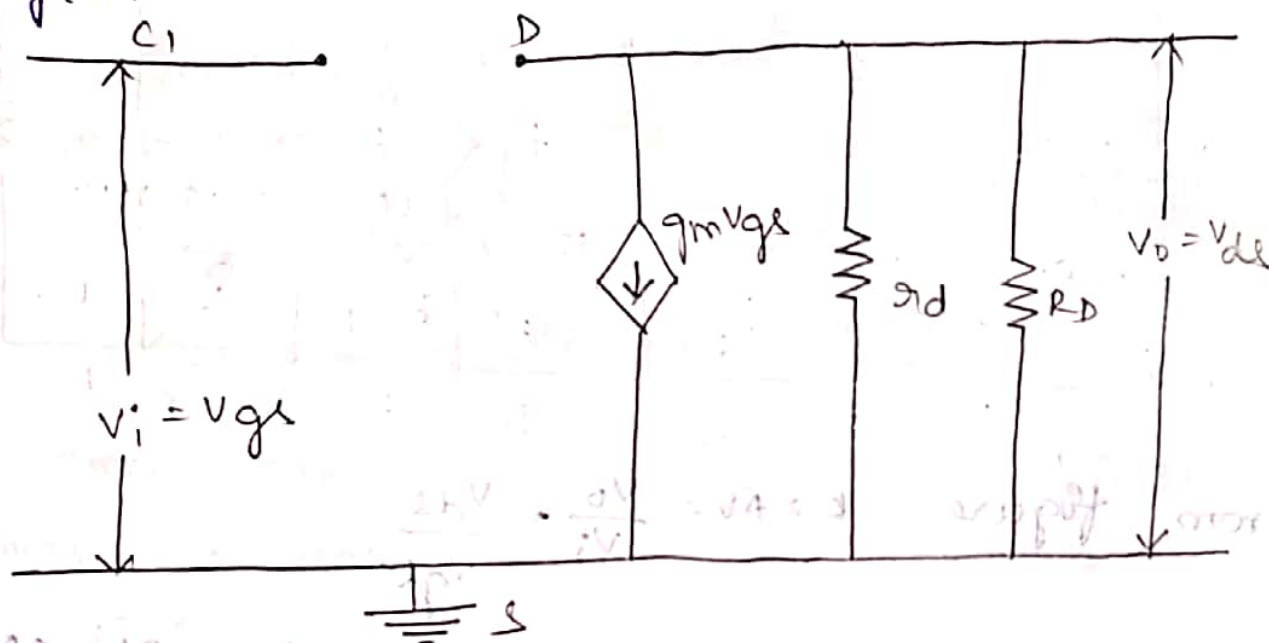
$$Z_{o/p} = \frac{1}{j\omega [C_{ds} + C_{gd}(1-\frac{1}{k})]} \parallel (r_d \parallel R_D) \quad V_{o/p} = \frac{1}{Z_{o/p}} (V)$$

$$= \frac{1}{j\omega [C_{ds} + C_{gd}(1-\frac{1}{k})]} \parallel \left( \frac{r_d R_D}{r_d + R_D} \right)$$

It is observed that reactance of the capacitor is inversely proportional to frequency i.e.,

$$X_C = \frac{1}{\omega C} = \frac{1}{2\pi f C}$$

Hence at low frequencies i.e. capacitive reactance very high the three capacitors are  $C_{gs}$ ,  $C_{ds}$  are treated as open circuit, as shown in fig (b).



High frequency hybrid- $\pi$  model of a FET at low frequencies

$$V_{ds} = -g_m V_{gs} (r_d \parallel R_D)$$

$$A_v = \frac{V_{ds}}{V_{gs}} = -g_m \left( \frac{r_d R_D}{r_d + R_D} \right)$$

$$= \frac{-g_m r_d R_D}{r_d + R_D}$$

$$= \frac{-\mu R_D}{r_d + R_D}$$

$$A_v = \frac{\mu}{\frac{r_d}{R_D} + 1}$$

$$\approx -\mu [r_d \ll R_D]$$



$\therefore R_D$  is also denoted  $R_L$  in common drain [CD] amplifier at high frequencies (or) source follower at high frequencies.

Consider a single stage common drain amplifier as shown in fig(a)

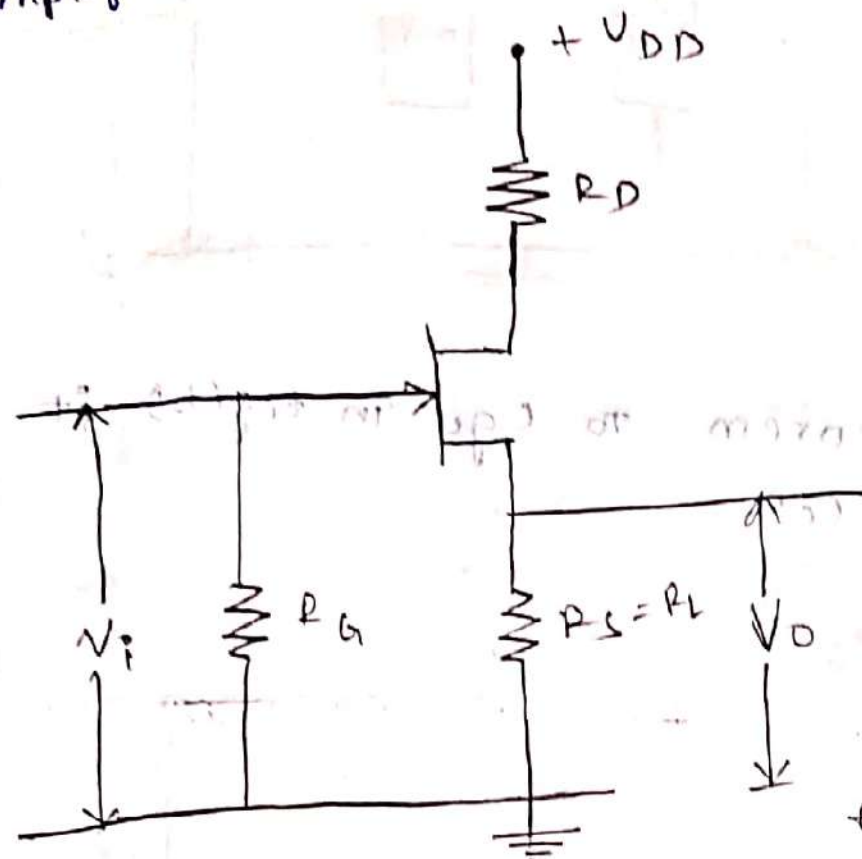


fig-(a)

Small signal equivalent circuit of CD amplifier [source follower] at high frequencies by using hybrid- $\pi$  model of FET is shown in fig-(b).

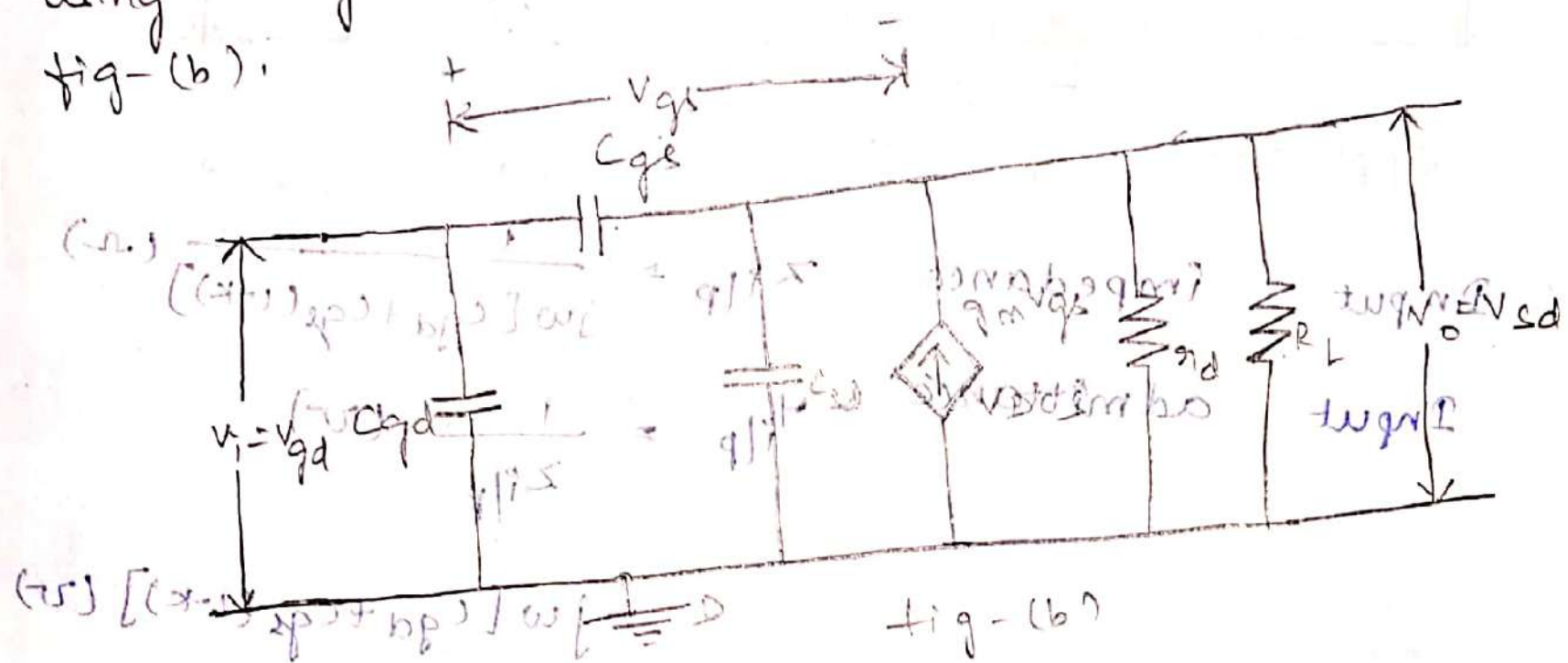


fig-(b)

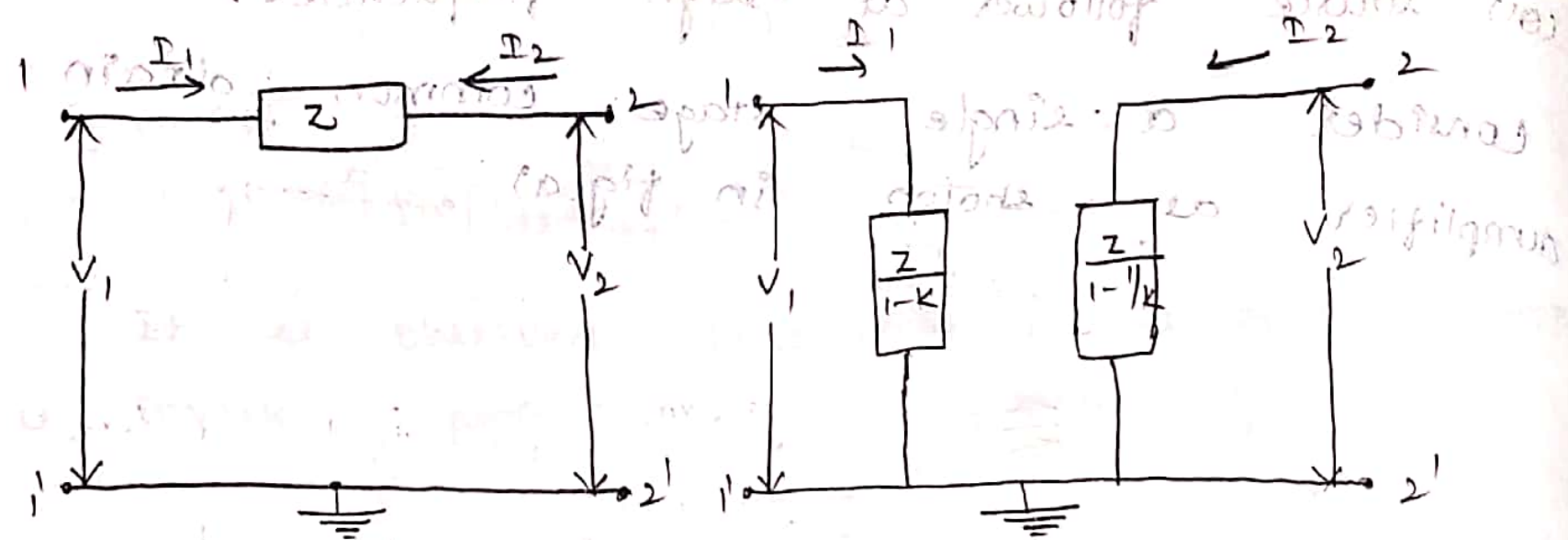
By definition, voltage gain  $A_v = \frac{V_O}{V_i} = \frac{V_{sd}}{V_{gs}}$

Miller's theorem states that if there is two nodes i.e.,  $n_1$  node and  $n_2$  node, the impedance between these two nodes can be replaced by two impedances  $Z_1$  and  $Z_2$  connected from input node  $n_1$  to ground and output node  $n_2$  to ground, where  $Z_1 = \frac{Z}{1-K}$  and  $Z_2 = \frac{Z}{1-1/K}$ .

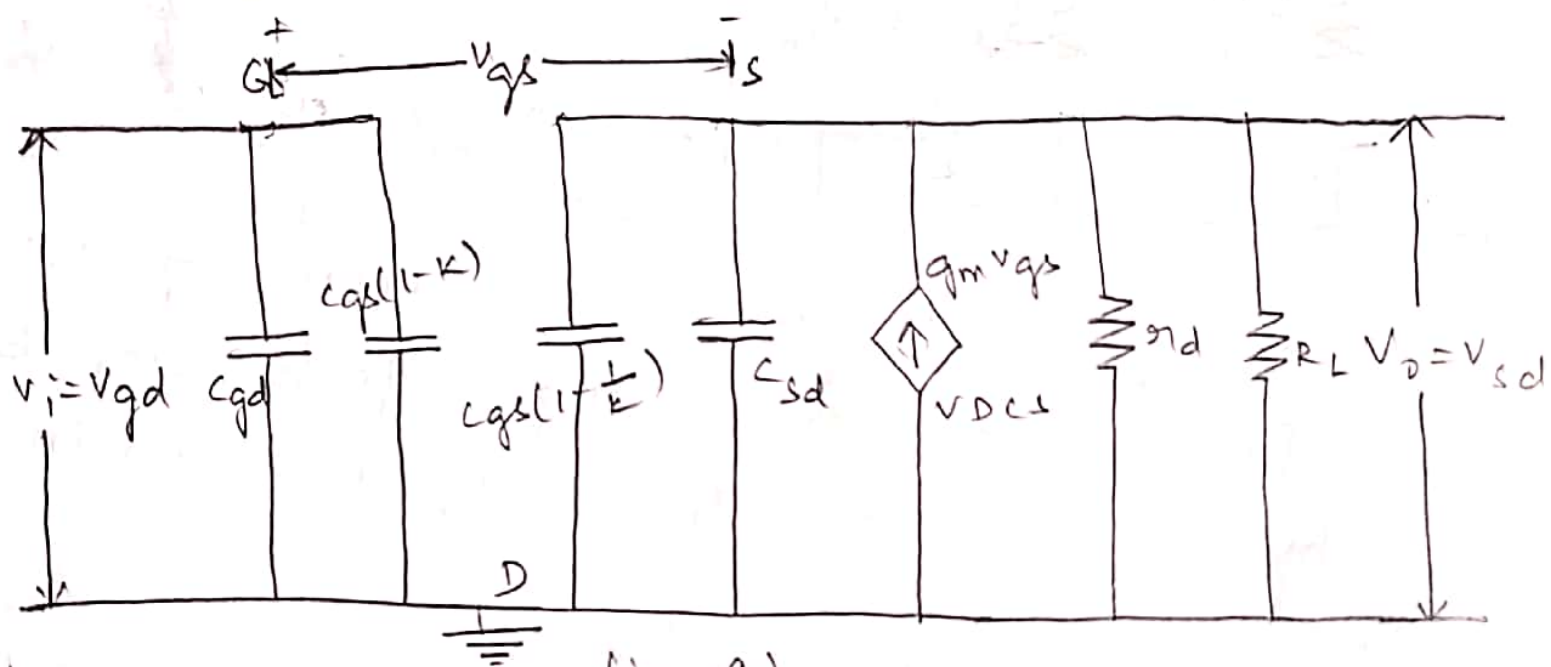


node and ground respectively

where  $k = A_v = \frac{V_2}{V_1}$



Apply miller's theorem to  $C_{gs}$  in fig (b) it will give us fig (c)



Application of miller's theorem to fig (b)

Input impedance  $Z_{i/p} = \frac{1}{j\omega [C_{gd} + C_{gs}(1-k)]} (\Omega)$

Input admittance  $Y_{i/p} = \frac{1}{Z_{i/p}} (\Omega)$

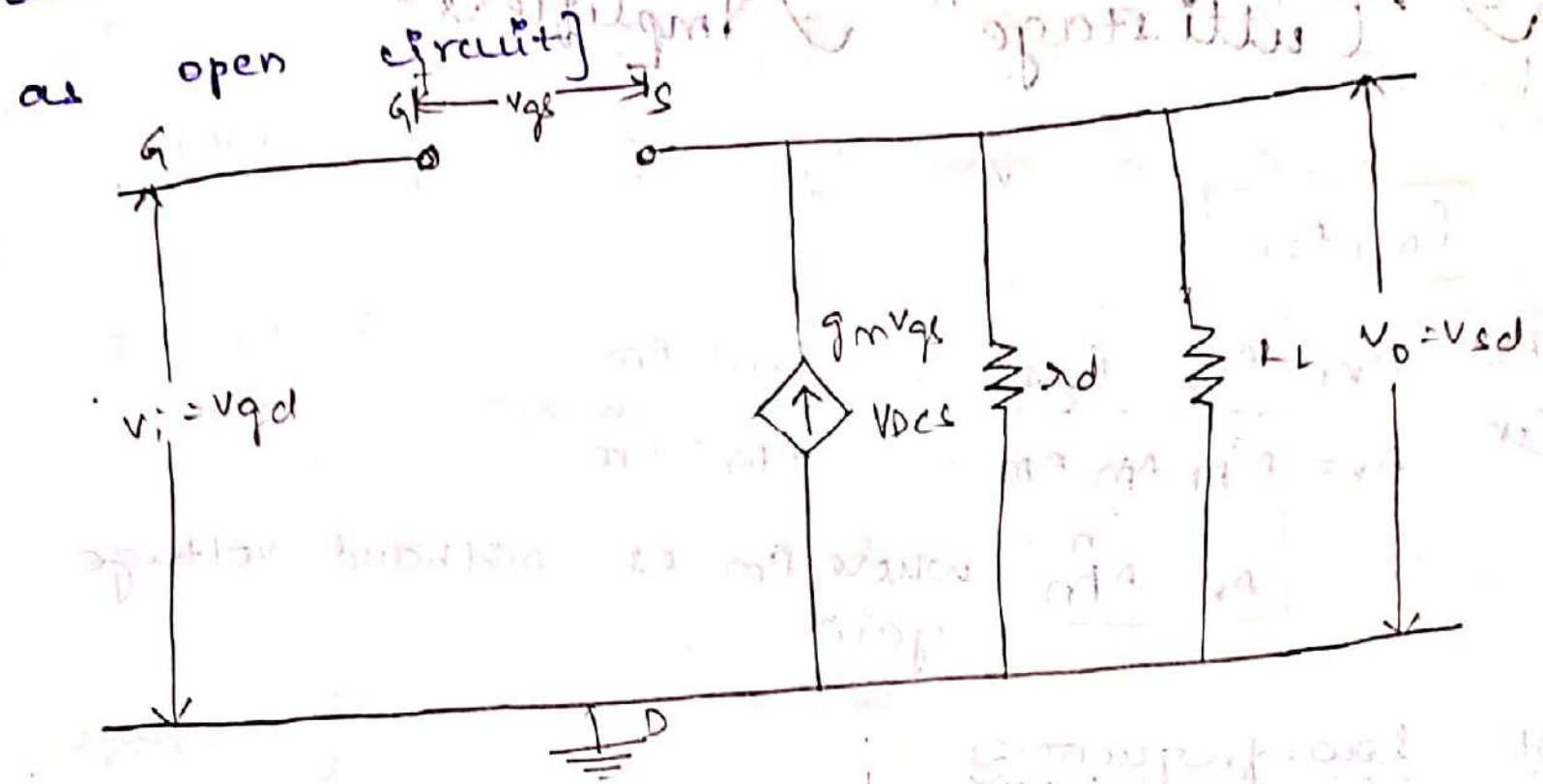
$= j\omega [C_{gd} + C_{gs}(1-k)] (\Omega)$

Output impedance  $Z_{o/p} = \frac{1}{j\omega [C_{sd} + C_{gs}(1-\frac{1}{k})]} \parallel (rd \parallel RL)$

$= \frac{rd \cdot RL}{j\omega [C_{sd} + C_{gs}(1-\frac{1}{k})] \cdot rd + RL} (\Omega)$

Output admittance  $Y_{o/p} = \frac{1}{Z_{o/p}} (\Omega)$

At low frequencies, all the three capacitors are having high reactances, and hence they can be disappeared in the approximate equivalent circuit [i.e.,  $X_C = \frac{1}{j\omega C} = \frac{1}{j2\pi fC} \rightarrow$  capacitors are treated as open circuit]



Approximate equivalent circuit at low frequencies

$$v_o = (g_m v_{gs}) (r_d || R_L)$$

$$= g_m (v_i - v_o) \left( \frac{r_d R_L}{r_d + R_L} \right)$$

$$= g_m (v_i - v_o) r_d \quad (\because r_d \ll R_L)$$

$$v_o = \mu (v_i - v_o)$$

$$v_o + \mu v_o = \mu v_i$$

$$\frac{v_o}{v_i} = \frac{\mu}{1 + \mu} \approx 1$$



FEEDBACK AMPLIFIERS

①

INTRODUCTION:

- \* Any System whether it is electrical, mechanical, hydraulic or pneumatic may be considered to have atleast one input and one output. If the system is to perform smoothly, we must be able to measure or control output.
- \* For example if the input is  $10\text{mV}$ , gain of the amplifier is 100, output will be  $1\text{V}$ . If the input deviates to  $9\text{mV}$  or  $11\text{mV}$ , output will be  $0.9\text{V}$  or  $1.1\text{V}$ . So there is no control over the output.
- \* But by introducing feedback between the output and input, there can be control over the output. If the input is increased, it can be made easy to increase by having a link between the output and input. So that input can be made to depend on output.
- \* Some examples for are
  1. Temperature of a Furnace
  2. Traffic light
  3. Our human eyes and mind.
- \* An amplifier is a device that amplifies the input signal, when we talk about ideal amplifier, there exist some parameters like Voltage gain, Input impedance, output impedance and Bandwidth.

### CIRCUIT DIAGRAM:

(3)

\* This circuit is a Two-port Network and it represents an Amplifier.

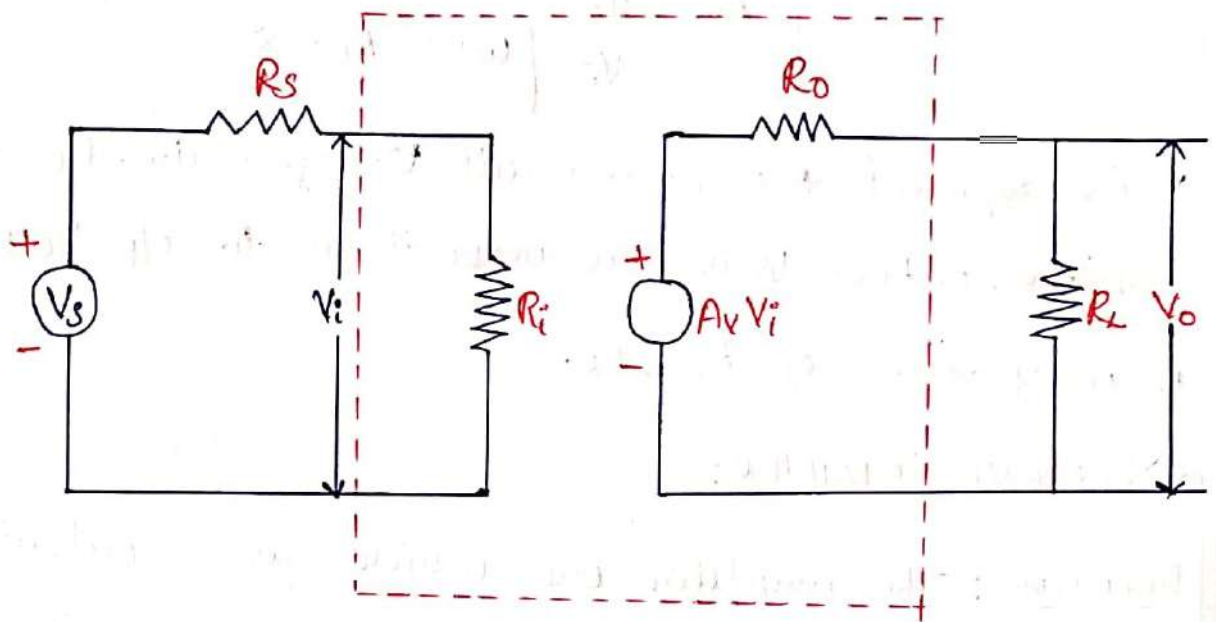


FIG: EQUIVALENT CIRCUIT OF VOLTAGE AMPLIFIERS

\* The Voltage amplifier can be designed with the help of thevenin equivalent circuit on both sides.

\* In the above fig, the amplifier i/p resistance  $R_i$  is large when compared to source resistance ( $R_s$ ) i.e.,  $R_i \gg R_s$ , so that drop across  $R_s$  is very small.

$$R_i \gg R_s, \quad V_i \approx V_s$$

\* Similarly load resistance  $R_L$  is large compared to the o/p resistance  $R_o$  of the amplifier.

$$\text{If } R_L \gg R_o, \quad V_o = A_v V_i$$

$$\boxed{V_o = A_v V_s} \quad \because V_i = V_s$$

$\therefore$  The Output Voltage is proportional to the Input Voltage.



∴ For Ideal Voltage Amplifier

$$R_i = \infty, R_o = 0$$

①

$$V_o = A_v V_i$$

$$A_v = \frac{V_o}{V_i} \text{ with } R_L = \infty$$

\*  $A_v$  represent the open circuit Voltage gain. For ideal voltage amplifier, output Voltage is proportional to i/p Voltage and is independent of  $R_s$  &  $R_L$ .

## 2) CURRENT AMPLIFIER:

Definition: The amplifier one which gives output current proportional to input current and the proportionality factor is independent of  $R_s$  &  $R_L$  then it is called as "Current Amplifier".

\* Current amplifier can be designed with the help of Norton's equivalent circuit on both sides.

### CIRCUIT DIAGRAM:

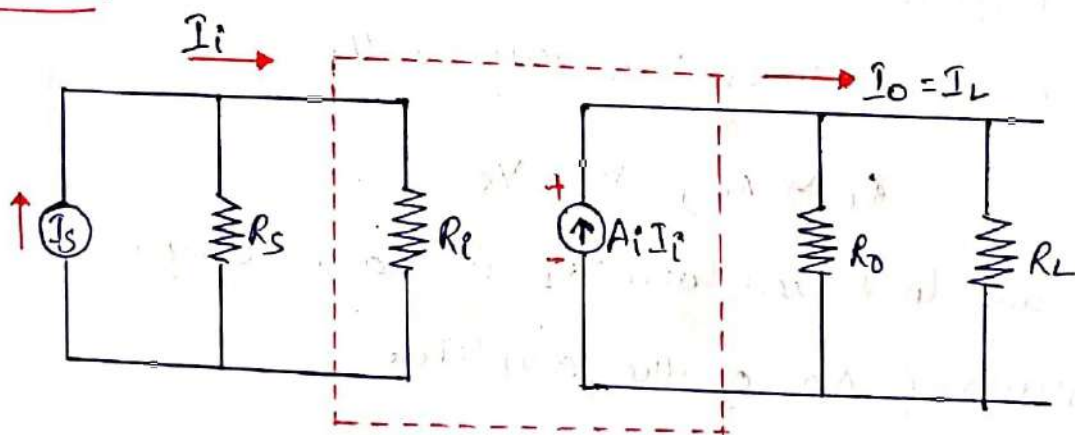


FIG: NORTON'S EQUIVALENT CIRCUIT OF CURRENT AMPLIFIER

\* In the above figure, the amplifier input resistance  $R_i$  is less when compare to  $R_s$ . So we get  $I_i = I_s$  ∴  $R_i \ll R_s$

\* Current amplifiers intend to amplify input current signal and provide output current signal. ⑤

\* If  $R_s \gg R_i$ , then  $I_i = I_s$ .

\* Similarly if  $R_o \gg R_L$ , then  $I_o = A_I I_i$ .

$$I_o = A_I I_s \quad \because I_i = I_s$$

\* For Ideal current amplifier,  $R_i = 0$ ,  $R_o = \infty$ .

$$\text{If } R_i = 0, \quad I_s \approx I_i$$

$$R_o = \infty \quad I_L = I_o = A_I I_i = A_I I_s$$

$$A_I = \frac{I_L}{I_i}$$

\*  $A_I$  represents the short circuit current amplification.

### 3) TRANSCONDUCTANCE AMPLIFIER:

DEFINITION: The Amplifier which supplies output current which is proportional to input voltage independently of the magnitude of  $R_s$  &  $R_L$  then such amplifier is called as "Transconductance Amplifier".

\* In Transconductance amplifier, the input signal is a voltage signal and o/p signal is a current signal.

\* Transconductance amplifier can be designed with the help of Thevenin's theorem at i/p side and Norton's theorem at o/p side.



### CIRCUIT DIAGRAM:

(6)

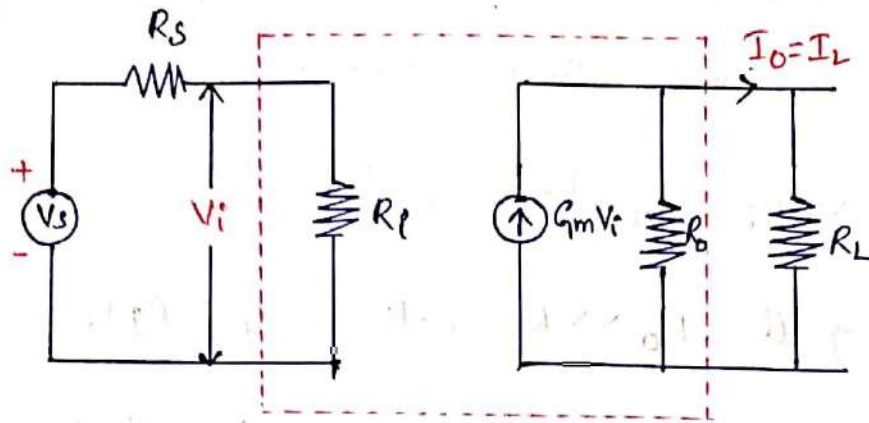


FIG: EQUIVALENT CIRCUIT FOR TRANS CONDUCTANCE AMPLIFIER.

\* In the above fig, the amplifier i/p resistance  $R_i$  is high compare to  $R_s$ , So we get  $V_i = V_s$

\* Similarly  $R_o$  is high compare to  $R_L$ , So we get,

$$I_o = g_m V_i$$

$$\therefore V_i = V_s \quad I_o = g_m V_s$$

\* Therefore, the o/p current is directly proportional to i/p Voltage and transconductance factor ( $g_m$ ) doesn't depend on  $R_L$  &  $R_s$ .

\* For ideal Trans conductance amplifier  $R_i = R_o = \infty$

### 4> TRANS RESISTANCE AMPLIFIER:

Definition: The amplifier which supplies o/p Voltage, which is proportional to i/p current and independent of  $R_s$  &  $R_L$  then such amplifier is called as "Trans resistance amplifier".

\* In Trans resistance amplifier the i/p signal is a current signal and o/p signal is a voltage signal.

\* Trans resistance amplifier can be designed with the help of Norton's theorem at i/p side and Thevenin's theorem at o/p side.

## CIRCUIT DIAGRAM:

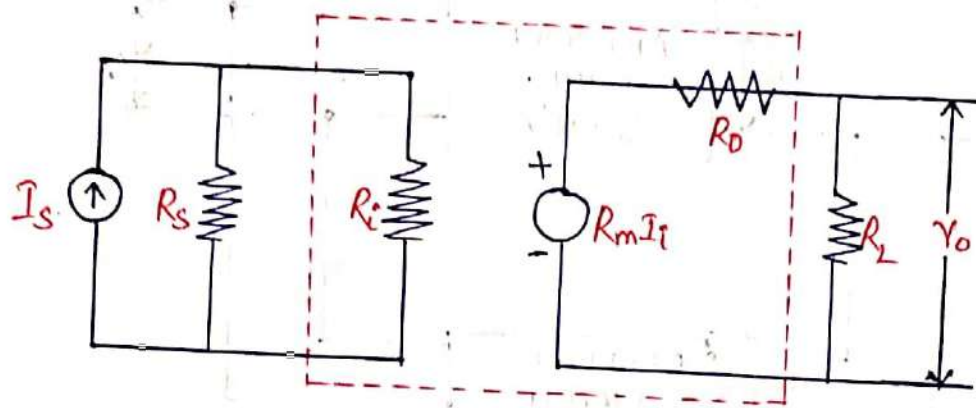


FIG: EQUIVALENT CIRCUIT FOR TRANS RESISTANCE AMPLIFIER.

\* In the above figure, the amplifier i/p resistance  $R_i$  is very less compare to  $R_s$  So we get  $I_i = I_s$ .  
( $R_i \ll R_s$ )

\* Similarly ' $R_o$ ' is very less compare to ' $R_L$ ' So, we get  $V_o = R_m I_i$ .  
( $R_o \ll R_L$ )  $\therefore I_i = I_s$   $V_o = R_m I_s$

\* Therefore, the O/P Voltage is directly proportional to i/p current and transresistance factor ' $R_m$ ' doesn't depend on  $R_L$  &  $R_s$ .

\* For ideal Trans resistance  $R_o = R_i = 0$

## FEEDBACK PRINCIPLE:

Definition: The process of Combining a fraction of output energy (Voltage or current) back to the input is known as "Feedback".

\* The amplifier which provides feedback is called as Feedback amplifiers.

\* The feedback circuit amplifier has two parts  
1. Amplifier  
2. Feedback Network



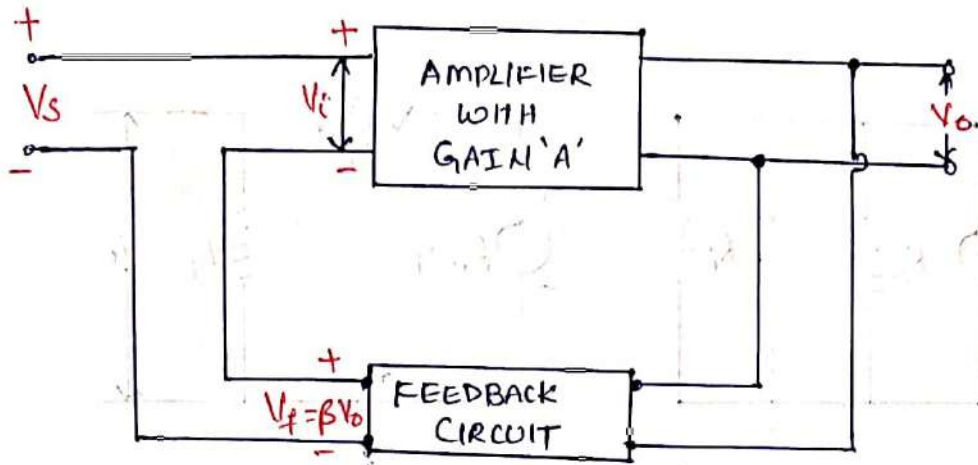


FIG: PRINCIPLE OF FEEDBACK AMPLIFIER.

\* Let 'A' be the gain of the amplifier without feedback

$$A = \frac{V_o}{V_i}$$

\* The feedback network extracts a voltage  $V_f = \beta V_o$  from the o/p  $V_o$  of the amplifier.

$$V_i = V_s + V_f = V_s + \beta V_o \quad (\text{positive feedback}) \quad (\because V_f = \beta V_o)$$

$$V_i = V_s - V_f = V_s - \beta V_o \quad (\text{Negative feedback})$$

$$\text{i.e., } V_i = V_s \pm V_f$$

\* The quantity  $\beta = \frac{V_f}{V_o}$  is called feedback ratio & feedback fraction

$$A = \frac{V_o}{V_i}$$

$$A = \frac{V_o}{V_s \pm V_f}$$

$$V_o = A (V_s \pm V_f)$$

$$V_o = A (V_s \pm \beta V_o)$$

$$V_o = A V_s \pm \beta A V_o$$

(9)

$$V_o \mp \beta A V_o = A V_s$$

$$V_o (1 \mp \beta A) = A V_s$$

$$\frac{V_o}{V_s} = \frac{A}{1 \mp \beta A} \rightarrow \textcircled{1}$$

$$\boxed{A_{\text{eff}} = \frac{A}{1 \mp \beta A}}$$

where  $A_{\text{eff}}$  is the overall gain (gain with feedback)

$A$  is the open loop gain (gain without feedback)

$\beta$  is the feedback ratio or feedback factor.

$\Rightarrow$  The ratio of output voltage  $V_o$  to the applied signal voltage  $V_s$  is called overall gain i.e., gain with feedback  $A_{\text{eff}}$ .

$$A_{\text{eff}} = \frac{\text{Output Voltage}}{\text{Input Signal Voltage}} = \frac{V_o}{V_s} \rightarrow \textcircled{2}$$

from eq ① & ②  $A_{\text{eff}} = \frac{A}{1 \mp \beta A}$

$$\text{i.e., } \boxed{\begin{aligned} A_{\text{eff}} &= \frac{A}{1 + \beta A} \text{ for Negative feedback} \\ A_{\text{eff}} &= \frac{A}{1 - \beta A} \text{ for positive feedback.} \end{aligned}}$$



\* Feedback means the o/p signal is coupled to the Input of the same circuit. This feedback signal provides the control element of the System.

Eg: 1. Temperature of device  
2. Human mind & eyes.

### TYPES OF FEEDBACK:

\* There are two types of Feedback.

1. Positive feedback
2. Negative Feedback.

### POSITIVE FEED BACK:

\* If the feedback signal  $V_f$  is in phase with the input signal  $V_s$ , then the net  $V_i = V_s + V_f$ . Hence, the input voltage applied to the basic amplifier is increased, thereby increasing the  $V_o$  exponentially. This type of feedback is said to be positive or regenerative feedback.

\* Gain of the amplifier with positive feedback is,

$$A_f = \frac{A}{1 - A\beta}$$

\* Since positive feedback causes excessive distortion and instability, it is rarely used in amplifier circuits.

\* However, because of its capability of increasing the power of increasing the original signal it is used in Oscillator circuit.

## NEGATIVE FEEDBACK:

(11)

\* If the feedback signal,  $V_f$  is out of phase with the input signal  $V_s$  then such feedback is known as Negative feedback or Degenerative feedback.

\* Then,  $V_i = V_s - V_f$ , So the input voltage applied to the basic amplifier is decreased and correspondingly the o/p is decreased.

\* Hence the voltage gain is reduced. Gain of the amplifier with negative feedback is,

$$A_f = \frac{A}{1 + A\beta}$$

\* The Negative feedback has various advantages like,

- Gain Stability
  - Reduction in Non-linear distortion
  - Reduction in Noise.
  - Increase in Bandwidth or Improvement in frequency response.
  - Increase in Input Impedance.
  - Decrease in Output Impedance.
- \* Because of its numerous advantages it is widely used in amplifier circuits.

\* The drawback in Negative feedback is it reduces overall gain of the amplifier, this problem can be compensated by increasing the number of stages in amplifier circuits.



## CONCEPT OF FEEDBACK:

(12)

\* The below figure represents the block diagram of an amplifier with feedback.

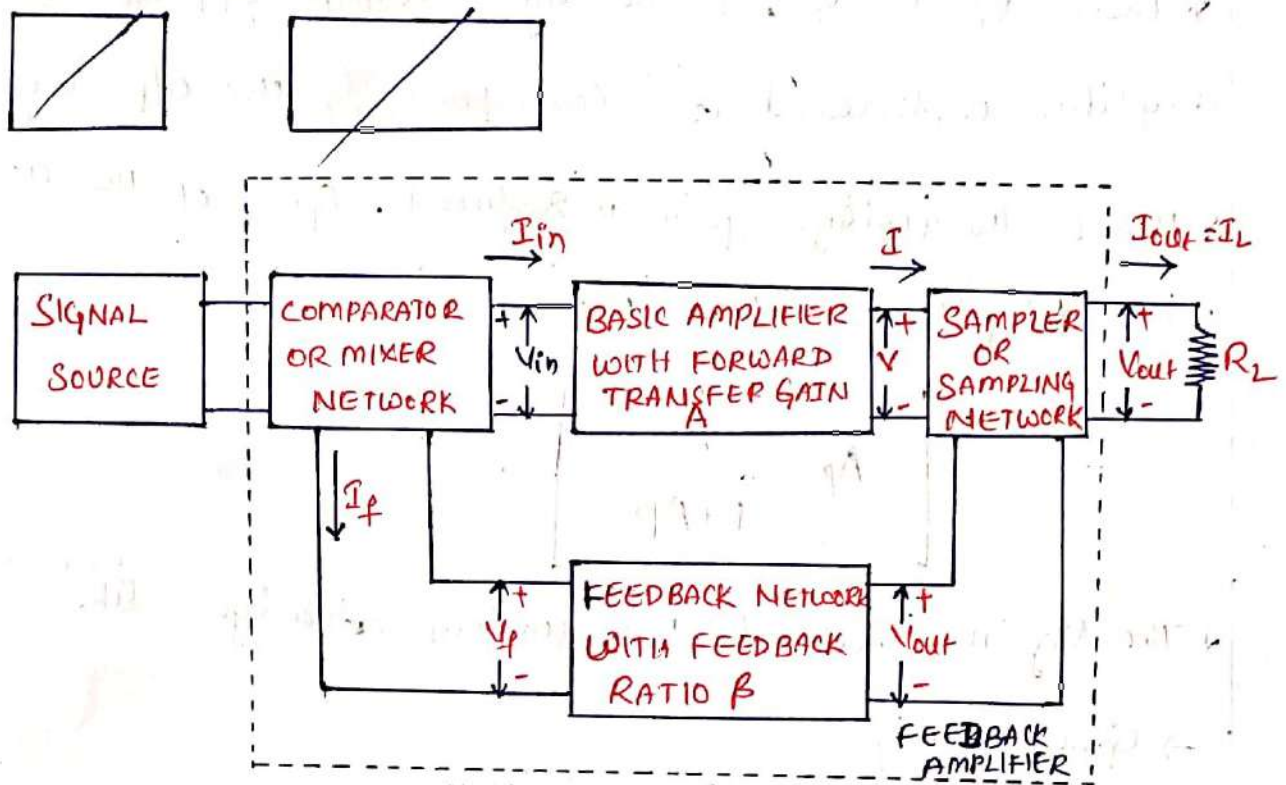


FIG: BLOCK DIAGRAM OF AN AMPLIFIER WITH FEEDBACK

\* The output quantity (either Voltage or Current) is sampled by means of a suitable sampling Network (or Sampler) and is fed to the feedback network.

\* The output of feedback network, which has a fraction of o/p signal is combined with the external (source) signal through a mixer network and fed into the basic amplifier.

\* The different blocks of feedback amplifier are explained below.

1. SIGNAL SOURCE: This block is either a signal Voltage  $V_s$  in series with a resistor  $R_s$  or a signal current  $I_s$  in parallel with a resistor  $R_s$ .

## TRANSFER RATIO (G) GAIN:

(13)

\* The symbol  $A$  in the basic amplifier represents the ratio of the output signal to the input signal.

\* The transfer ratio  $A_V = \frac{V_o}{V_i}$  referred to as Voltage gain

The transfer ratio  $A_I = \frac{I_o}{I_i}$  referred to as Current gain

The transfer ratio  $G_m = \frac{I_o}{V_i}$  referred to as transconductance.

The transfer ratio  $R_m = \frac{V_o}{I_i}$  referred to as transresistance.

\* These are the transfer gains of the basic amplifier without feedback and is represented by symbol  $A$ .

\* The symbol  $A_f$  is defined as the ratio of the o/p signal to the input signal of the amplifier configuration.

\*  $A_f$  is the transfer gain of the amplifier with feedback.

$$A_{Vf} = \frac{V_o}{V_s}, \quad A_{If} = \frac{I_o}{I_s}, \quad R_{mf} = \frac{V_o}{I_s}, \quad G_{mf} = \frac{I_o}{V_s}$$

## GENERAL STRUCTURE OF SINGLE LOOP FEEDBACK AMPLIFIER:

\* The below figure represents the signal-flow diagram of a feedback amplifier in which quantity "X" represents either Voltage & Current signals.

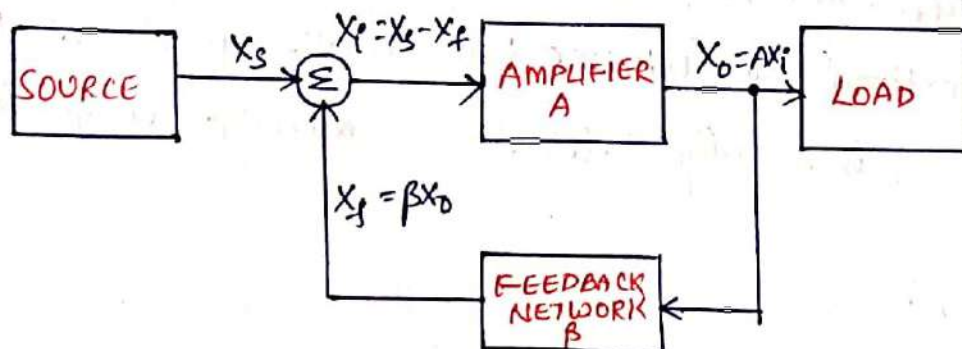


FIG: GENERAL STRUCTURE OF SINGLE LOOP FEEDBACK AMPLIFIER

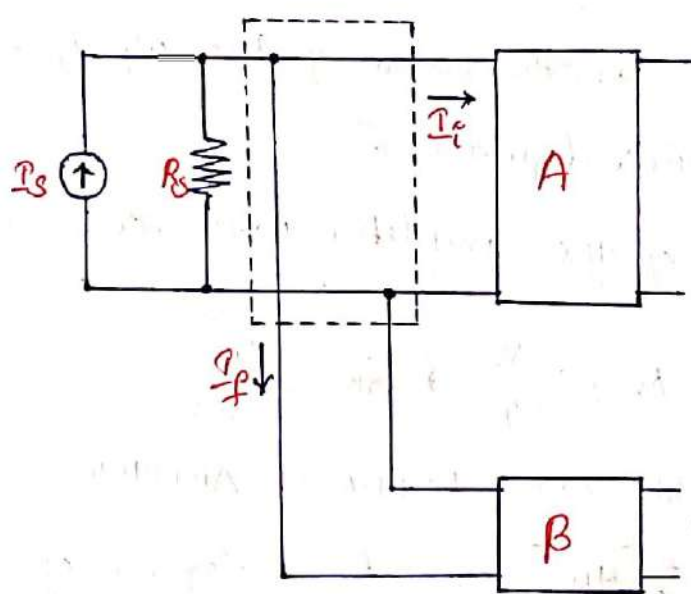


\* This is because in this case, feedback signal is returned to the input in series to oppose the applied voltage causing input current to fall and consequently makes the input impedance to increase.

## 2. SHUNT MIXER:

\* The connecting of feedback signal in parallel with an input current source is known as "Shunt mixer".

\* In case of shunt or parallel connections, the current drawn from the signal source is increased by an amount equal to feedback current  $I_f$  and therefore, input impedance falls.



→ Voltage f/b tends to increase  
reduce the o/p impedance  
→ Current f/b tends to  
increase the o/p impedance.

FIG: SHUNT MIXER

MIXER: A differential Amplifier, which has two inputs and one output proportional to difference between the signals at the two inputs, is usually referred as a mixer or Comparator.

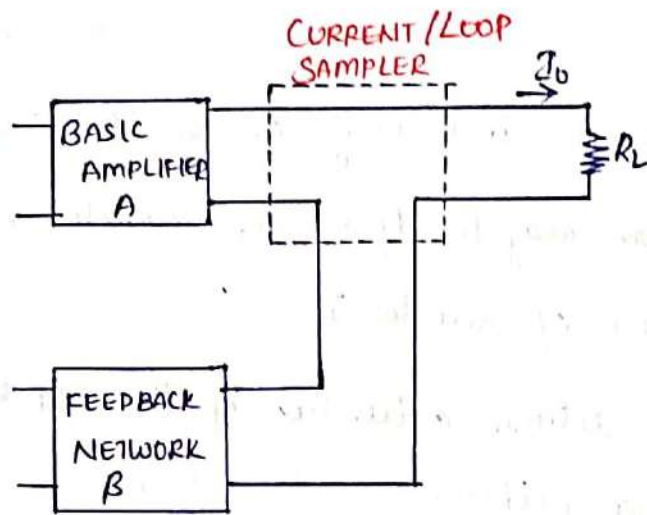


FIG: CURRENT (OR) LOOP SAMPLING

#### 4. MIXER NETWORK:

- \* Like sampling, there are two ways of mixing the feedback signal. Mixer is also known as comparator.
- \* Mixer is of two types
  1. Series mixer
  2. Shunt mixer

#### SERIES MIXER:

- \* The connection of feedback signal in series with the input signal voltage is known as "Series mixer".
- \* Series feedback connection tends to increase the input impedance of the amplifier.

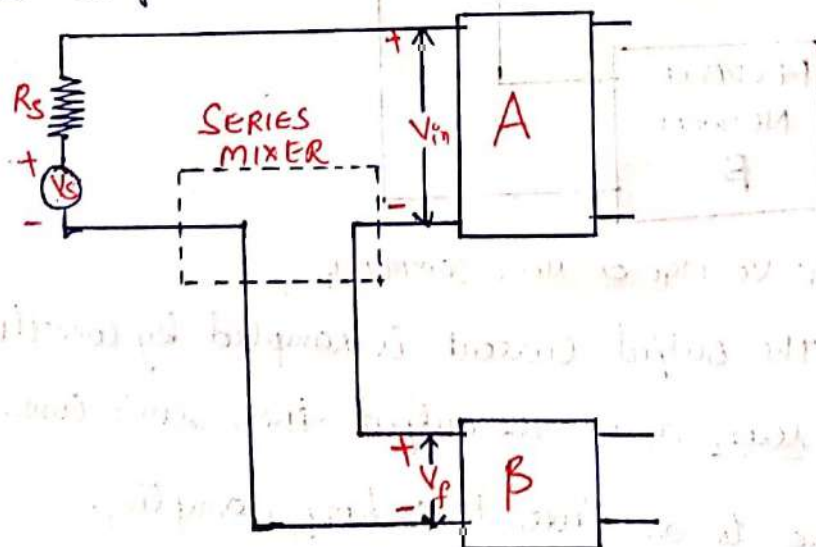


FIG: SERIES MIXING



## 2. FEEDBACK NETWORK:

(16)

\* The feedback network is usually in the form of a passive two-port network and may be formed of: resistors, inductors and capacitors (most often of resistors).

\* Its function is to return a fraction of the o/p energy (Voltage or current) to the input of the amplifier.

## 3. SAMPLING NETWORK:

\* Sampling Networks are of two types

1. Voltage Sampling

2. Current Sampling

1. VOLTAGE SAMPLING: The output voltage is sampled by connecting the feedback network in shunt across the output then such connection at the output is referred to as Voltage or node sampling.

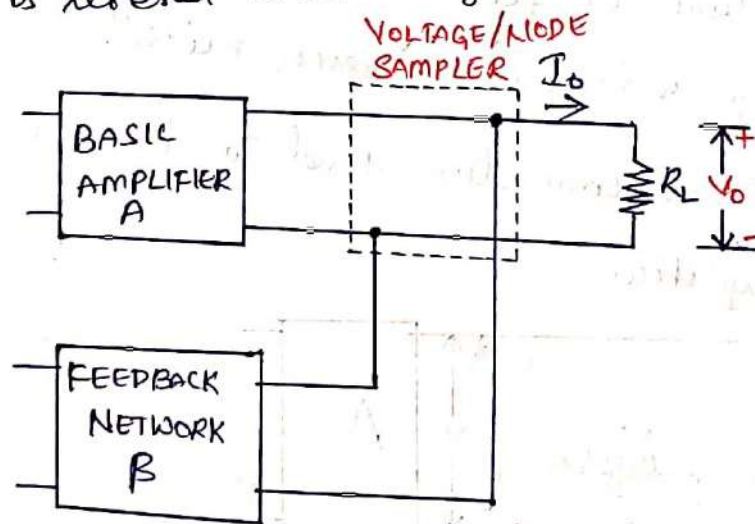


FIG: VOLTAGE(OR) NODE SAMPLING

2. CURRENT SAMPLING: The output current is sampled by connecting the feedback network in series across the output. then such connection at the output is referred to as Current or loop sampling.

\* When the feedback signal  $X_f$  and the input signal  $X_i$  are out of phase, then the feedback is called negative feedback. (15)

From the above figure, we have

$$X_i = X_s - X_f = X_d$$

\* Where  $X_d$  represents the difference between the applied input signal  $X_s$  and feedback signal  $X_f$  and it is called the error signal or comparison signal.

\* The feedback factor  $\beta = \frac{X_f}{X_o}$

Where  $X_o$  is the output voltage/current

\* The transfer gain without feedback  $A$  is defined by

$$A = \frac{X_o}{X_i}$$

\* The transfer gain with feedback  $A_f$  is defined by,

$$A_f = \frac{X_o}{X_s} = \frac{X_o}{X_i + X_f} \quad \left( \because X_i = X_s - X_f \right)$$

$$X_s = X_i + X_f$$

$$= \frac{X_o}{X_i + \beta X_o}$$

$$\left( \because \beta = \frac{X_f}{X_o} \right)$$

$$X_f = \beta X_o$$

$$= \frac{X_o}{X_i \left( 1 + \beta \frac{X_o}{X_i} \right)}$$

$$= \frac{X_o}{X_i} \cdot \frac{1}{1 + \beta A_f / X_i}$$



$$A_f = A \cdot \frac{1}{1 + \beta A}$$

$$(\because A = \frac{x_o}{x_i})$$

$$A_f = \frac{A}{1 + \beta A}$$

### CLASSIFICATION OF FEEDBACK AMPLIFIERS :

\* Based on the type of sampling at the output side and the type of mixing to the input side, feedback amplifiers are classified into four topologies.

1. Voltage-Series feedback or Series Shunt feedback
2. Current-Series feedback or Series Series feedback
3. Current-Shunt feedback or Shunt Series feedback
4. Voltage-Shunt feedback

\* If the feedback signal is connected in series with the i/p signal then it is called as Series feedback amplifier.

\* If the feedback signal is connected in shunt with the i/p signal then it is called as Shunt feedback amplifier.

### VOLTAGE SERIES FEEDBACK AMPLIFIER:

\* If the <sup>Feedback o/p</sup> Input signal is connected in series with the i/p signal then it is called as Voltage series feedback amplifier.

\* If Voltage is sampled and mixing is in series then the type of feedback is known as Voltage Series feedback.

\* The block diagram of Voltage series feedback amplifier is as shown below. (19)

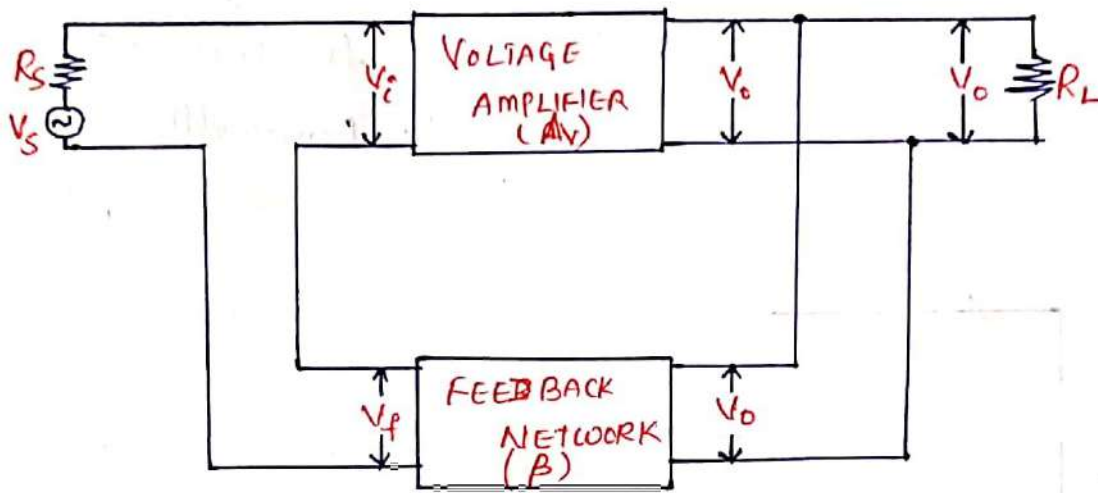


FIG: BLOCK DIAGRAM OF VOLTAGE SERIES FEEDBACK AMPLIFIER.

\* Here Basic amplifier is a Voltage Amplifier

\* At Input side Voltages are mixed by means of series feedback

$V_s \rightarrow$  Source Signal

$V_i \rightarrow$  Input Signal

$V_f \rightarrow$  feedback signal

From the figure  $V_i = V_s - V_f$

$$V_s = V_i + V_f$$

\* At output side Voltage is sampled by using shunt sampling

$V_o \rightarrow$  output signal.

\* Voltage Series feedback amplifier is also known as Shunt-Series feedback amplifier



- \* The circuit diagram for Voltage-Series feedback amplifier to (2)
- derive amplifier parameters such as,
1. Input Impedance ( $R_i$ )
  2. Output Impedance ( $R_o$ )
  3. Voltage gain with feedback ( $A_{vf}$ )
  4. Bandwidth

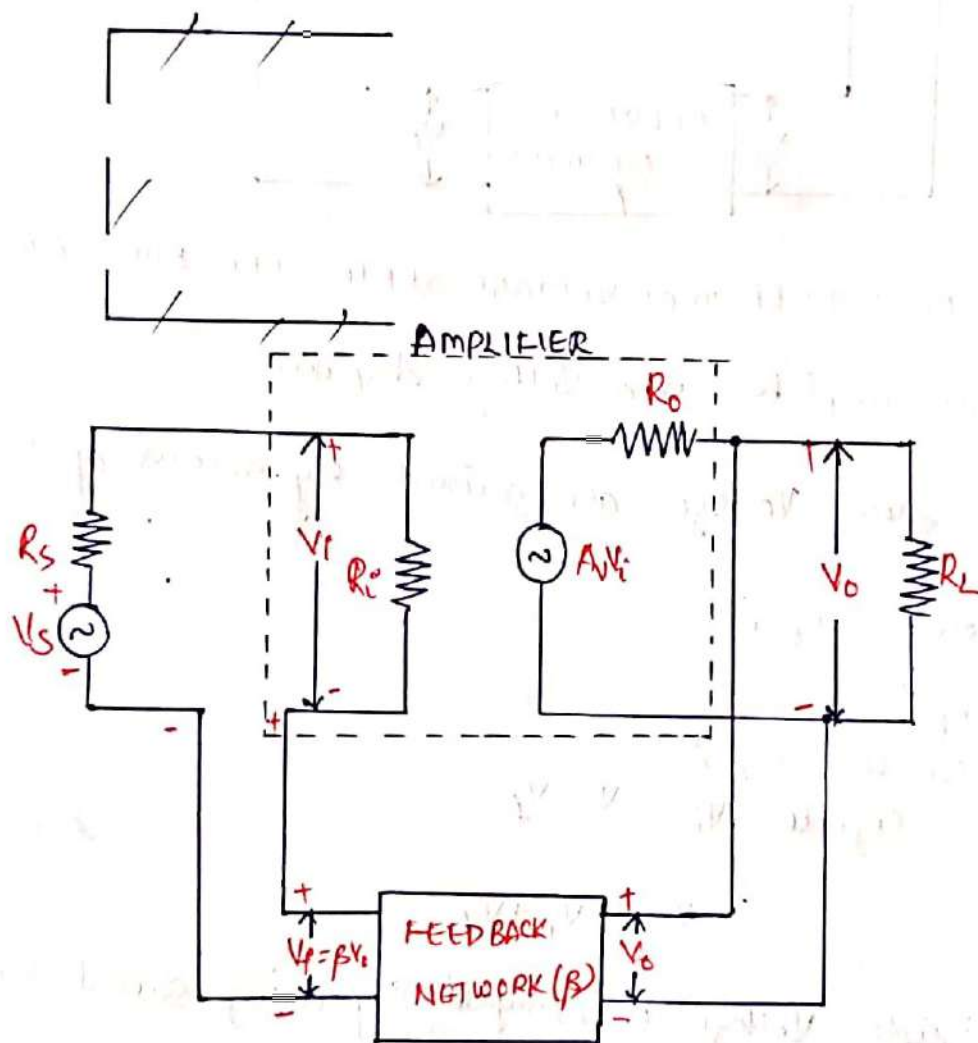


FIG: CIRCUIT DIAGRAM FOR VOLTAGE SERIES FEEDBACK AMPLIFIER.

⇒ By definition, open loop voltage gain (or) gain without feedback

$$\text{is } A_v = \frac{V_o}{V_i} \rightarrow (1)$$

$$\text{feedback factor } \beta = \frac{V_f}{V_o} \rightarrow (2)$$

\* closed loop Voltage gain (or) Voltage gain with feedback is,

$$A_{vf} = \frac{V_o}{V_s} \rightarrow (1)$$

### Voltage Gain with Feedback:

\* The ratio of output Voltage to source Voltage is defined as Voltage gain with feedback.

$$A_{vf} = \frac{V_o}{V_s} \rightarrow (1)$$

$$\text{we know that } V_s = V_i + V_{vf} \rightarrow (2)$$

Substitute eq. (2) in eq. (1)

$$\begin{aligned} A_{vf} &= \frac{V_o}{V_i + V_{vf}} \\ &= \frac{V_o}{V_i + \beta V_o} \quad (\because V_{vf} = \beta V_o) \end{aligned}$$

$$= \frac{V_o}{V_i \left[ 1 + \beta \frac{V_o}{V_i} \right]}$$

$$= \frac{V_o / V_i}{1 + \beta \frac{V_o}{V_i}}$$

$$= \frac{A_v}{1 + A_v \beta} \quad (\because A_v = \frac{V_o}{V_i})$$

$\therefore$  Voltage gain with feedback  $A_{vf} = \frac{A_v}{1 + A_v \beta} \rightarrow A_{vf} < A_v$



## BANDWIDTH WITH FEEDBACK:

(22)

\* If Voltage gain with feedback decreases, it increases the bandwidth with feedback.

\* This is due to the product of gain & Bandwidth always be a constant

$$\therefore \text{Bandwidth with feedback (B.W)}_f = BW(1 + A_V \beta) \Rightarrow (B.W)_f > B.W$$

## INPUT RESISTANCE WITH FEEDBACK:

Definition: The ratio of Input Voltage to Input Current is defined as Input resistance without feedback

\* Input resistance without feedback  $R_i^o = \frac{V_i}{I_i}$

\* Input resistance with feedback is defined as the ratio of Source Voltage to the Input current.

$$R_{if} = \frac{V_s}{I_i}$$

We know that  $V_s = V_i + V_f$

$$R_{if} = \frac{V_i + V_f}{I_i}$$

$$= \frac{V_i + \beta V_o}{I_i}$$

$$= \frac{V_i + \beta (A_V V_i)}{I_i}$$

$$\because A_V = \frac{V_o}{V_i} \\ V_o = A_V V_i$$

$$= \frac{V_i [1 + \beta A_v]}{I_i} = \frac{V_i}{I_i} [1 + A_v \beta]$$

$$= R_i [1 + A_v \beta] \quad (\because R_i = \frac{V_i}{I_i})$$

$\therefore$  Input Resistance with feedback  $R_{if} = R_i (1 + A_v \beta) \Rightarrow R_{if} > R_i$

\* High Input Impedance is always desirable in an amplifier. Such a desirable characteristic can be achieved with the help of Negative feedback.

### OUTPUT RESISTANCE WITH FEEDBACK:

\* Just as High Input Impedance is advantageous to an amplifier,

Similarly low Output Impedance is desirable.

\* With lower output Impedance, the amplifier is better suited to drive a low impedance load.

\* The input terminals are short-circuited i.e.,  $V_s = 0$

$$V_s = V_i + V_f$$

$$0 = V_i + V_f$$

$$V_i = -V_f$$

\* When  $V_s = 0$ ,  $-V_f$  is the only input voltage to the amplifier

$$R_{of} = \left. \frac{V_o}{I_o} \right|_{V_s=0}$$

$$V_i = V_s - V_f = 0 - \beta V_o$$



Apply KVL to the output side,

(24)

we have  $V_o = I_o R_o + A_v V_i$

$$V_o = I_o R_o + A_v (-\beta V_o)$$

$$V_o + A_v \beta V_o = I_o R_o$$

$$V_o (1 + A_v \beta) = I_o R_o$$

$$\frac{V_o}{I_o} = \frac{R_o}{1 + A_v \beta}$$

$$\therefore R_{of} = \frac{R_o}{1 + A_v \beta} \quad (R_{of} \ll R_o)$$

\* When the  $A_v$  increases, output impedance decreases and vice versa.

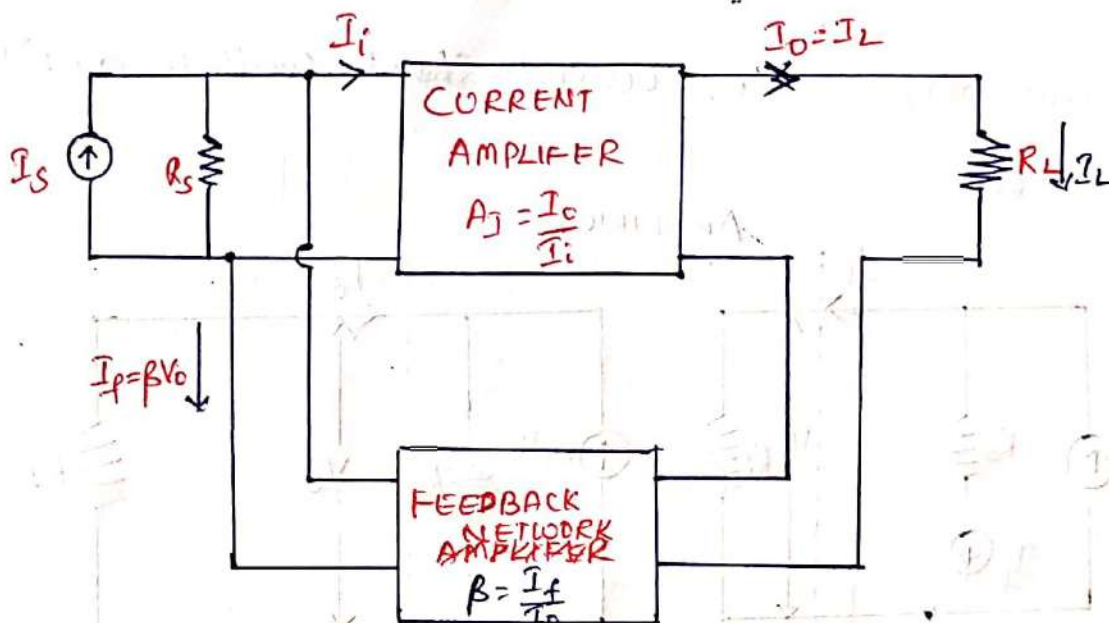
For Voltage Series Feedback amplifier

1. Gain with feedback  $A_{vf} = \frac{A_v}{1 + A_v \beta}$  (Decreases)
2. Band<sup>width</sup> with feedback  $(B.W)_f = B.W (1 + A_v \beta)$  (Increases)
3. Input resistance with feedback  $R_{if} = R_i (1 + A_v \beta)$  (increases)
4. Output resistance with feedback  $R_{of} = \frac{R_o}{1 + A_v \beta}$  (decreases)

## 2. CURRENT SHUNT FEEDBACK AMPLIFIER:

(25)

\* The Block diagram for Current-shunt feedback amplifier, is as shown below.



BLOCK DIAGRAM OF  
FIG: CURRENT SHUNT FEEDBACK AMPLIFIER.

\* In Current-shunt feedback amplifier, the feedback signal is in parallel with the Input signal.

\* This is also known as Shunt-series Feedback Amplifier

\* The Current-shunt feedback amplifier works as a true current amplifier as the input signal is a current and the output signal is a current.

Basic Amplifier: Current amplifier

- At input side currents are mixed by means of Shunt feedback.
- At output side currents are sampled by means of series sampling current



$I_s \rightarrow$  Source signal

$I_i \rightarrow$  Input signal

$I_f \rightarrow$  Feedback signal.

\* The circuit diagram for current-shunt feedback amplifier is as shown below.

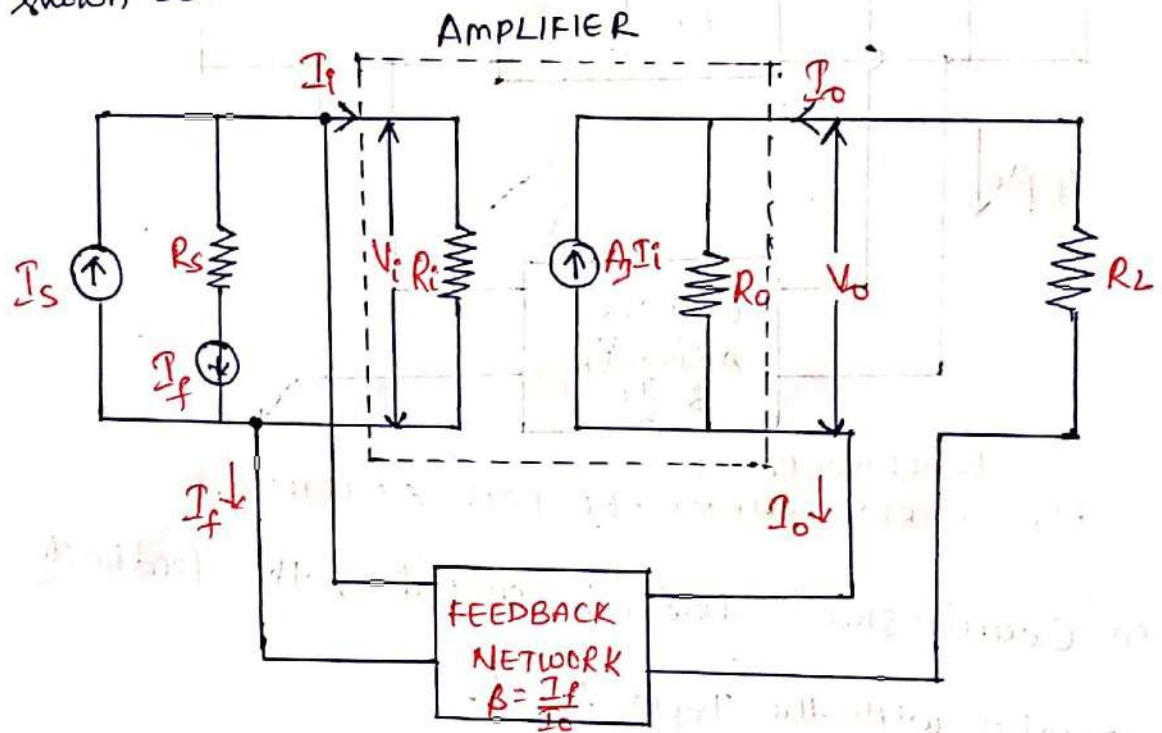


FIG: CIRCUIT DIAGRAM FOR CURRENT-SHUNT FEEDBACK AMPLIFIER.

\* By definition

$\rightarrow$  open loop current gain ( $\oslash$ ) current gain without feedback

$$A_i = \frac{I_o}{I_i}$$

Feedback factor  $\beta = \frac{I_f}{I_o}$

$\rightarrow$  Closed loop current gain ( $\oslash$ ) current gain with feedback

$$A_{if} = \frac{I_o}{I_s}$$

## CURRENT GAIN WITH FEEDBACK: ( $A_{I_f}$ )

(27)

Definition: The ratio of output current  $I_o$  to the Source Current  $I_s$  is called as current gain with feedback.

$$A_{I_f} = \frac{I_o}{I_s}$$

We know that  $I_i = I_s - I_f$

$$I_s = I_i + I_f$$

$$\text{then } A_{I_f} = \frac{I_o}{I_i + I_f}$$

$$= \frac{I_o}{I_i + \beta I_o} \quad (\because I_f = \beta I_o)$$

$$A = \frac{I_o}{I_i + \beta I_o} \quad (\because A_I = \frac{I_o}{I_i})$$

$$= \frac{I_o}{I_i \left( 1 + \beta \frac{I_o}{I_i} \right)} \quad I_o = A_I I_i$$

$$= \frac{I_o / I_i}{1 + \beta \frac{I_o}{I_i}} = \frac{A_I}{1 + A_I \beta}$$

$$\therefore \text{Current gain with feedback } A_{I_f} = \frac{A_I}{1 + \beta A_I} \quad A_{I_f} < A_I$$

\* Current gain with feedback decreases.



## BANDWIDTH WITH FEEDBACK:

(28)

\* As current gain with feedback decreases, Bandwidth with feedback should increase.

\* Because gain  $\times$  Bandwidth product must be always constant

$$\text{Bandwidth with feedback } (B.W)_f = B.W(1 + A\beta) \quad B.W_f > B.W$$

## INPUT RESISTANCE WITH FEEDBACK:

\* By definition Input resistance without feedback is,

$$R_i = \frac{V_i}{I_i}$$

\* Input resistance with feedback is,

$$R_{if} = \frac{V_i}{I_s}$$

$$\text{we know that } I_s = I_i + I_f$$

$$\text{then } R_{if} = \frac{V_i}{I_i + I_f} = \frac{V_i}{I_i + \beta I_o}$$

$$= \frac{V_i}{I_i [1 + \beta A_o]}$$

$$= \frac{V_i}{I_i [1 + \beta A\beta I_i]}$$

$$= \frac{V_i}{I_i [1 + \beta A\beta]} = \frac{V_i / I_i}{1 + \beta A\beta} = \frac{R_i}{1 + \beta A\beta}$$

$$\begin{aligned} \because A_o &= \frac{I_o}{I_i} \\ I_o &= A\beta I_i \end{aligned}$$

$$\therefore \text{Input resistance with feedback } R_{if} = \frac{R_i}{1 + \beta A\beta} \quad R_{if} < R_i$$

\* When the negative feedback signal is fed back to the input in shunt with the applied signal, the input resistance is decreased. (29)

### OUTPUT RESISTANCE WITH FEEDBACK:

\* In current shunt feedback amplifier, at the output side current sampling is done, it tends to increase the output resistance with feedback.

\* By open circuiting the current source  $I_S$ ,  $-I_f$  is the only i/p to the amplifier

$$R_{of} = \frac{V_o}{I_o} \bigg|_{\text{with } I_S = 0}$$

$$I_i = I_S - I_f = 0 - I_f = -I_f$$

Apply KCL to the o/p loop.

$$I_o = \frac{V_o}{R_o} + A_v I_i$$

$$I_o = \frac{V_o}{R_o} + A_v (-I_f)$$

$$I_o + A_v \beta I_o = \frac{V_o}{R_o}$$

$$I_o (1 + \beta A_v) = \frac{V_o}{R_o}$$

$$R_o = \frac{V_o}{I_o (1 + \beta A_v)}$$

$$\frac{V_o}{I_o} = R_o (1 + \beta A_v)$$



∴ output resistance with feedback  $R_{of} = R_o(1+A_f\beta) \Rightarrow R_{of} > R_o$

(3)

∴ For Current-Shunt feedback Amplifier

→ Current gain with feedback  $A_{if} = \frac{A_i}{1+A_i\beta}$  (decreases)

→ Bandwidth with feedback  $(B.W)_f = B.W(1+A_i\beta)$  [Increases]

→ Input resistance with feedback  $R_{if} = \frac{R_i}{1+A_i\beta}$  [decreases]

→ Output resistance with feedback  $R_{of} = R_o(1+A_i\beta)$  [Increases]

### VOLTAGE-SHUNT FEEDBACK AMPLIFIER:

\* The Block diagram for Voltage-shunt feedback amplifier is as shown below.

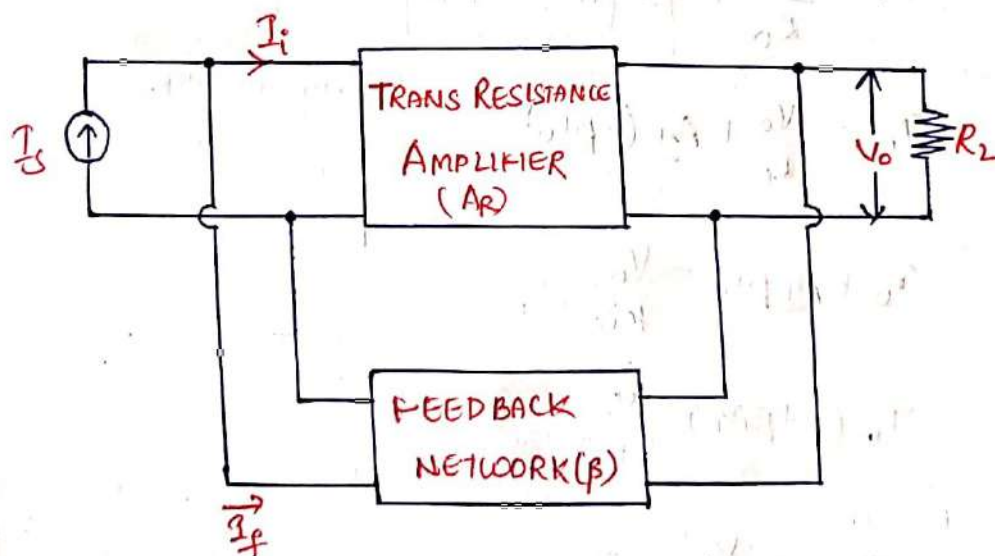


FIG: BLOCK DIAGRAM FOR VOLTAGE-SHUNT FEEDBACK

\* In Voltage-shunt feedback, the feedback signal is in parallel with Input signal. At the output side feedback signal is in parallel with output signal.

\* It is also called a shunt-derived, shunt-fed feedback connection. (3)

\* Here, a fraction of the output voltage is supplied in parallel with the input voltage through the feedback network.

\* The feedback signal  $I_f$  is proportional to the output voltage  $V_o$ .

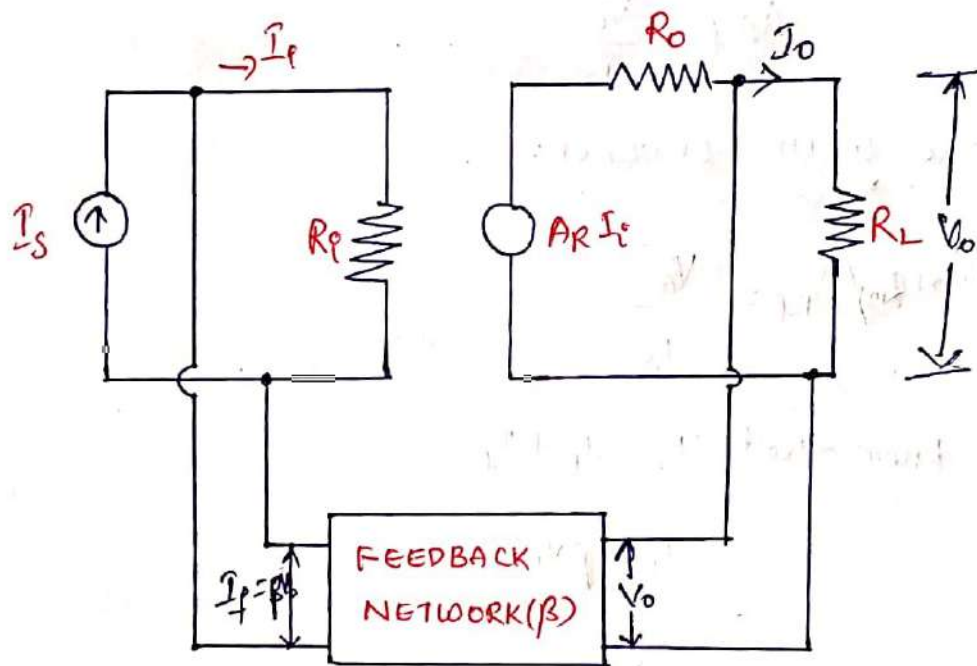


FIG: CIRCUIT DIAGRAM FOR VOLTAGE SHUNT FEEDBACK.

1. Basic Amplifier is transresistance Amplifier

\* At input, currents are mixed by means of shunt feedback.

$I_s \rightarrow$  Source signal

$I_i \rightarrow$  Input signal

$I_f \rightarrow$  Feedback signal

\* At output, voltage is sampled by means of shunt output signal  $V_o$ .



\* open loop gain transresistance  $A_R$  &  $R_m$  without feedback (32)

$$A_R = \frac{V_o}{I_i}$$

feedback factor  $\beta = \frac{I_f}{V_o}$

\* closed loop transresistance with feedback

$$A_{Rf} = \frac{V_o}{I_s}$$

TRANS RESISTANCE WITH FEEDBACK:

\* By definition  $R_{mf}/A_{Rf} = \frac{V_o}{I_s}$

we know that  $I_s = I_i + I_f$

$$I_f = \beta V_o$$

$$A_{Rf} = \frac{V_o}{I_i + I_f}$$

$$= \frac{V_o}{I_i + \beta V_o} \quad (\because V_o = A_R I_i)$$

$$= \frac{V_o}{I_i + \beta A_R I_i} = \frac{V_o}{I_i (1 + A_R \beta)}$$

$$\boxed{A_{Rf} = \frac{A_R}{1 + A_R \beta}}$$

$$A_{Rf} \ll A_R$$

### BANDWIDTH WITH FEEDBACK:

(33)

\* As transresistance with feedback decreases bandwidth with feedback should increase. The reason is gain bandwidth product should always be constant.

$$(B \cdot \omega)_f = B \cdot \omega (1 + A_R \beta)$$

$$B \cdot \omega = f_2 - f_1$$

$$f_{2f} = f_2 (1 + A_R \beta)$$

$$f_{1f} = \frac{f_1}{1 + A_R \beta}$$

### INPUT RESISTANCE WITH FEEDBACK:

\* Input resistance without feedback  $R_i = \frac{V_i}{I_i}$

Input resistance with feedback  $R_{if} = \frac{V_i}{I_s}$

$$R_{if} = \frac{V_i}{I_s}$$

$$= \frac{V_i}{I_i + I_f} = \frac{V_i}{I_i + \beta V_o}$$

$$= \frac{V_i}{I_i + \beta A_R I_i} = \frac{V_i}{I_i (1 + A_R \beta)}$$

$$R_{if} = \frac{R_i}{1 + A_R \beta}$$



## OUTPUT RESISTANCE WITH FEEDBACK ( $R_{of}$ ):

(34)

\* By definition,  $R_o = \frac{V_o}{I_o} \Big|_{\text{with } I_s = 0}$

$$V_i = V_s - I_f \quad I_i = I_s - I_f$$

$$V_i = -I_f \quad I_i = -I_f \quad (\because I_s = 0)$$

By applying KVL to the o/p ckt

$$V_o = I_o R_o + A_R I_i$$

$$V_o = I_o R_o + A_R (-I_f)$$

$$V_o = I_o R_o - A_R \beta V_o$$

$$(\because A_R = \frac{V_o}{I_i})$$

$$V_o + A_R \beta V_o = I_o R_o$$

$$V_o = A_R I_i$$

$$I_f = \beta V_o$$

$$V_o (1 + A_R \beta) = I_o R_o$$

$$\frac{V_o}{I_o} = \frac{R_o}{1 + A_R \beta}$$

$$\boxed{R_{of} = \frac{R_o}{1 + A_R \beta}}$$

for Voltage-Shunt feedback

$$\text{Forward Gain, } A_{Rf} = \frac{A_R}{1 + A_R \beta} \quad (\text{decreases})$$

$$\text{Input resistance } R_{if} = \frac{R_i}{1 + A_R \beta} \quad (\text{decreases})$$

$$\text{Bandwidth } (B.W)_{f} = B.W (1 + A_R \beta) \quad (\text{increases})$$

$$\text{output resistance } R_{of} = \frac{R_o}{1 + A_R \beta} \quad (\text{decreases})$$

## CURRENT-SERIES FEEDBACK:

(35)

\* The block diagram for a current series feedback is as shown below.

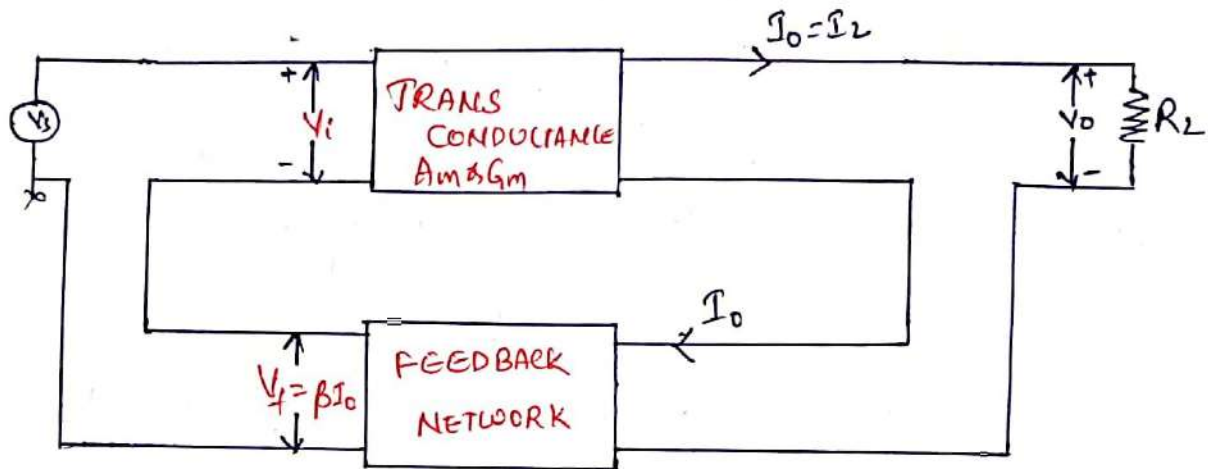


FIG: CURRENT SERIES FEEDBACK AMPLIFIER

\* If current is sampled and the mixing is in series with the input then the type of feedback is known as current series feedback.

\* Since the current is sampled, the output parameter monitored is current and mixing is series, the parameter affected is the Input Voltage.

\* Hence the parameter that is stabilised in current series feedback is transconductance  $G_m = \frac{I_o}{V_i}$ .

\* The feedback factor is the ratio of feedback voltage to the output current that is  $\beta = \frac{V_f}{I_o}$



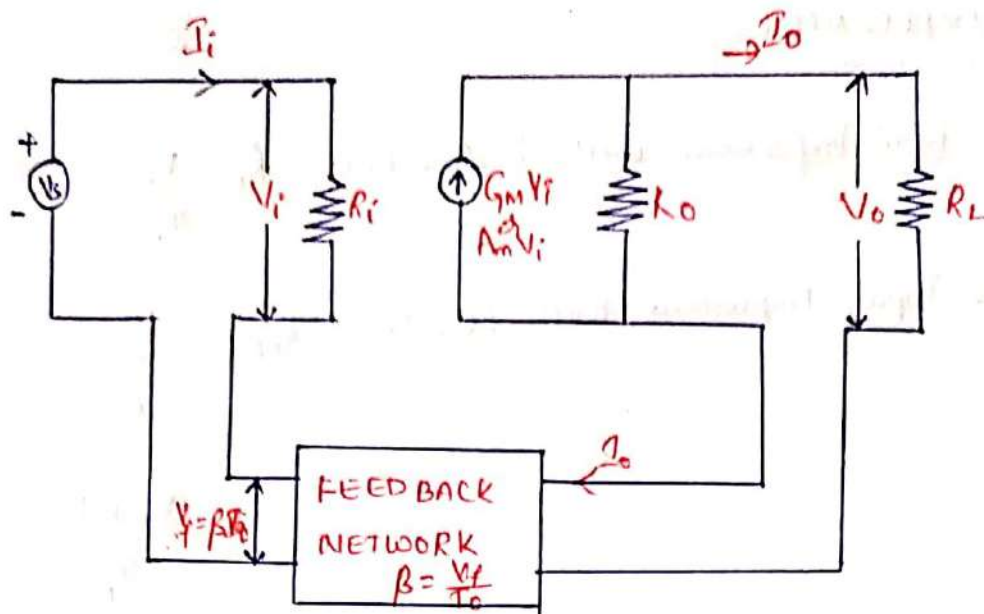


FIG. CIRCUIT DIAGRAM FOR VOLTAGE-CURRENT SERIES FEEDBACK

### TRANS CONDUCTANCE WITH FEEDBACK:

\* The transconductance without feedback is  $G_m = \frac{I_o}{V_i}$

The transconductance with feedback is  $G_{mf} = \frac{I_o}{V_s}$

We know that  $V_s = V_i + V_f$

$$G_{mf} = \frac{I_o}{V_i + V_f}$$

$$(\because V_f = \beta I_o)$$

$$= \frac{I_o}{V_i + \beta I_o}$$

$$= \frac{I_o}{V_i \left[ 1 + \beta \frac{I_o}{V_i} \right]}$$

$$G_{mf} = \frac{G_m}{1 + \beta G_m}$$

$$\therefore G_{mf} = \frac{G_m}{1 + \beta G_m}$$

INPUT IMPEDANCE:

\* The input impedance without feedback  $R_i = \frac{V_i}{I_i}$

The input impedance with feedback  $R_{if} = \frac{V_s}{I_i}$

$$= \frac{V_i + V_f}{I_i}$$

$$= \frac{V_i + \beta I_o}{I_i}$$

$$R_{if} = \frac{V_i}{I_i} + \beta \cdot \frac{I_o}{I_i} \quad \left( \because G_m = \frac{I_o}{V_i} \right)$$

$$I_o = G_m V_i$$

$$= R_i + \beta \cdot G_m \frac{V_i}{I_i}$$

$$= R_i + \beta G_m R_i$$

$$\boxed{R_{if} = R_i (1 + \beta G_m)}$$

OUTPUT IMPEDANCE:

\* The output impedance can be obtained by equating  $V_s = 0$

Apply KCL to o/p loop

$$R_D = \frac{V_o}{I_o}$$

$$R_{of} = \frac{V_o}{I_o} \Big|_{V_s=0}$$

$$I_o + G_m V_i = \frac{V_o}{R_D}$$

$$I_o = \frac{V_o}{R_D} - G_m V_i$$

$$I_o = \frac{V_o}{R_D} - G_m (-V_f)$$

$$\left( \because V_s = 0 \right. \\ \left. V_i = V_s - V_f \right. \\ \left. V_i = -V_f \right)$$



$$I_o = \frac{V_o}{R_o} + G_m V_f$$

$$I_o = R_o + G_m \beta I_o$$

\* The Output Impedance can be obtained by equating  $V_s = 0$

$$V_s = V_i + V_f$$

$$0 = V_i + V_f \quad V_i = -V_f$$

$$R_{of} = \left. \frac{V_o}{I_o} \right|_{V_s=0}$$

By applying KCL

$$I_o = \frac{V_o}{R_o} + G_m V_i$$

$$I_o = \frac{V_o}{R_o} + G_m (-V_f)$$

$$I_o = \frac{V_o}{R_o} + G_m \beta I_o$$

$$I_o + G_m \beta I_o = \frac{V_o}{R_o}$$

$$I_o (1 + G_m \beta) = \frac{V_o}{R_o}$$

$$R_o (1 + G_m \beta) = \frac{V_o}{I_o}$$

$$\boxed{R_{of} = R_o (1 + G_m \beta)}$$

For Current series feedback

(3)

$$A_{gmf} = \frac{G_m}{1 + G_m \beta} \quad (\text{decreases})$$

$$R_{if} = R_i (1 + \beta G_m) \quad (\text{Increases})$$

$$R_{of} = R_o (1 + G_m \beta) \quad (\text{Increases})$$

$$(B.W)_f = B.W (1 + G_m \beta) \quad (\text{Increases})$$

### PERFORMANCE COMPARISON OF FEEDBACK AMPLIFIERS

PARAMETER	VOLTAGE SERIES	VOLTAGE SHUNT	CURRENT SERIES	CURRENT SHUNT
1. Type of Amplifier	Voltage Amplifier	Trans-resistance Amplifier	Trans-conductance Amplifier	Current Amplifier
2. Sensitivity	$A_{vf}$	$R_{m-f}$	$G_{mf}$	$A_{if}$
3. Transfer gain	Decreases	Decreases	Decreases	Decreases
4. Non linear distortion	Decreases	Decreases	Decreases	Decreases
5. Noise	Reduces	Reduces	Reduces	Reduces
6. Bandwidth	Increases	Increases	Increases	Increases
7. Input Impedance	Increases	Decreases	Increases	Decreases
8. Output Impedance	Decreases	Decreases	Increases	Increases



## GENERAL CHARACTERISTICS OF NEGATIVE FEEDBACK AMPLIFIER:

(40)

\* The Negative feedback improves many desirable characteristics.  
The main

### STABILIZATION OF GAIN WITH NEGATIVE FEEDBACK:

\* The variations in temperature, supply voltages, ageing of components & variations in transistor parameters with replacement are some of the factors that affect the gain of an amplifier and cause it to change.

\* However, the overall gain of the amplifier can be made independent of these variations if negative feedback is used, this is an ~~more~~ important advantage of negative feedback.

→ The voltage gain with negative feedback is given as,

$$A_f = \frac{A}{1 + A\beta}$$

$$\text{if } A\beta \gg 1 \quad \text{then } A_f = \frac{A}{A\beta} \approx \frac{1}{\beta}$$

\* Thus, gain with feedback is independent of internal gain of the amplifier and depends on the passive elements such as resistors i.e., feedback network.

\* The values of resistors ~~mainly~~ remain fairly constant because they can be chosen very precisely with almost zero temperature coefficient of resistance. Thus the gain is stabilised.

$$A_f = \frac{A}{1+A\beta} \rightarrow (1)$$

(1)

Differentiating eq (1) w.r.t A

$$\frac{dA_f}{dA} = \frac{(1+A\beta)(1) - \frac{d}{dA}(1+A\beta) \cdot A}{(1+A\beta)^2} \quad \left( \because \frac{dP}{dV} = \frac{V \cdot \frac{dV}{dX} - P \cdot \frac{dV}{dX}}{V^2} \right)$$

$$= \frac{1+A\beta - (0+\beta) \cdot A}{(1+A\beta)^2} = \frac{1+A\beta - A\beta}{(1+A\beta)^2}$$

$$\frac{dA_f}{dA} = \frac{1}{(1+A\beta)^2}$$

$$\frac{dA_f}{A_f} = \frac{dA}{(1+A\beta)^2} \rightarrow (2)$$

Dividing eq (2) by eq (1)

$$\frac{dA_f}{A_f} = \frac{dA}{(1+A\beta)^2} \cdot \frac{A}{1+A\beta}$$

$$\frac{dA_f}{A_f} = \frac{dA}{(1+A\beta)^2} \cdot \frac{1+A\beta}{A} = \frac{dA}{A} \cdot \frac{1}{1+A\beta}$$

\* The term  $\frac{dA_f}{A_f}$  represents the fractional change in amplifier transfer gain with feedback and  $\frac{dA}{A}$  denotes the fractional change in voltage gain without feedback.

\* The term  $\frac{1}{1+A\beta}$  is called sensitivity.



∴ The sensitivity is defined as the ratio of percentage change in Voltage gain with feedback to the percentage change in Voltage gain without feedback.

$$\text{Sensitivity} = \frac{\left(\frac{\Delta A_f}{A_f}\right)}{\left(\frac{\Delta A}{A}\right)} = \frac{1}{1+A\beta}$$

∴ The Reciprocal of the Sensitivity is called Desensitivity.

i.e., Desensitivity  $D = (1+A\beta)$ .

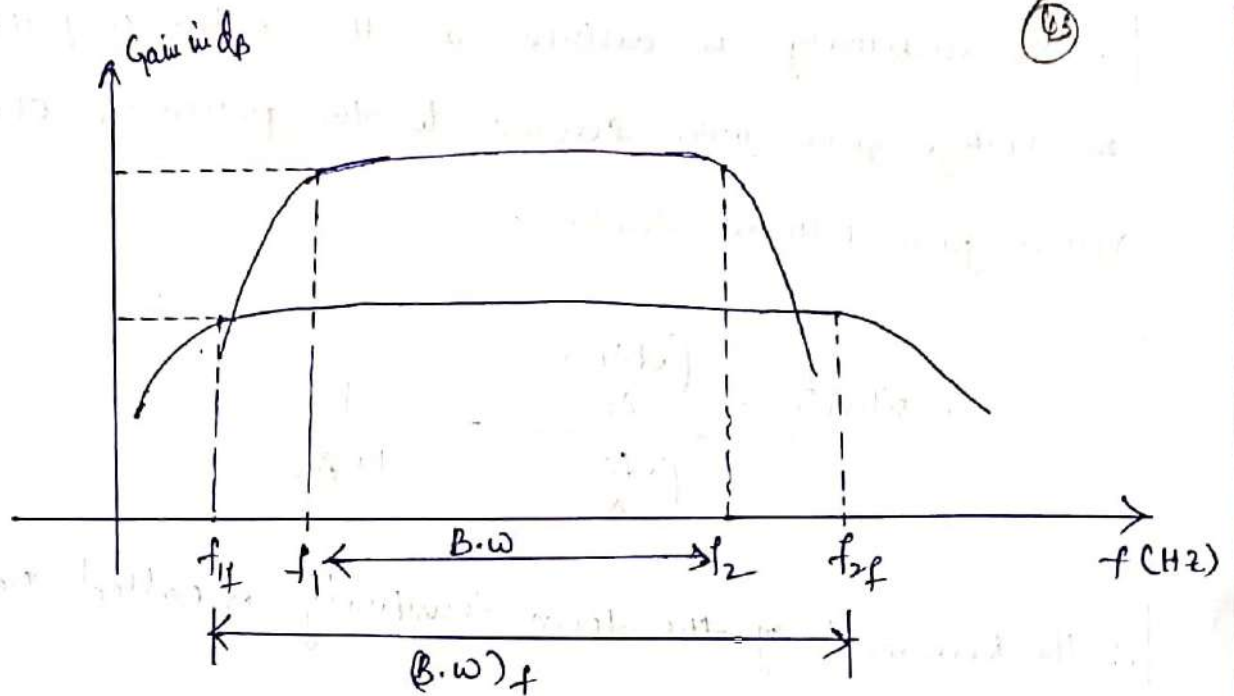
## 2. INCREASE OF BANDWIDTH:

\* The Bandwidth of an amplifier is the difference between the upper cut-off frequency  $f_2$  and the lower cut-off frequency  $f_1$ .

\* Due to the negative feedback in the amplifier, the upper cut-off frequency  $f_{2f}$  is increased by the factor  $(1+A\beta)$  and the lower cut-off frequency  $f_{1f}$  is decreased by the same factor  $(1+A\beta)$ .

$$f_{2f} = f_2(1+A\beta)$$

$$f_{1f} = f_1 / (1+A\beta)$$



\* As the voltage gain of a feedback amplifier reduces by the factor  $\frac{1}{(1+A\beta)}$ , its bandwidth would be increased by  $(1+A\beta)$  i.e.,

$$B.W_f = BW(1+A\beta)$$

### DECREASED DISTORTION:

\* Consider an amplifier with an open loop voltage gain and ~~at~~ a total harmonic distortion  $D$ . Then with the introduction of negative feedback with the feedback ratio  $\beta$ , the distortion will reduce to

$$D_f = \frac{D}{1+A\beta}$$

### DECREASED NOISE:

\* There are many sources of noise in an amplifier depending upon the active device used. With using the negative feedback with the feedback ratio  $\beta$ , the noise  $N$ , can be reduced by a



factor of  $(1+A\beta)$  in a similar manner to non-linear distortion  
Thus the noise with feedback is given by, (44)

$$N_f = \frac{N}{1+A\beta}$$

### INCREASE IN INPUT IMPEDANCE:

\* An amplifier should have high input impedance so that it will not load the preceding stage or the input voltage source.

\* Such a desirable characteristic can be achieved with the help of negative series voltage feedback. The input impedance with feedback is given by,

$$Z_{if} = Z_i (1+A\beta)$$

\* Thus the input impedance is increased by a factor of  $(1+A\beta)$ .

### DECREASE IN OUTPUT IMPEDANCE:

\* An Amplifier with low output impedance is capable of delivering power to the load without much loss. Such a desirable characteristic is achieved by employing negative series voltage feedback in an amplifier.

$$Z_{of} = \frac{Z_o}{1+A\beta}$$

\* For analyzing the feedback amplifier, it is necessary to go through the following steps.

Step 1: Identify Topology (type of feedback)

a) To find the type of Sampling Network.

i. By shorting the output the feedback signal becomes zero, then it is called "Voltage Sampling".

ii. By opening the output loop the feedback signal becomes zero, then it is called "Current Sampling".

b) To find the type of mixing Network.

i. If the feedback signal is subtracted from the externally applied signal as a voltage in the input loop, it is called "Series mixing".

ii. If the feedback signal is subtracted from the externally applied signal as a current in the input loop, it is called as "Shunt mixing".

\* Thus, by finding the type of sampling network and mixing network, type of feedback amplifier can be identified.

Step 2: To find the Input circuit.

i. For Voltage Sampling, the output voltage is made zero by shorting the output.



ii. For Current Sampling, the output current is made zero <sup>(46)</sup> by opening the output loop.

Step-5: To find the output circuit.

i. For Series mixing, the input current is made zero by opening the input loop.

ii. For Shunt mixing, the input Voltage is made zero by shorting the input loop.

Step-2 & Step-3: Ensure that the feedback is reduced to zero without altering the loading on the basic amplifier.

Step-4: Optional. Replace each <sup>active</sup> device by its h-parameter model at low frequency.

Step-5: Find the open loop gain (gain without feedback) of the amplifier.

Step-6: Indicate  $X_f$  (feedback Voltage or feedback current) and  $X_o$  (output Voltage or o/p current) on the circuit and evaluate  $\beta = X_f/X_o$ .

Step-7: From  $A$  &  $\beta$ , find  $D$ ,  $A_f$ ,  $R_{if}$ ,  $R_{of}$  and  $R_{o_f}$ .

## PROBLEMS

1. The Voltage gain of an amplifier without feedback is 3,000. Calculate the Voltage gain of the amplifier if the Negative feedback is introduced in the circuit. Given that feedback fraction  $\beta = 0.01$ .

Sol Given data Voltage gain without feedback  $A_v = 3,000$   
feedback fraction  $\beta = 0.01$

Find Voltage gain with -ve feedback  
 $A_{vf} = ?$

$$\begin{aligned}\text{We know that } A_{vf} &= \frac{A_v}{1 + A_v \beta} \\ &= \frac{3000}{1 + 3000(0.01)} \\ &= \frac{3000}{31} \\ &= 96.774\end{aligned}$$

$\therefore$  Voltage gain with -ve feedback  $A_{vf} = 96.774$

2. Calculate the gain of a -ve feedback amplifier with an internal gain  $A_v = 75$  and feedback fraction  $\beta = \frac{1}{15}$ , what will be the gain if  $A_v$  doubles?

Sol Given data Internal gain  $A_v = 75$   
feedback fraction  $\beta = \frac{1}{15}$



$$\text{Voltage gain with feedback } A_{vf} = \frac{A_v}{1 + A_v \beta}$$

$$= \frac{75}{1 + 75 \times \frac{1}{18}}$$

$$= \frac{75}{6}$$

$$A_{vf} = 12.5$$

When  $A_v$  doubles i.e.,  $A_v = 2(75)$

$$A_v = 150$$

$$A_{vf} = \frac{A_v}{1 + A_v \beta} = \frac{150}{1 + 150 \times \frac{1}{18}}$$

$$= \frac{150}{11}$$

$$A_{vf} = 13.64$$

3. An amplifier with  $\neg$ ve feedback gives an o/p of 12.5V with an input of 1.5V. When feedback is removed, it requires 0.25V i/p for the same o/p. find i) Value of Voltage gain without f/b, ii) Value of  $\beta$ , if the i/p & o/p are <sup>out of</sup> phase and  $\beta$  is real.

sol

$$\text{Given that } V_o = 12.5 \text{ V}$$

$$V_s = 1.5 \text{ V}$$

$$V_i = 0.25$$

i. Voltage gain without feedback  $A_v = \frac{V_o}{V_i}$

$$= \frac{12.5}{0.25}$$

$$\boxed{A_v = 50}$$

Voltage gain with feedback  $A_{vf} = \frac{V_o}{V_s}$

$$= \frac{12.5}{1.5}$$

$$\boxed{A_{vf} = 8.333}$$

ii.) feedback ratio  $\beta = ?$

We know that  $A_{vf} = \frac{A_v}{1 + \beta A_v}$

$$(1 + \beta A_v) A_{vf} = A_v$$

$$1 + \beta A_v = A_v / A_{vf}$$

$$\beta = \frac{A_v / A_{vf} - 1}{A_v}$$

$$= \frac{50 / 8.33 - 1}{50}$$

$$\boxed{\beta = 0.10}$$



## OSCILLATORS

### INTRODUCTION:

\* As, we know that an amplifier strengthens the input signal without any change in its waveform and frequency. The additional power required comes from the external source.

\* Thus an amplifier is essentially an energy converter that draws energy from a dc supply and converts it into ac energy at signal frequency, the energy conversion process being controlled by the input signal.

\* On the other hand an oscillator does not require any external signal either to start or maintain the process of energy conversion and the energy conversion process is controlled by the oscillator itself.

\* Oscillators find wide applications in electronic <sup>communication measuring</sup> equipment. In AM, FM super heterodyne receivers, "local oscillator" is used to assist in the reduction of the incoming radio frequency (IF).

\* Other applications include their use as "clocks" in digital systems such as microcomputers, in the sweep circuits found in TV sets and oscilloscopes.

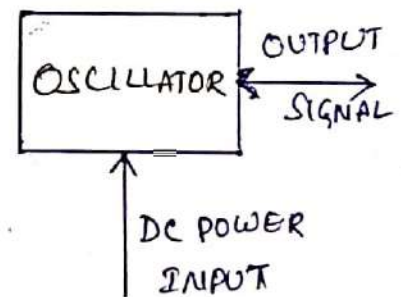
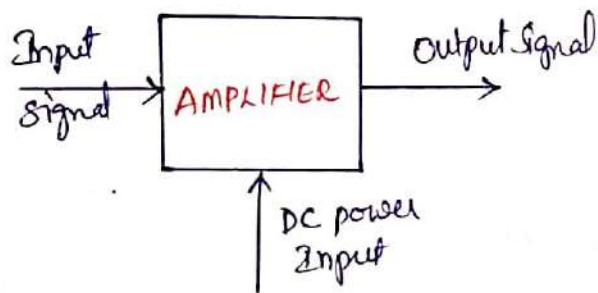
## OSCILLATOR:

2

DEFINITION: The electronic circuit which is used to generate a periodic waveform without an AC input signal is called as Oscillator.

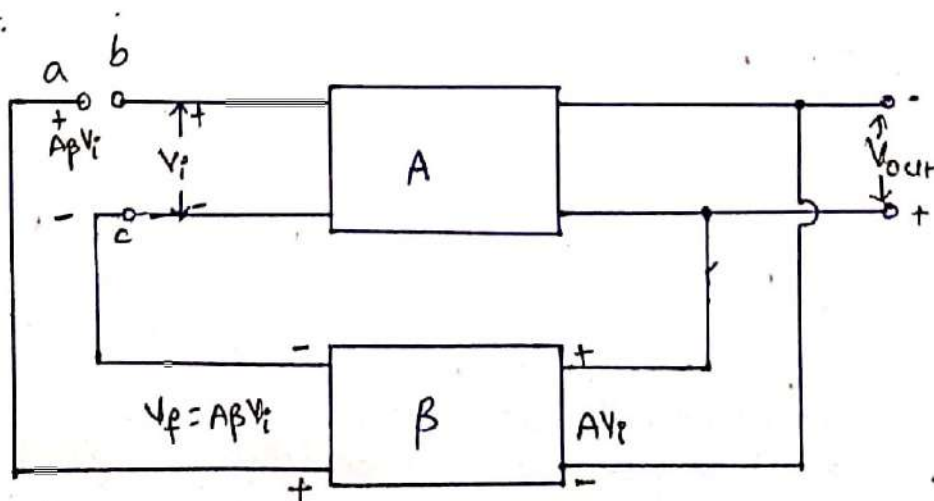
\* As we know that an amplifier strengthens the input signal without any change in its waveform and frequency.

\* In an oscillator, the output signal frequency depends on the passive components employed in the circuit. Oscillator may provide fixed & variable frequency.



### OPERATION OF OSCILLATOR:

\* To understand how an oscillator produces an output signal without an external input signal, let us consider the feedback circuit.





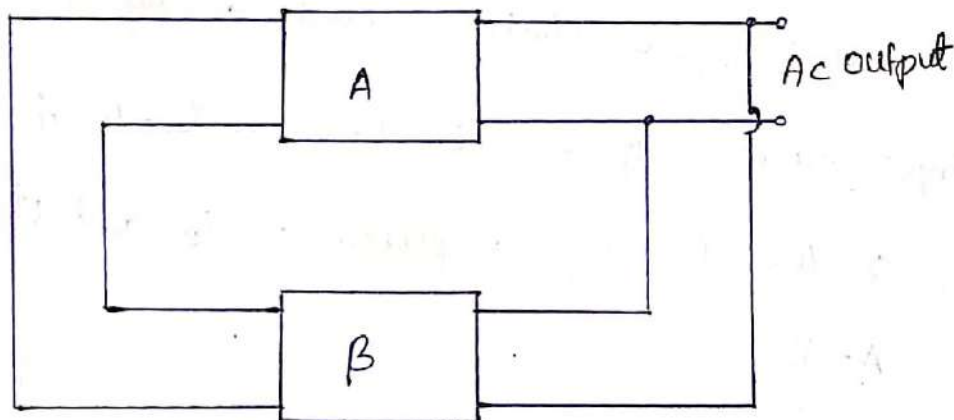
\* Where  $V_i$  is the voltage of ac input driving the input terminals of an amplifier having voltage gain  $A$ . 3

\* The amplified voltage is  $V_o = AV_i$ .

\* This voltage drives a feedback circuit that is usually a resonant circuit, as we get maximum feedback at one frequency. The feedback voltage returning to point a is given by,

$$V_f = A\beta V_i$$

Where  $\beta$  is the gain of feedback network.



\* Here, the amplifier generates  $180^\circ$  phase shift.

CONDITION FOR OSCILLATIONS (S) BARKHAUSEN CRITERION:

1. The total phase shift around the loop should be zero degrees or  $360^\circ$ .

2. The magnitude of product of the open loop gain of the amplifier ( $A$ ) and the feedback factor is unity i.e.,  $|A\beta| = 1$ .

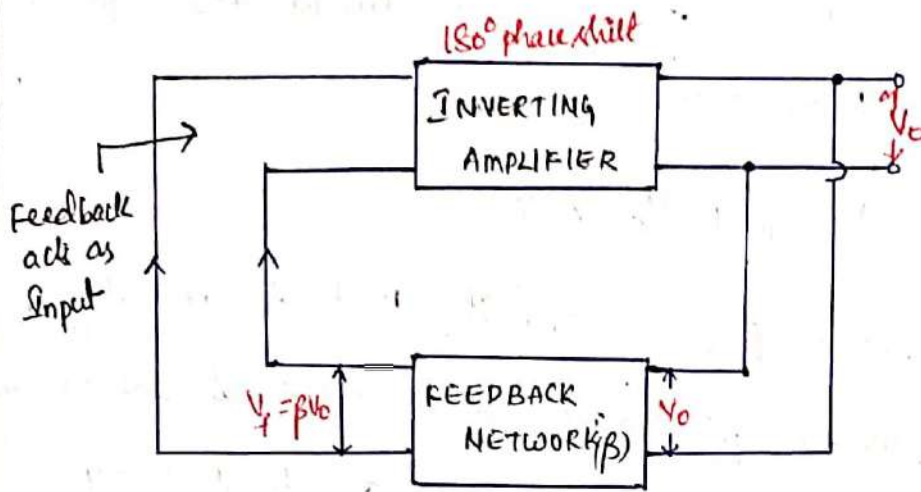


FIG: BASIC BLOCK DIAGRAM OF OSCILLATOR CIRCUIT

\* For the feedback network input is ' $V_o$ ' then the feedback network produces  $180^\circ$  phase shift

\* This feedback signal is given to the input of the inverting amplifier so that phase shift around a loop is  $0^\circ$  or  $360^\circ$ .

\* Let the input voltage of the feedback network is  $V_o$  i.e., output voltage of the inverting amplifier is  $V_o$  and it is given by,

$$A = \frac{V_o}{V_i}$$

$$V_o = AV_i$$

\* Feedback network provides  $180^\circ$  phase shift and it is given by

$$\beta = \frac{-V_f}{V_o} \quad V_f = -\beta V_o$$

\* Where  $-ve$  sign indicates the  $180^\circ$  phase shift provided by the feedback network.

$$V_f = -\beta V_o \Rightarrow V_f = -\beta AV_i$$

$\Rightarrow$  For the oscillator  $V_f$  must act as an input voltage of inverting amplifier.



$$V_i = V_f$$

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$$V_i = -A\beta V_i$$

$$-A\beta = 1$$

$$\boxed{|A\beta| = 1}$$

→ The above condition is called Barkhausen criterion.

→ The inverting amplifier produces  $180^\circ$  phase shift and the feedback network produces  $180^\circ$ . So that phase shift around the loop is  $360^\circ$ .

\* The above two conditions are required to be satisfied by the circuit to work as an oscillator producing sustained oscillations of constant frequency and amplitude.

\* Let us see the effect of magnitude of the product of gain and feedback factor on the nature of the oscillation.

1.  $|A\beta| > 1$

\* The total phase shift around a loop  $0^\circ$  or  $360^\circ$  and  $|A\beta| > 1$  then the oscillations are growing type. The amplitude of oscillation goes on increasing.

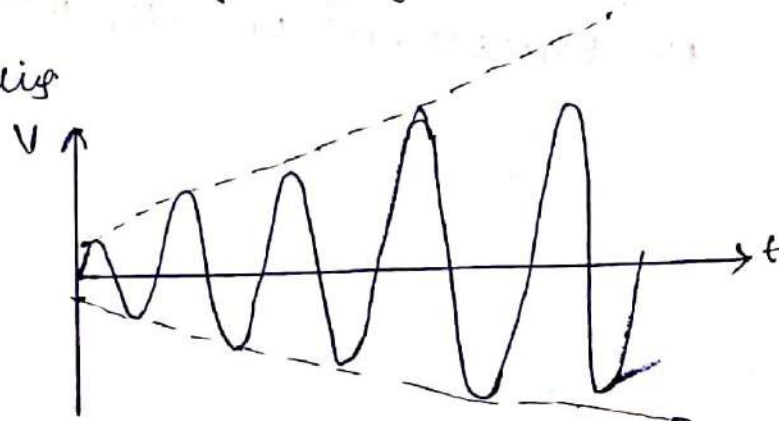


FIG: GROWING TYPE OSCILLATIONS

2.  $|A\beta| = 1$

\* When the total phase shift around a loop is  $0^\circ$  or  $360^\circ$  ensuring positive feedback and  $|A\beta| = 1$  then the oscillations are with constant frequency and amplitude called Sustained Oscillations.

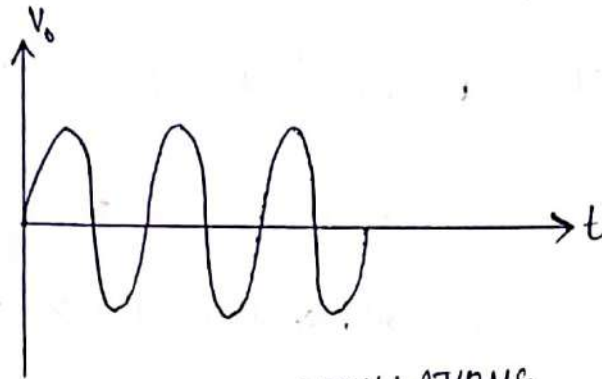


FIG: SUSTAINED OSCILLATIONS.

3.  $|A\beta| < 1$

\* When total phase shift around a loop is  $0^\circ$  or  $360^\circ$  but  $|A\beta| < 1$  then the oscillations are of decaying type i.e., the amplitude decreases exponentially.

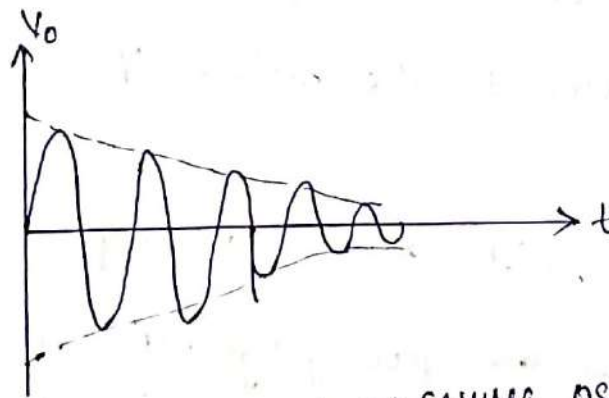


FIG: EXPONENTIALLY DECAYING OSCILLATIONS.



## CLASSIFICATION OF OSCILLATORS:

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\* Oscillators are classified in the following different ways.

1. According to the waveforms generated

a. Sinusoidal oscillator

b. Relaxation oscillator

a. Sinusoidal oscillator generates sinusoidal waveforms.

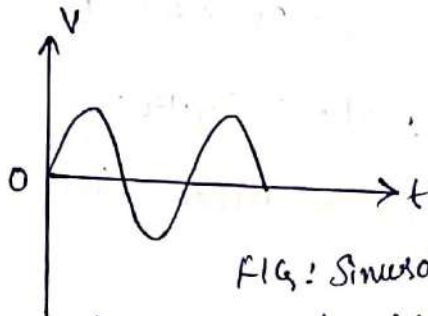


FIG: Sinusoidal wave form

b. Relaxation oscillator generates voltages or currents which vary abruptly one or more times in a cycle of oscillation.

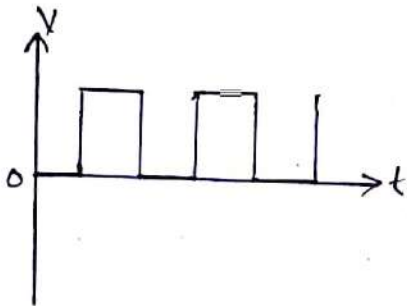


FIG: Square waveform

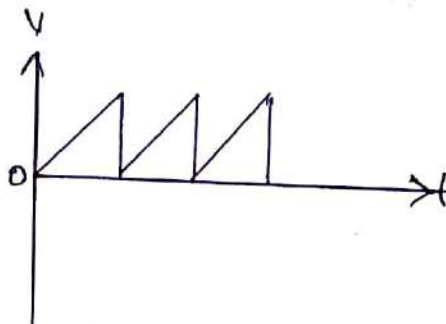


FIG: Sawtooth

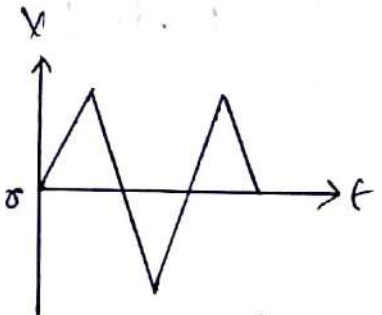


FIG: Triangular

2. According to the fundamental mechanism involved

a. Negative Resistance oscillator

b. Feedback oscillator

\* Negative resistance oscillator uses negative resistance of the amplifying device to neutralise the positive resistance of the oscillator.

Feedback oscillator uses positive feedback in the feedback<sup>s</sup> amplifier to satisfy the Barkhausen criterion.

3. According to the frequency generated:

- a. Audio frequency oscillator (AFO) : up to 20 kHz.
- b. Radio frequency oscillator (RFO) : 20 kHz to 30 MHz
- c. Very high frequency (VHF) oscillator : 30 MHz to 300 MHz.
- d. Ultra high frequency (UHF) oscillator : 300 MHz to 3 GHz.
- e. Microwave frequency oscillator : above 3 GHz.

4. According to the type of circuit used, Sine-wave oscillators may be classified as,

- a. LC tuned oscillators
- b. RC phase shift oscillator



## \* RC phase Shift Oscillator \*

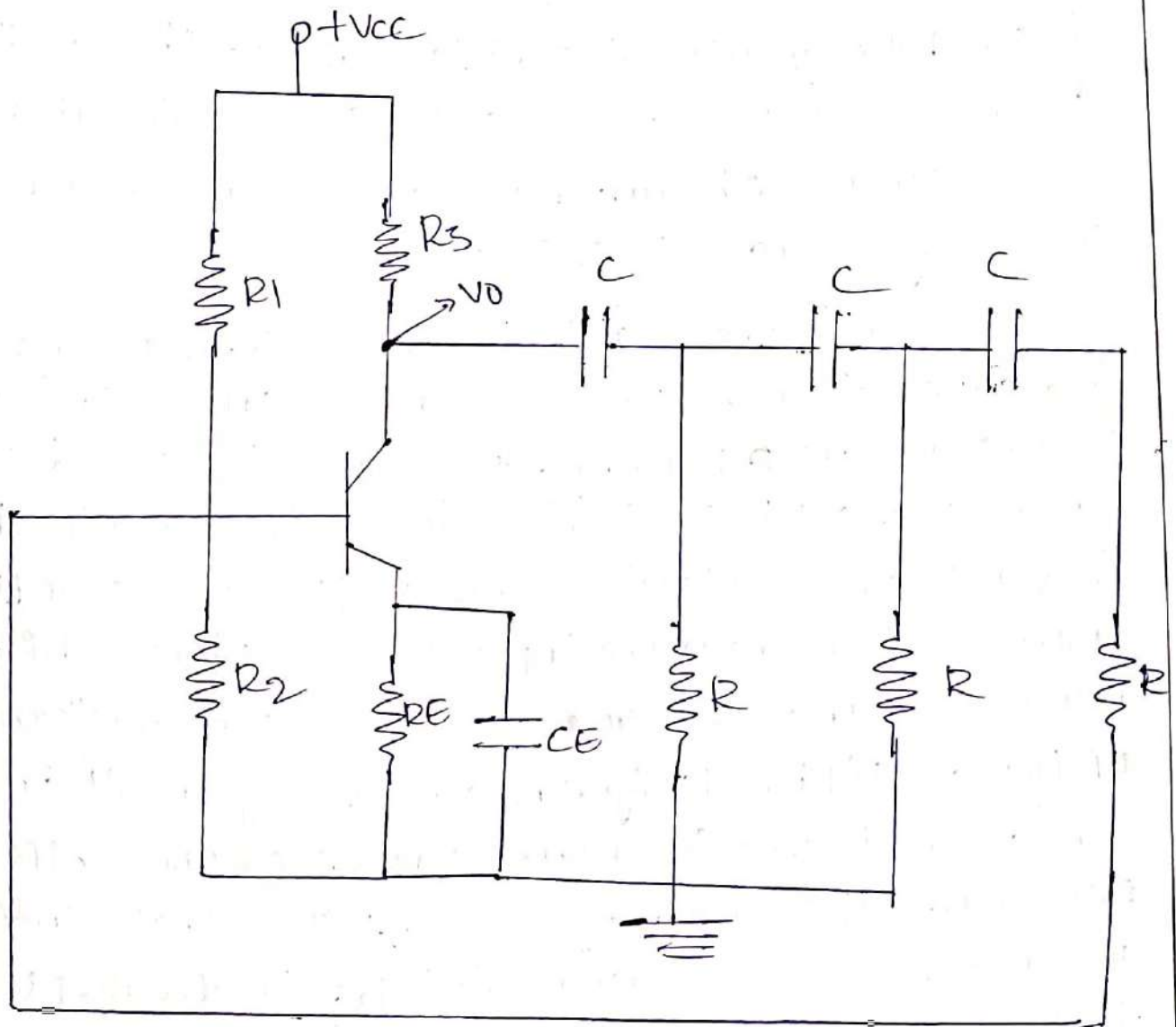
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for producing oscillations is an oscillator circuit we need positive feedback which means that the voltage signal feedback should be in phase with the input signal. for providing a positive feedback at one particular frequency, an inverting amplifier may be used with a feedback network that causes a phase shift of  $180^\circ$  at the desired frequency of oscillations as shown in figure. The  $180^\circ$  phase shift in the feedback signal can be obtained by a suitable network consisting of three R-C sections.

When a phase shift network such as that indicated given below is used in a phase shift oscillator, the R's and C's must be selected so as to produce a phase shift of  $180^\circ$  at the desired frequency of oscillation. The output of the voltage amplifier is fed to the input to the phase shift network. Thus  $V_1 = V_{out}$ . The output resistance of the amplifier designed to be very small in comparison to the input impedance of the shift network. The output voltage of the phase shift network  $V_2$  is fed into the input of the amplifier. i.e.,  $V_2 = V_{in}$ . The amplifier's input impedance must be much larger than the output impedance of the phase shift network.

Alternatively, a positive feedback can be obtained by using two stages of amplifiers each giving a phase shift of  $180^\circ$ . A part of this output is feedback to the input through a feedback network without causing any further phase shift. Wein bridge oscillator operates on this principle.

(i)



Circuit diagram.



- (i)  $h_{re}$  of the transistor is usually negligibly small and therefore,  $h_{re} V_{out}$  is omitted from the circuit.
- (ii)  $h_{oe}$  of the transistor is very small i.e.  $\frac{1}{h_{oe}}$  is much larger than  $R_C$ . Thus the effect of  $h_{oe}$  can be neglected.

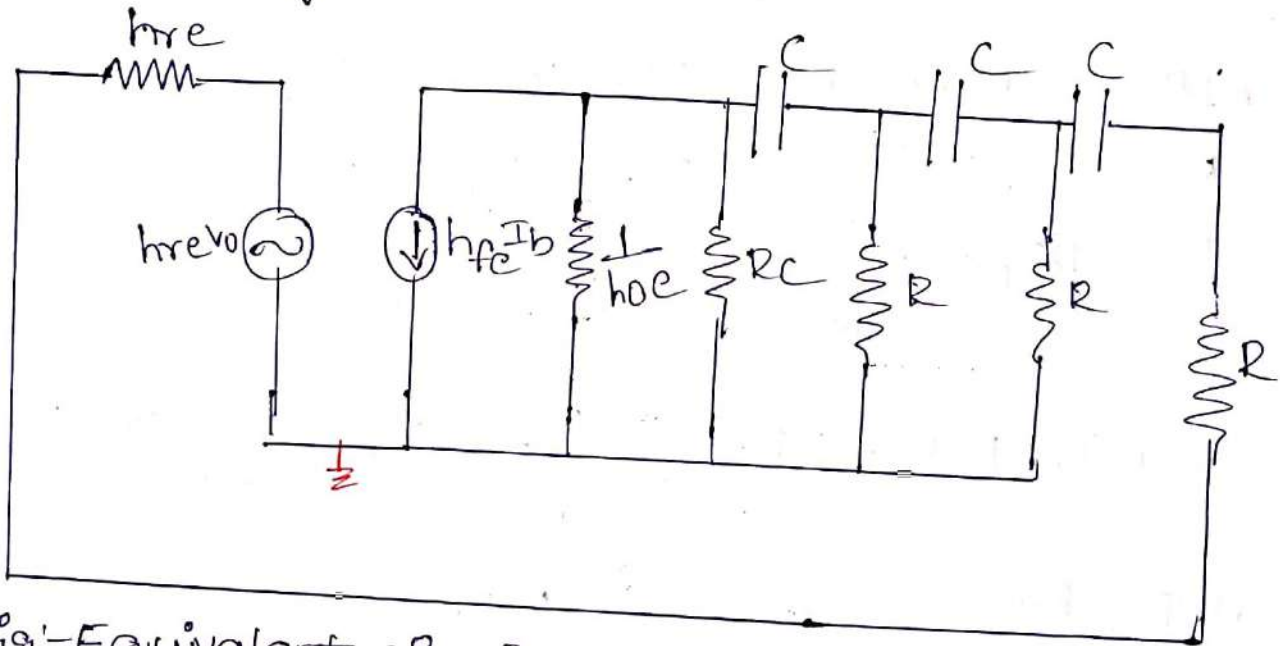
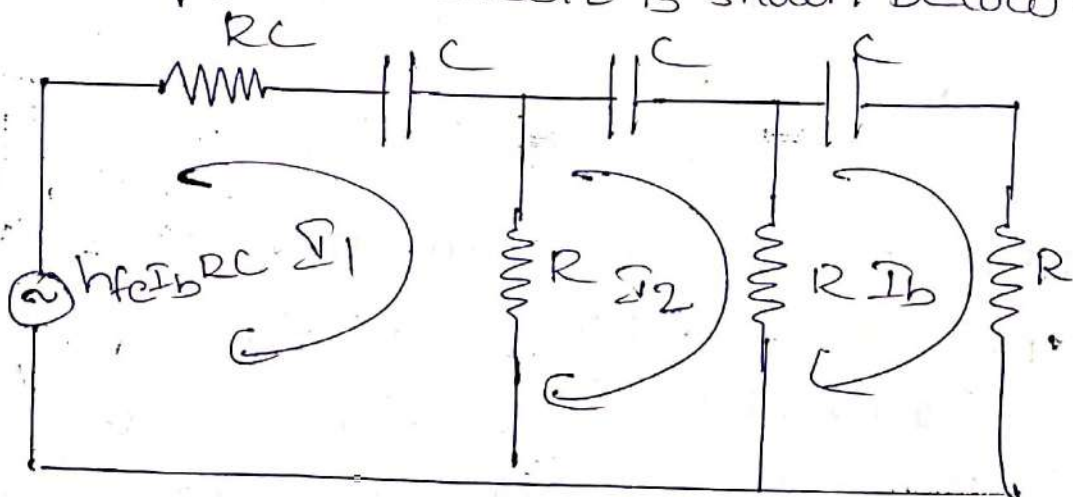


fig:- Equivalent circuit of Transistor phase-shift Oscillator is shown above.

Making above assumptions and replacing current source by equivalent voltage source, the simplified equivalent circuit is shown below.



Apply KVL to the loop 1.

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$$-hfe I_b R_C + I_1 R_C + \frac{1}{j\omega C_1} I_1 + R(I_1 - I_2) = 0$$

$$hfe I_b R_C + I_1 R_C + \frac{1}{j\omega C_1} I_1 + R I_1 - R I_2 = 0$$

$$hfe I_b R_C + I_1 \left( R_C + \frac{1}{j\omega C_1} + R \right) - R I_2 = 0 \rightarrow (1)$$

Apply KVL to the loop 2.

$$R(I_2 - I_1) + \frac{1}{j\omega C} I_2 + R(I_2 - I_b) = 0$$

$$R I_2 - R I_1 + \frac{1}{j\omega C} I_2 + R I_2 - R I_b = 0$$

$$-R I_1 + I_2 \left( 2R + \frac{1}{j\omega C} \right) - R I_b = 0 \rightarrow (2)$$

Apply KVL to the loop (3)

$$R(I_b - I_2) + \frac{1}{j\omega C} I_b + R I_b = 0$$

$$R I_b - R I_2 + \frac{1}{j\omega C} I_b + R I_b = 0$$

$$-R I_2 + I_b \left( R + R + \frac{1}{j\omega C} \right) = 0 \rightarrow (3)$$

$$\text{where, } \frac{1}{j\omega C} = X_C \text{ (or) } jX_C$$

Apply Cramer's rule from above equations.

$$I_1 (R_C + R + jX_C) - R I_2 - hfe R_C I_b = 0 \rightarrow (1)$$

$$-R I_1 + (2R + jX_C) I_2 - R I_b = 0 \rightarrow (2)$$

$$-R I_2 + (2R + jX_C) I_b = 0 \rightarrow (3)$$



Cramer's rule,

13

$$\begin{vmatrix} R_C + R - j\omega L & -R & h_{fe} R_C \\ -R & 2R - j\omega L & -R \\ 0 & -R & 2R - j\omega L \end{vmatrix} = 0$$

$$= R_C + R - j\omega L [(2R - j\omega L)^2 - R^2] - (-R) (-R(2R - j\omega L) - 0) + (-h_{fe} R_C) (R^2 + 0) = 0$$

$$= R_C + R - j\omega L [ (2R)^2 - 2 \cdot 2R \cdot j\omega L - \omega^2 L^2 ] + R (-2R^2 + j\omega L R) - h_{fe} R_C R^2 = 0$$

$$= (R_C + R - j\omega L) (3R^2 - j4\omega L R - \omega^2 L^2) - 2R^3 + j\omega^2 L^2 R - h_{fe} R_C R^2 = 0$$

$$= 3R^2 R_C - j4\omega L R C R - R_C \omega^2 L^2 + 3R^3 - j4\omega L R^2 - \omega^2 L^2 R - j3\omega L R^2 - 4R \omega^2 L^2 + j\omega^3 L^3 - 2R^3 + j\omega^2 L^2 R - h_{fe} R_C R^2 = 0$$

$$= R^3 + R^2 R_C (3 + h_{fe}) - 5R \omega^2 L^2 - R_C \omega^2 L^2 - 6j\omega^2 L^2 R - j4R R_C \omega L + j\omega^3 L^3 = 0$$

equating the imaginary component of the equation to zero.

$$6R^2 \omega L + 4R R_C \omega L - \omega^3 L^3 = 0$$

$$\omega L (6R^2 + 4R R_C - \omega^2 L^2) = 0$$

$$6R^2 + 4R R_C - \omega^2 L^2 = 0$$

$$6R^2 + 4R R_C = \omega^2 L^2$$

$$\omega = \sqrt{6R^2 + 4R R_C} / L$$

$$\therefore X_C = \frac{1}{2\pi f_c C}$$

14

$$\frac{1}{2\pi f_c} = \sqrt{R^2 + 4R R_C}$$

$$\frac{1}{2\pi f_c} = \sqrt{R^2 \left(6 + \frac{4R_C}{R}\right)}$$

$$\therefore R_C = R$$

$$\frac{1}{2\pi f_c} = \sqrt{R^2 \left(6 + 4 \frac{R}{R}\right)}$$

$$\frac{1}{2\pi f_c} = \sqrt{R^2 (10)}$$

$$\frac{1}{2\pi f_c} = R\sqrt{10}$$

$$\frac{1}{f} = 2\pi R\sqrt{10}$$

$$f = \frac{1}{2\pi R\sqrt{10}}$$

$$\therefore \text{where } \frac{R_C}{R} = k$$

$$\frac{1}{2\pi f_c} = \sqrt{R^2 (6 + 4k)}$$

$$\frac{1}{2\pi f_c} = \sqrt{R^2 (6 + 4k)}$$

$$f = \frac{1}{2\pi R\sqrt{6 + 4k}}$$



Now equating real term into zero. 15

$$R^3 + (3 + hfe) R^2 R_C - X_C^2 R_C - 5X_C^2 R = 0$$

$$R^3 + 3R^2 R_C + hfe R^2 R_C - X_C^2 R_C - 5X_C^2 R = 0$$

$$R^3 + 3R^2 R_C + hfe R^2 R_C - X_C^2 (R_C + 5R) = 0$$

$$\therefore X_C^2 = 6R^2 + 4R R_C \text{ from imaginary part}$$

$$R^3 + 3R^2 R_C + hfe R^2 R_C - (6R^2 + 4R R_C)(R_C + 5R) = 0$$

$$R^3 + 3R^2 R_C + hfe R^2 R_C - 6R^2 R_C - 30R^3 - 4R R_C^2 - 20R^2 R_C = 0$$

$$-23 R^2 R_C + hfe R^2 R_C - 29 R^3 - 4R R_C^2 = 0$$

$$-29 R^3 - 23 R^2 R_C - 4R R_C^2 + hfe R^2 R_C = 0$$

$$-29 R^3 - 23 R^2 R_C - 4R R_C^2 + hfe R^2 R_C = 0$$

$$hfe R^2 R_C = 29 R^3 + 23 R^2 R_C + 4R R_C^2$$

$$hfe R^2 R_C = R^2 R_C \left[ 29 \cdot \frac{R^3}{R^2 R_C} + 23 \frac{R^2 R_C}{R^2 R_C} + \frac{4R R_C^2}{R^2 R_C} \right]$$

$$hfe = 29 \cdot \frac{R}{R_C} + 23 + \frac{4R_C}{R} \quad \therefore \text{If } R_C = R$$

$$A_V = 29$$

$$B = \frac{1}{29}$$

$$AB = 29 \cdot \frac{1}{29}$$

$$\boxed{AB = 1}$$

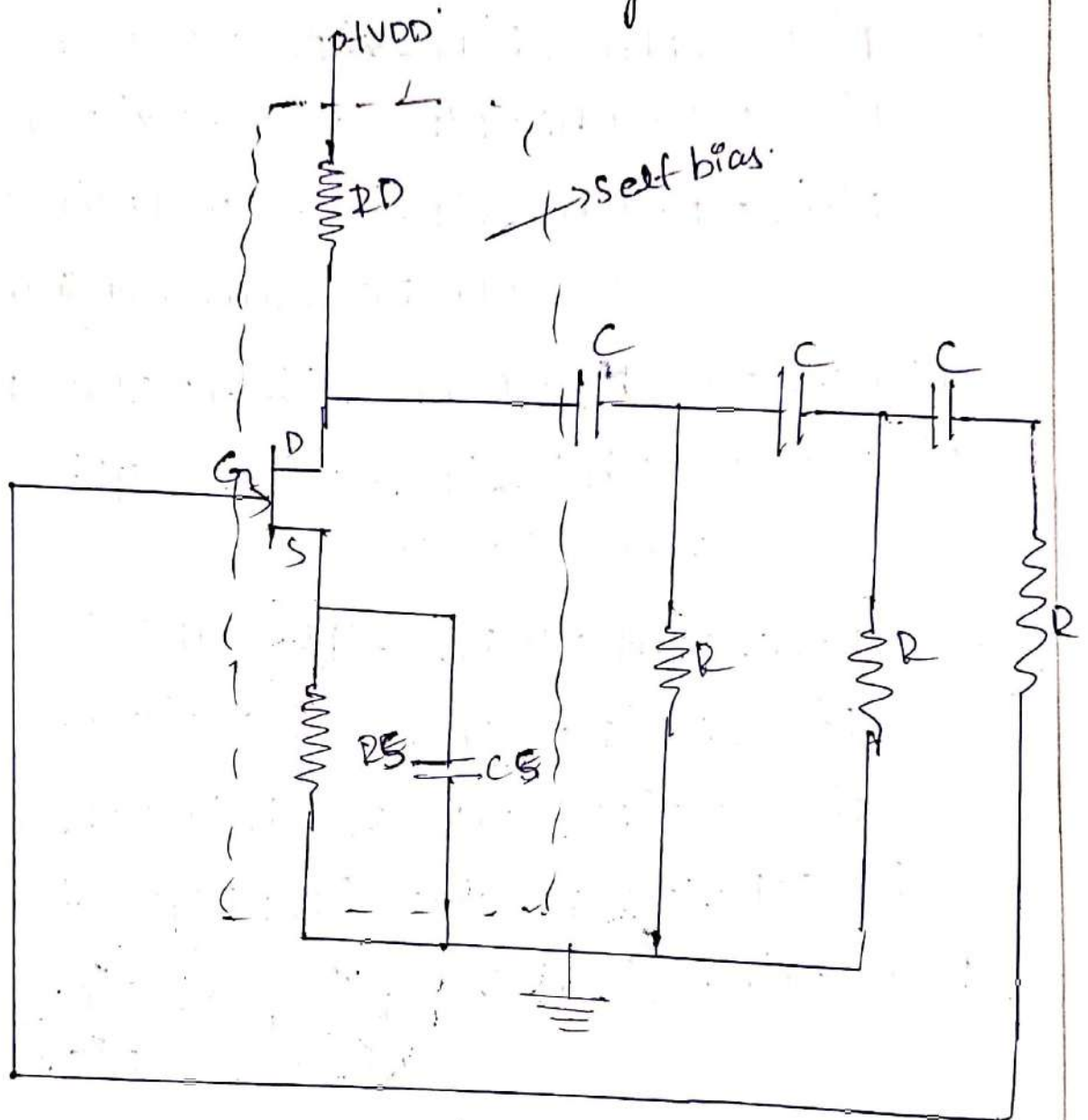
$$\therefore \frac{R_C}{R} = k \rightarrow \frac{R}{R_C} = \frac{1}{k}$$

$$hfe = \frac{29}{k} + 23 + 4k$$

$$hfe = 29 + 23 + 4$$

$$\boxed{hfe = 56}$$

# RC phase shift oscillator using FET:-



$$X_C = \sqrt{6} R$$

$$\frac{1}{\omega C} = \sqrt{6} R$$

$$\frac{1}{2\pi f_c} = \sqrt{6} R$$

$$2\pi f_c = \frac{1}{\sqrt{6} R}$$

$$f_0 = \frac{1}{2\pi f_c \sqrt{6} R}$$



## WIEIN BRIDGE OSCILLATOR :

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\* It is one of the most popular type of oscillator used in audio and sub-audio frequency ranges (20-20KHz).

\* The circuit diagram of wein bridge oscillator using BJT is shown in the below figure.

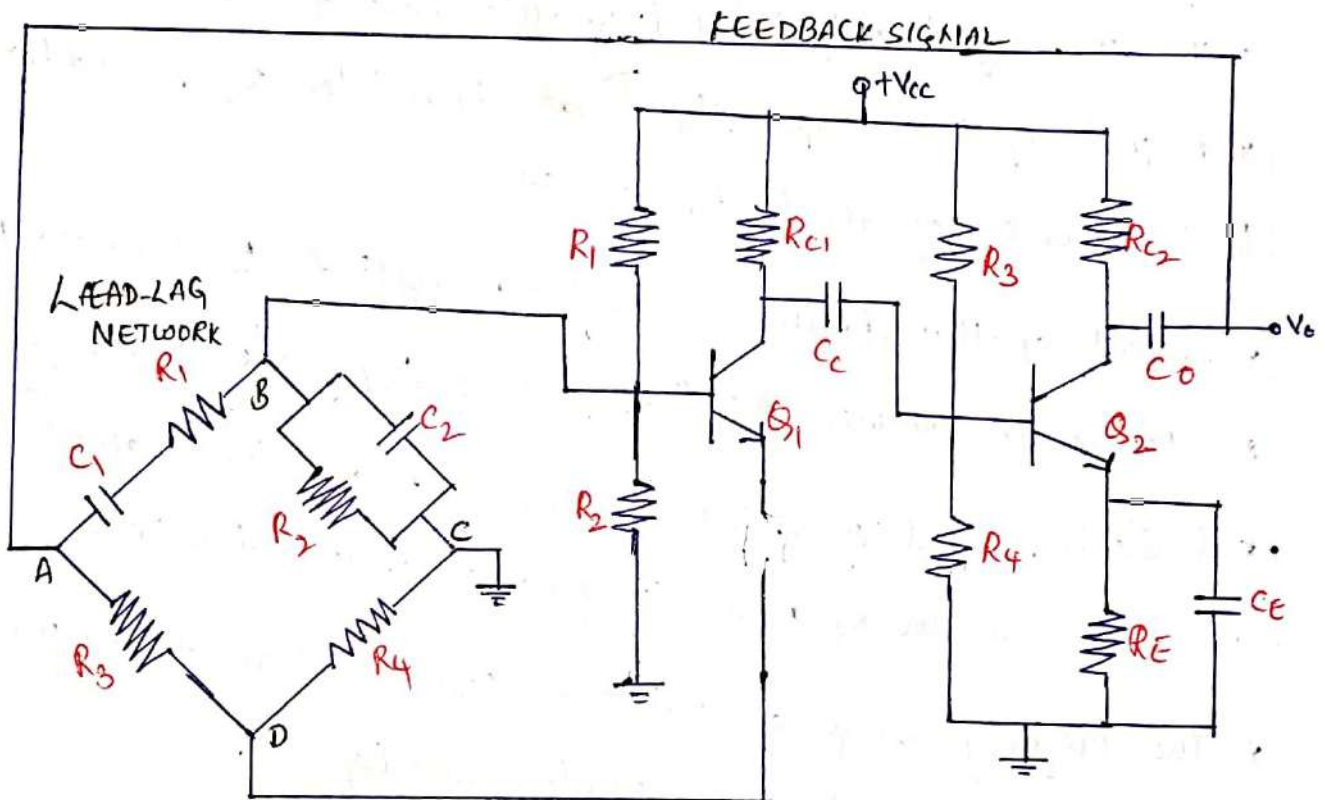


FIG: WIEIN BRIDGE OSCILLATOR USING BJT

### OPERATION :

\* The circuit is set in oscillation by any random change in base current of transistor  $Q_1$ , that may be due to noise inherent in the transistor or variation in voltage of dc supply.

\* This variation in base current is amplified in collector circuit of transistor  $Q_1$  but with a phase shift of  $180^\circ$ .

- \* The output of transistor  $Q_1$  is fed to the base of second transistor  $Q_2$  through capacitor  $C_4$ . Here  $Q_1$  acts as both oscillator & amplifier.
- \* Now a still further amplified and twice phase-reversed signal appears at the collector of the transistor  $Q_2$ .
- \* Having been inverted twice, the output signal will be in phase with the signal output input to the base of transistor  $Q_1$ .
- \* A part of the output signal at transistor  $Q_2$  is fed back to the input of the bridge circuit.
- \* A part of this feedback signal is applied to emitter resistor  $R_4$  where it produces negative feedback to provide constant o/p over a range of frequencies.
- \* Similarly, a part of feedback signal is applied across the base bias resistor  $R_2$  where it produces positive feedback.
- \* The frequency range of oscillator can be changed by varying  $R_1, R_2$  and  $C_1, C_2$  values of resistors and capacitors.

### CONSTRUCTION:

- \* Wein bridge oscillator is essentially a two-stage amplifier with an R-C bridge circuit (Wein bridge).
- \* Here, Wein bridge is a lead-lag network ( $R_1-C_1$  &  $R_2-C_2$ ). The phase shift across the network lags with increasing frequency and leads with decreasing frequency.



\* By adding Wein bridge feedback network, the oscillator becomes sensitive to a signal of only one particular frequency. 19

\* This particular frequency is that at which Wein bridge is balanced and for which the phase shift is  $0^\circ$ .

\* By employing Wein-bridge feedback network, frequency stability is increased.

\* In the bridge circuit  $R_1$  in series with  $C_1$ ,  $R_3$ ,  $R_4$  and  $R_2$  in parallel with  $C_2$  form the four arms.

\* From the analysis of the bridge circuit it is obvious that the bridge will be balanced only when,

$$\frac{R_3}{R_4} = \frac{R_1}{R_2}$$

$$R_3 R_2 = R_1 R_4$$

$$R_3 (R_2 \parallel C_2) = (R_1 \text{ series } C_1) R_4$$

$$R_3 \left[ \frac{R_2 * \frac{1}{j\omega C_2}}{R_2 + \frac{1}{j\omega C_2}} \right] = \left( R_1 + \frac{1}{j\omega C_1} \right) R_4 \rightarrow \textcircled{1}$$

$$R_3 \left[ \frac{R_2 / j\omega C_2}{j\omega C_2 R_2 + 1/j\omega C_2} \right] = \left[ R_1 - \frac{j}{\omega C_1} \right] R_4$$

$$R_3 \left[ \frac{R_2}{1 + j\omega R_2 C_2} \right] = \left[ R_1 - \frac{j}{\omega C_1} \right] R_4$$

$$R_2 R_3 = R_4 \left( R_1 - \frac{j}{\omega C_1} \right) (1 + j\omega R_2 C_2)$$

$$R_2 R_3 = R_4 \left( R_1 - \frac{j}{\omega C_1} + j \omega C_2 R_2 R_1 - \frac{j^2 \omega^2 C_2 R_2}{\omega C_1} \right) \quad 20$$

$$R_2 R_3 = R_4 \left( R_1 - \frac{j}{\omega C_1} + j \omega R_1 R_2 C_2 + \frac{C_2 R_2}{C_1} \right)$$

$$R_2 R_3 = R_1 R_4 - j \frac{R_4}{\omega C_1} + j \omega R_1 R_2 C_2 R_4 + R_2 R_4 \frac{C_2}{C_1}$$

$$R_2 R_3 - R_1 R_4 - \frac{C_2}{C_1} R_2 R_4 + j \left[ \frac{R_4}{\omega C_1} + R_1 R_2 C_2 R_4 \omega \right] = 0$$

Separating real and imaginary terms we have

$$R_2 R_3 - R_1 R_4 - \frac{C_2}{C_1} R_2 R_4 = 0$$

$$\frac{C_2}{C_1} R_2 R_4 = R_2 R_3 - R_1 R_4$$

$$\frac{C_2}{C_1} \cancel{R_2 R_4} = \cancel{R_2 R_4} \left[ \frac{R_2 R_3}{R_2 R_4} - \frac{R_1 R_4}{R_2 R_4} \right]$$

$$\frac{C_2}{C_1} = \frac{R_3}{R_4} - \frac{R_1}{R_2} \quad \frac{C}{C} = \frac{R_3}{R_4} - \frac{R}{R}$$

Equating imaginary parts to zero  $1 - \frac{R_3}{R_4} = 1$

$$\frac{R_4}{\omega C_1} + R_1 R_2 C_2 R_4 \omega = 0$$

$$\frac{R_4 - R_1 R_2 C_2 R_4 \omega^2 C_1}{\omega C_1} = 0$$

$$R_4 - R_1 R_2 C_1 C_2 R_4 \omega^2 = 0$$

$$R_1 R_2 C_1 C_2 R_4 \omega^2 = R_4$$

$$\frac{R_3}{R_4} = 2$$

$$R_3 = 2 R_4$$



$$\omega^2 = \frac{1}{R_1 R_2 C_1 C_2}$$

$$\omega = \frac{1}{\sqrt{R_1 R_2 C_1 C_2}}$$

$$2\pi f = \frac{1}{\sqrt{R_1 R_2 C_1 C_2}}$$

$$f = \frac{1}{2\pi \sqrt{R_1 R_2 C_1 C_2}}$$

If  $C_1 = C_2 = C$  and  $R_1 = R_2 = R$ , then

$$f = \frac{1}{2\pi CR}$$

$$R_3 = 2R_4$$

$C_1 = C_2 = C$ ,  $R_1 = R_2 = R$  then 2)  $C_1 \text{ (I) becomes}$

$$\frac{R_3 R}{1 + j\omega CR} = \frac{R_4 (1 + j\omega CR)}{j\omega CR}$$

$$jR_3 R \omega C = R_4 (1 + j\omega CR)^2$$

$$jR_3 R \omega C = R_4 (1 - \omega^2 R^2 C^2 + 2j\omega CR)$$

$$jR_3 R \omega C = R_4 - \omega^2 R^2 C^2 R_4 + 2j\omega CR R_4$$

$$R_4 - \omega^2 R^2 C^2 R_4 + j(2\omega CR R_4 - R_3 R \omega C) = 0$$

Equating imaginary part to zero

$$2\omega CR R_4 - R_3 R \omega C = 0$$

$$2\omega CR R_4 = R_3 R \omega C$$

$$R_3 = 2R_4$$

\* Thus, we see that in a bridge circuit the output will be in phase with the input only when the bridge is balanced i.e., at resonant frequency.

\* At all other frequencies the bridge is off-balance i.e., the voltage feedback and output voltage do not have the correct phase relationship for sustained oscillations.

\* The amplifier voltage gain,  $A = \frac{R_3 + R_4}{R_4} = \frac{R_3}{R_4} + \frac{R_4}{R_4} = \frac{R_3}{R_4} + 1$

$$R_3 = 2R_4 \Rightarrow A = \frac{2R_4}{R_4} + 1 \quad \boxed{A = 3}$$

\* The above corresponds with the feedback network attenuation of  $1/3$ . Thus in this case, voltage gain must be equal to or greater than 3 to sustain oscillations.

## LC OSCILLATORS:

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- \* The oscillators which use the elements  $L$  and  $C$  to produce the oscillations are called LC oscillators.
- \* The circuit using  $L$  and  $C$  is called tank circuit & Oscillatory circuit & tuned circuit.
- \* These oscillators are used for high frequency range from  $200\text{ kHz}$  to few  $\text{MHz}$ .
- \* Due to high frequency range, these oscillators are used for sources of RF (Radio frequency) range.

### OPERATION OF LC TANK CIRCUIT:

- \* The LC tank circuit consists of elements  $L$  and  $C$  connected in parallel as shown in the figure.

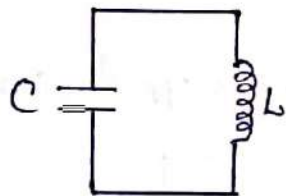


FIG: LC TANK CIRCUIT

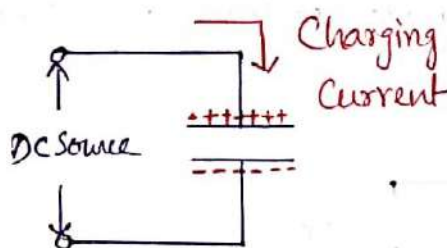


FIG: INITIAL CHARGING

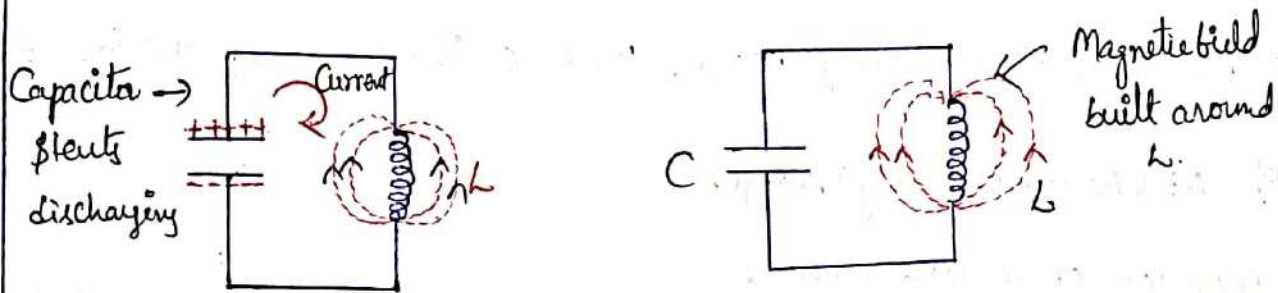
- \* Let capacitor is initially charged from a DC source with the polarity as shown in the figure.
- \* When the capacitor gets charged, the energy gets stored in a capacitor as Electrostatic Energy.
- \* Then such a charged capacitor is connected across inductor in a tank circuit, then the capacitor starts discharging through inductor.



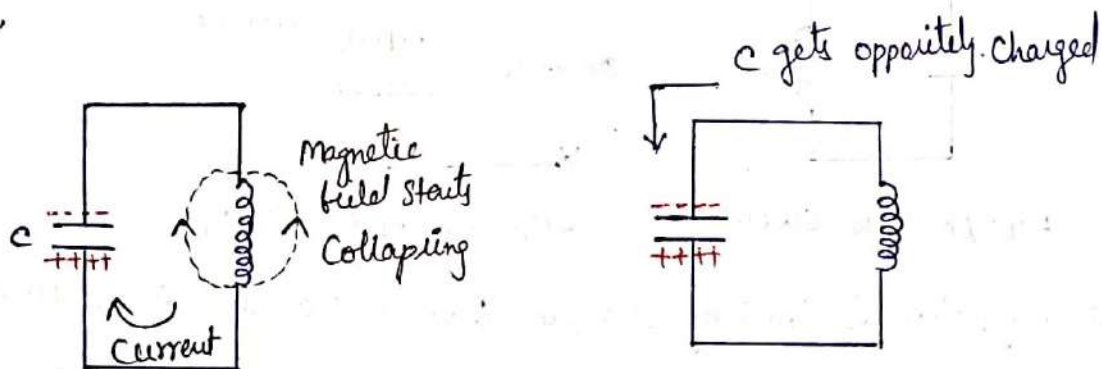
\* Then the Conventional Current flows due to this the magnetic field gets set up around the inductor  $L$ . Thus inductor stores energy in the form of Electromagnetic field. 23

\* When Capacitor is fully discharged, the maximum Current flows through the circuit.

\* At this instant all the electrostatic energy gets stored as a magnetic energy in the Inductor  $L$ .



\* Now, the magnetic field around  $L$  starts collapsing. As per Lenz's law. This starts the charging the capacitor with opposite polarity making lower plate positive and upper plate negative as shown in figure.



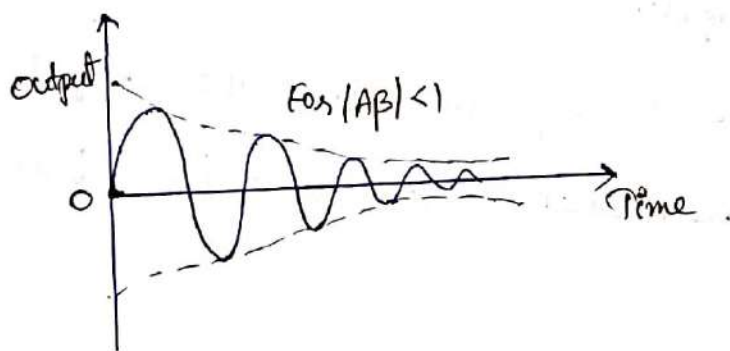
\* Then Capacitor again starts discharging through inductor  $L$ , but the direction of Current through the inductor is ' $L$ ' but the direction of Current through the inductor in the ' $L$ ' but the direction as shown in figure.

\* Again Electrostatic energy converted to magnetic field when the Capacitor is fully discharged and again magnetic field collapses and again the Capacitor gets charging in opposite direction <sup>24</sup>

\* Thus the Capacitor charges with alternate polarities and discharging producing alternating current in the tank circuit.

\* This alternating current generates electronic oscillations. But these oscillations of the Capacitor are damped because every time transfer of energy from L to C and C to L dissipates energy in the form of heat in the resistance of the coil and in the connecting wires in the form of electromagnetic radiation.

\* These losses decrease the amplitude of oscillating current gradually till it ceases. These are called as exponentially decaying oscillations or damped oscillations.



### GENERAL FORM OF AN L-C OSCILLATOR:

\* In general form of an oscillator, the amplifier section may be active devices such as vacuum tube, BJT, FET & op-amp may be used in the amplifier section.

\*  $Z_1$ ,  $Z_2$  &  $Z_3$  are the reactive elements constituting the feedback tank circuit which determines the frequency of oscillations.



\* Here,  $Z_1$  &  $Z_2$  serve as an ac voltage divider to the output voltage and feedback signal. 25

\* Thus, the voltage across  $Z_2$  is the feedback signal.

\* The equivalent circuit is as shown below.

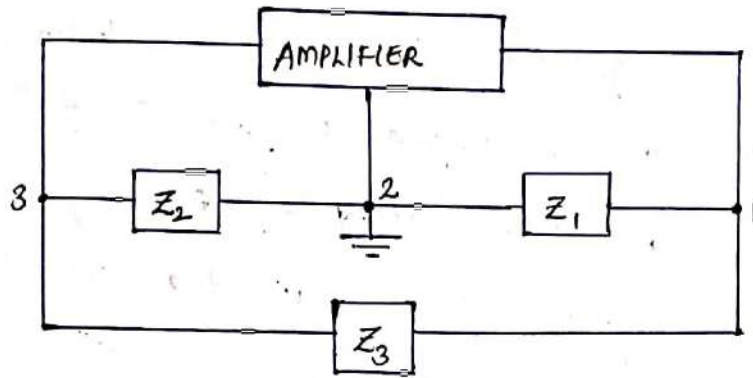


FIG: GENERAL FORM OF AN OSCILLATOR

\* The equivalent circuit is drawn with the following two assumptions  
 i.  $h_{ie}$  of transistor is negligibly small and therefore, the feedback source  $h_{ie}V_{out}$  is negligible.

ii.  $h_{oe}$  of the transistor is very small i.e., the output resistance  $1/h_{oe}$  is very large and, therefore  $1/h_{oe}$  is omitted from the equivalent circuit.

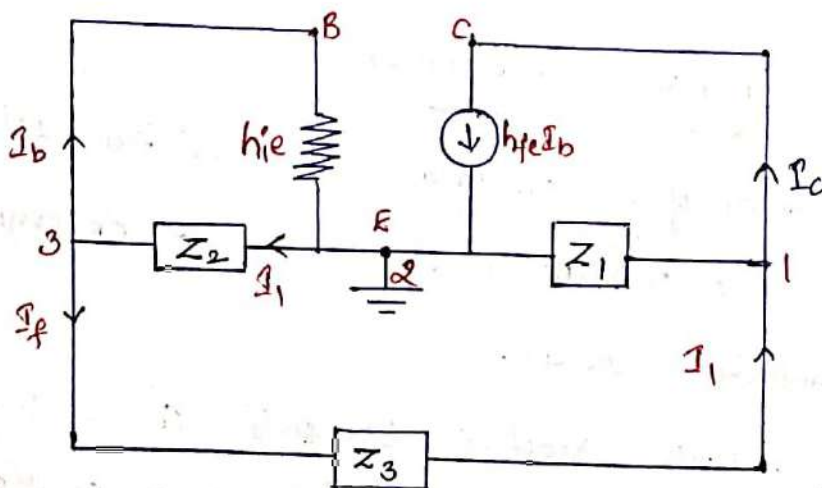


FIG: EQUIVALENT CIRCUIT

\* Let us determine the load impedance between output terminals 1 and 2. Here  $Z_2$  and  $h_{ie}$  are in parallel and their resultant impedance is in series with impedance  $Z_3$ . 26

$$\text{i.e., } Z_3 + (Z_2 \parallel h_{ie}) \rightarrow \textcircled{1}$$

\* The equivalent impedance of eq/① is in parallel with impedance  $Z_1$ .

\* Thus load impedance between output terminals is given as,

$$Z_L = Z_1 \parallel [Z_3 + (Z_2 \parallel h_{ie})]$$

$$= Z_1 \parallel \left[ Z_3 + \frac{Z_2 h_{ie}}{Z_2 + h_{ie}} \right]$$

$$= Z_1 \parallel \left[ \frac{Z_3(Z_2 + h_{ie}) + Z_2 h_{ie}}{Z_2 + h_{ie}} \right]$$

$$= \frac{Z_1 * \left[ \frac{Z_3(Z_2 + h_{ie}) + Z_2 h_{ie}}{Z_2 + h_{ie}} \right]}{Z_1 + \frac{Z_3(Z_2 + h_{ie}) + Z_2 h_{ie}}{Z_2 + h_{ie}}}$$

$$= \frac{Z_1 Z_3 (Z_2 + h_{ie}) + Z_1 Z_2 h_{ie} / Z_2 + h_{ie}}{Z_1 (Z_2 + h_{ie}) + Z_3 (Z_2 + h_{ie}) + Z_2 h_{ie} / Z_2 + h_{ie}}$$

$$= \frac{Z_1 Z_2 Z_3 + Z_1 Z_3 h_{ie} + Z_1 Z_2 h_{ie}}{Z_1 Z_2 + Z_1 h_{ie} + Z_2 Z_3 + Z_3 h_{ie} + Z_2 h_{ie}}$$

$$Z_L = \frac{Z_1 [Z_2 Z_3 + h_{ie} (Z_2 + Z_3)]}{h_{ie} [Z_1 + Z_2 + Z_3] + Z_1 Z_2 + Z_2 Z_3}$$



\* The Voltage gain of a CE amplifier without feedback is given as

$$A = \frac{-I_1}{I_b} + \frac{V_2}{V_1}$$

$$(\because V_2 = -I_c Z_L \quad (\because I_c = h_{fe} I_b))$$

$$= \frac{-h_{fe} I_b Z_L}{h_{ie} I_b}$$

$$V_1 = h_{ie} I_b \quad (2)$$

$$A = \frac{-h_{fe} Z_L}{h_{ie}}$$

\* The output voltage between terminals 1 and 2 is given as,

$$V_{out} = [Z_3 + (Z_2 \parallel h_{ie})] I_1$$

$$= \left[ Z_3 + \frac{Z_2 h_{ie}}{Z_2 + h_{ie}} \right] I_1$$

$$= \left[ \frac{Z_3 (Z_2 + h_{ie}) + Z_2 h_{ie}}{Z_2 + h_{ie}} \right] I_1$$

$$= \left[ \frac{Z_3 Z_2 + Z_3 h_{ie} + Z_2 h_{ie}}{Z_2 + h_{ie}} \right] I_1$$

$$= \left[ \frac{Z_2 Z_3 + h_{ie} (Z_2 + Z_3)}{Z_2 + h_{ie}} \right] I_1$$

\* The Voltage feedback to the input terminals 2 and 3 is given as

$$V_f = \frac{Z_2 h_{ie}}{Z_2 + h_{ie}} I_1$$

So, feedback fraction,  $\beta = \frac{V_f}{V_{out}}$

$$\beta = \frac{\frac{z_2 h_{ie}}{z_2 + h_{ie}} \times \mathcal{I}_1}{\frac{h_{ie}(z_2 + z_3) + z_2 z_3}{z_2 + h_{ie}} \times \mathcal{I}_1}$$

(28)

$$\beta = \frac{z_2 h_{ie}}{z_2 z_3 + h_{ie}(z_2 + z_3)}$$

\* Applying the criterion of oscillation i.e.,  $A\beta = -1$ , we have

$$\frac{-h_{fe} \times z_L}{h_{ie}} \times \frac{z_2 h_{ie}}{z_2 z_3 + h_{ie}(z_2 + z_3)} = 1$$

Substituting the value of  $z_L$ , then

$$-h_{fe} \left[ \frac{z_1 [h_{ie}(z_2 + z_3) + z_2 z_3]}{h_{ie}(z_1 + z_2 + z_3) + z_1 z_2 + z_2 z_3} \right] \times \frac{z_2}{z_2 z_3 + h_{ie}(z_2 + z_3)} = 1$$

$$\frac{h_{fe} z_1 [h_{ie}(z_2 + z_3) + z_2 z_3]}{h_{ie}(z_1 + z_2 + z_3) + z_1 z_2 + z_2 z_3} \times \left[ \frac{z_2}{z_2 z_3 + h_{ie}(z_2 + z_3)} \right] = -1$$

$$\frac{h_{fe} z_1 z_2}{h_{ie}(z_1 + z_2 + z_3) + z_1 z_2 + z_2 z_3} = -1$$

$$h_{fe} z_1 z_2 = -[h_{ie}(z_1 + z_2 + z_3) + z_1 z_2 + z_2 z_3]$$

$$h_{fe} z_1 z_2 + h_{ie}(z_1 + z_2 + z_3) + z_1 z_2 + z_2 z_3 = 0$$

$$h_{ie}(z_1 + z_2 + z_3) + z_1 z_2(1 + h_{fe}) + z_2 z_3 = 0 \rightarrow (2)$$

\* This equation 2 is the general equation for the oscillator.



\* The oscillator in which  $Z_1$  and  $Z_2$  are inductors and  $Z_3$  is a Capacitor then it is called Hartley oscillator.

\* The following figure represents the Hartley oscillator using BJT.

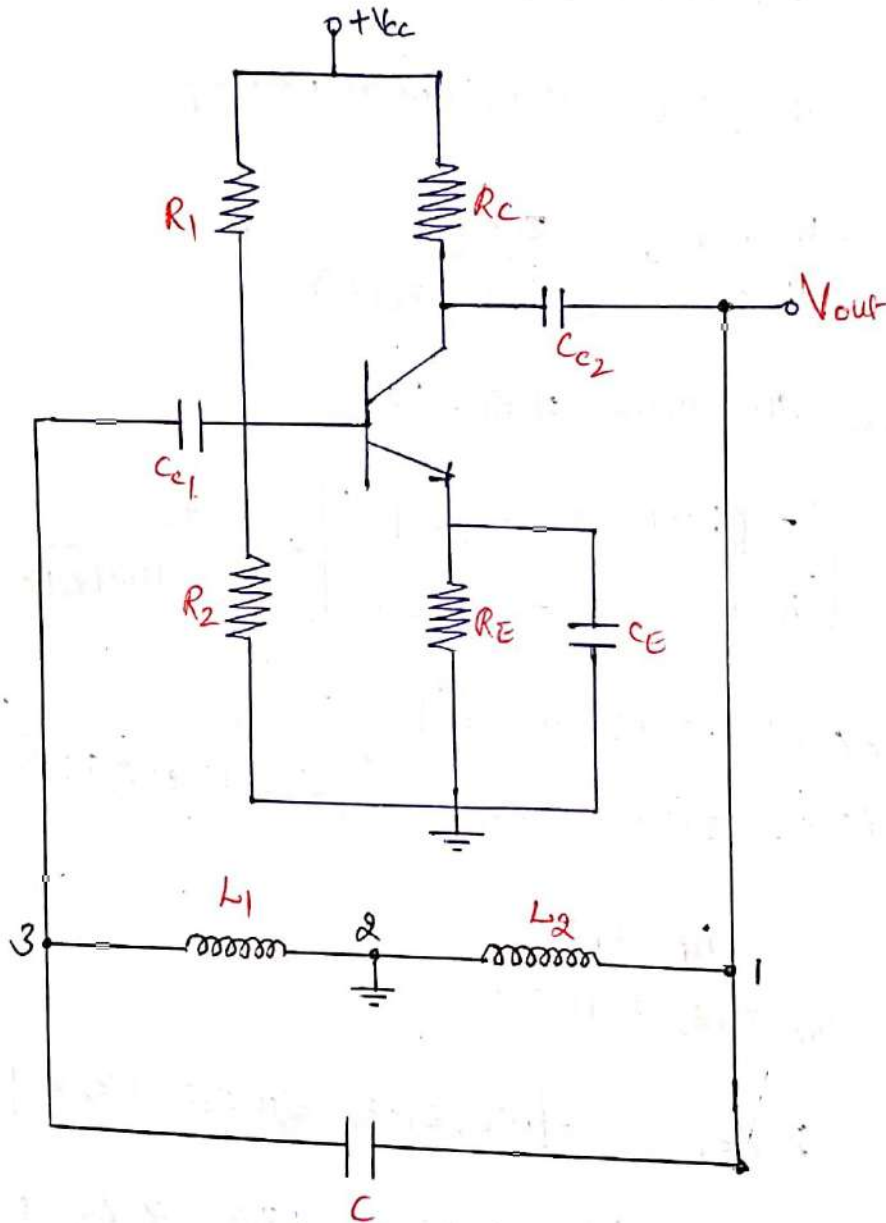


FIG: HARTLEY OSCILLATOR USING BJT

\* Resistors  $R_1$ ,  $R_2$  and  $R_E$  provides the necessary de-bias to the transistor.  $C_E$  is a bypass Capacitor.  $C_1$  and  $C_2$  are Coupling Capacitors.

\* The feedback network consisting of inductors  $L_1$  and  $L_2$  and Capacitor  $C$  determines the frequency of the oscillator. 30

\* When the supply voltage  $+V_{cc}$  is switched ON, a transient current is produced in the tank circuit.

\* The oscillatory current in the tank circuit produces a voltage across  $L_1$  and  $L_2$ . As terminal 2 is grounded, it is at zero potential.

\* If terminal 3 is at positive potential with respect to 2, at any instant, terminal 1 will be at a negative potential w.r.t. terminal 3 at the same instant.

\* Thus the phase difference between the terminals 1 and 3 is always  $180^\circ$ . In the CE mode, the transistor provides the phase difference of  $180^\circ$  between the input and output. Therefore, the total phase shift is  $360^\circ$ .

\* Thus at the frequency determined for the tank circuit, the necessary condition for sustained oscillations is satisfied. If the feedback is adjusted so that the loop gain  $A\beta = 1$ , the circuit acts as an oscillator.

### ANALYSIS:

\* In the Hartley oscillator,  $Z_1$  and  $Z_2$  are inductive reactances and  $Z_3$  is the capacitive reactance. Suppose  $m$  is the mutual inductance between the inductors, then,

$$Z_1 = j\omega L_1 + j\omega m$$

$$Z_2 = j\omega L_2 + j\omega m$$

$$Z_3 = \frac{1}{j\omega C} = \frac{-j}{\omega C}$$



The general equation for LC oscillator is, 31

$$h_{ie}(z_1 + z_2 + z_3) + z_1 z_2 (1 + h_{fe}) + z_2 z_3 = 0 \rightarrow (1)$$

Substituting  $z_1, z_2, z_3$  values in eq (1)

$$h_{ie} \left[ j\omega L_1 + j\omega m + j\omega L_2 + j\omega m - \frac{j}{\omega c} \right] + [j\omega L_1 + j\omega m][j\omega L_2 + j\omega m](1 + h_{fe}) + (j\omega L_1 + j\omega m) \left( j\omega L_2 + j\omega m - \frac{j}{\omega c} \right) = 0$$

$$j h_{ie} \omega \left[ L_1 + m + L_2 + m - \frac{1}{\omega^2 c} \right] + [-\omega^2 L_1 L_2 - \omega^2 L_1 m - \omega^2 L_2 m - \omega^2 m^2] (1 + h_{fe}) + \frac{j\omega L_1 + j\omega m}{\omega c} = 0$$

$$j h_{ie} \omega \left[ L_1 + L_2 + 2m - \frac{1}{\omega^2 c} \right] - \omega^2 [L_1 L_2 + L_1 m + L_2 m + m^2] (1 + h_{fe}) + \frac{L_1}{\omega c} + \frac{m}{\omega c} = 0$$

$$\frac{L_1}{c} + \frac{m}{c} = \frac{1}{\omega^2 c} [L_1 + L_2 + 2m] = 0$$

Equating imaginary term to zero

$$h_{ie} \omega \left[ L_1 + L_2 + 2m - \frac{1}{\omega^2 c} \right] = 0$$

$$L_1 + L_2 + 2m - \frac{1}{\omega^2 c} = 0$$

$$\frac{1}{\omega^2 c} = L_1 + L_2 + 2m$$

$$\omega^2 c = \frac{1}{L_1 + L_2 + 2m}$$

$$(\because L_{eq} = L_1 + L_2 + 2m)$$

$$\omega^2 c = \frac{1}{L_{eq}}$$

$$\omega^2 = \frac{1}{L_{eq} c}$$

$$(2\pi f)^2 = \frac{1}{L_{eq} c}$$

$$f^2 = \frac{1}{(2\pi)^2 L_{eq} C}$$

32

$$f = \frac{1}{\sqrt{(2\pi)^2 L_{eq} C}}$$

$$\boxed{f_0 = \frac{1}{2\pi\sqrt{L_{eq} C}}} \rightarrow \text{This is the resonant frequency.}$$

Equating real term to zero

$$-\omega^2 [L_1 L_2 + L_1 m + L_2 m + m^2] (1 + h_{fe}) + \frac{1}{\omega^2 C} (L_2 + m) = 0$$

$$-\omega^2 (L_2 + m) \left[ L_1 + L_2 + 2m (L_1 + m)(1 + h_{fe}) - \frac{1}{\omega^2 C} \right] = 0$$

$$(L_1 + m)(1 + h_{fe}) - \frac{1}{\omega^2 C} = 0$$

$$(L_1 + m)(1 + h_{fe}) = \frac{1}{\omega^2 C}$$

$$(1 + h_{fe}) = \frac{1}{\omega^2 C (L_1 + m)}$$

$$\left( \because \frac{1}{\omega^2 C} = L_1 + L_2 + 2m \right)$$

$$1 + h_{fe} = \frac{L_1 + L_2 + 2m}{L_1 + m} \Rightarrow 1 + h_{fe} = \frac{(L_1 + m) + (L_2 + m)}{L_1 + m}$$

$$1 + h_{fe} = 1 + \frac{L_2 + m}{L_1 + m} \quad = \frac{L_1 + m}{L_1 + m} + \frac{L_2 + m}{L_1 + m}$$

$$\boxed{h_{fe} = \frac{L_2 + m}{L_1 + m}}$$



\* As for other oscillator circuits, the loop gain must be greater than 1 to ensure that circuit oscillates

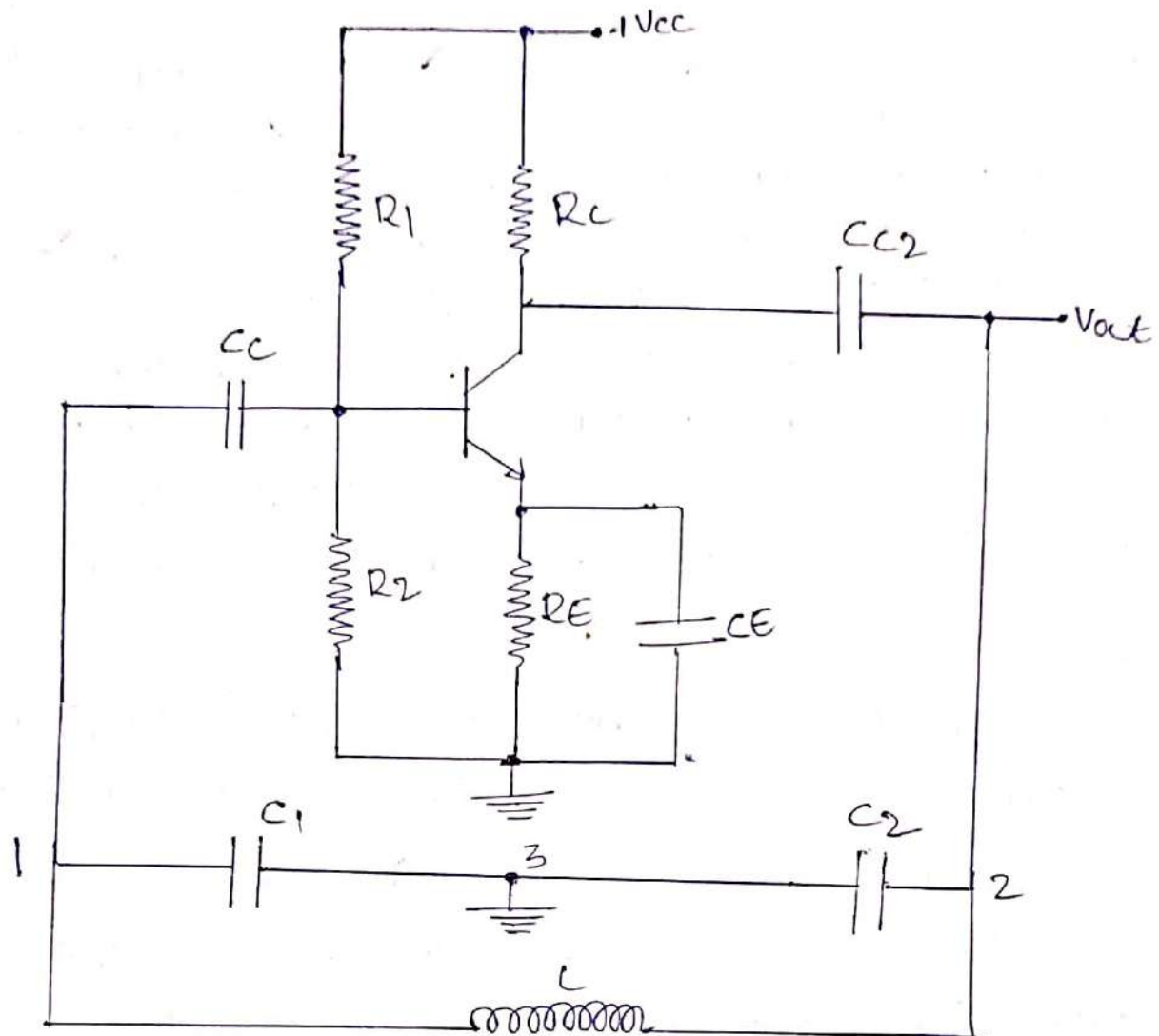
33

### FET HARTLEY OSCILLATOR:



## \* Colpitts Oscillator :-

(39)



Colpitts oscillator circuit diagram.

- \* In the Colpitt's Oscillator shown in above figure,  $Z_1$  and  $Z_2$  are capacitors and  $Z_3$  is an inductor.
- \* The Resistors  $R_1$ ,  $R_2$  and  $R_E$  provide the necessary d.c bias to the transistor.  $C_E$  is a bypass capacitor.
- \*  $C_B$  and  $C_C$  are coupling capacitors. The feedback network consisting of capacitors  $C_1$  and  $C_2$  and an inductor  $L$  determines the frequency of the oscillator.



When the supply voltage  $+V_{CC}$  is switched ON, a transient current is produced in the tank circuit and consequently, damped harmonic oscillations are set up in the circuit. The oscillator current in the tank circuit produces a.c voltages across  $C_1$  and  $C_2$ . As terminal 3 is earthed, it will be at zero potential. If terminal 1 is at a positive potential with respect to 3 at any instant, terminal 2 will be at a negative potential with respect to 3 at the same instant. Thus the phase difference between the terminals 1 and 2 is always  $180^\circ$ .

In the CE mode, the transistor provides the phase difference of  $180^\circ$  between the input and output. Therefore the total phase shift is  $360^\circ$ . Thus at the frequency determined for the tank circuit the necessary condition for sustained oscillations is satisfied. If the feedback is adjusted so that the loop gain  $AB = 1$ .

Analysis:-

$$Z_1 = \frac{1}{j\omega C_1} = \frac{-j}{\omega C_1}$$

$$Z_2 = \frac{1}{j\omega C_2} = \frac{-j}{\omega C_2}$$

$$Z_3 = j\omega L.$$

$$\begin{aligned} h_{ie} [z_1 + z_2 + z_3] + z_1 z_2 (1 + h_{fe}) + z_2 z_3 &= 0 \\ = h_{ie} \left[ \frac{-j}{\omega c_1} + \left( \frac{-j}{\omega c_2} \right) + j\omega L \right] + \left[ \frac{-j}{\omega c_1} \cdot \frac{j}{\omega c_2} \right] (1 + h_{fe}) \\ &+ \left( \frac{-j}{\omega c_2} \right) (j\omega L) = 0 \\ = h_{ie} \left[ \frac{-j}{\omega c_1} - \frac{j}{\omega c_2} + j\omega L \right] + \left[ \frac{-j}{\omega c_1} \right] \left[ \frac{-j}{\omega c_2} \right] (1 + h_{fe}) \\ &+ \left[ \frac{-j}{\omega c_2} \right] (j\omega L) = 0 \\ = -jh_{ie} \left[ \frac{1}{\omega c_1} + \frac{1}{\omega c_2} - \omega L \right] - \left[ \frac{1}{\omega^2 c_1 c_2} (1 + h_{fe}) \right] \frac{L}{c_2} = 0 \end{aligned}$$

equating imaginary term to zero.

$$-h_{fe} \left[ \frac{1}{\omega c_1} + \frac{1}{\omega c_2} - \omega L \right] = 0$$

$$\frac{1}{\omega c_1} + \frac{1}{\omega c_2} - \omega L = 0$$

$$\omega L = \frac{1}{\omega c_1} + \frac{1}{\omega c_2}$$

$$\omega L = \frac{\omega c_2 + \omega c_1}{\omega c_1 \omega c_2}$$

$$\omega L = \frac{\omega c_2 + \omega c_1}{\omega^2 c_1 c_2}$$

$$\omega L = \frac{\cancel{\omega} (c_1 + c_2)}{\omega^{\cancel{2}} c_1 c_2}$$



$$\omega L = \frac{C_2 + C_1}{\omega C_1 C_2}$$

$$\omega^2 L = \frac{C_2 + C_1}{C_1 C_2}$$

$$\omega^2 = \frac{C_1 + C_2}{(C_1 C_2) L}$$

$$\omega^2 = \frac{1}{C_{eq} L}$$

$$\therefore C_{eq} = \frac{C_1 C_2}{C_1 + C_2}$$

$$(2\pi f)^2 = \frac{1}{C_{eq} L}$$

$$\frac{1}{C_{eq}} = \frac{C_1 + C_2}{C_1 C_2}$$

$$f_0 = \frac{1}{2\pi \sqrt{C_{eq} L}}$$

$\therefore$  equating real term equal to zero.

$$-\frac{1}{\omega^2 C_1 C_2} (1 + h_{fe}) + \frac{L}{C_2} = 0$$

$$(1 + h_{fe}) \frac{1}{\omega^2 C_1 C_2} = \frac{L}{C_2}$$

$$(1 + h_{fe}) = \frac{L \omega^2 C_1 C_2}{C_2}$$

$$(1 + h_{fe}) = \left[ \frac{C_1 + C_2}{C_1 C_2} \right] L$$

$$(1 + h_{fe}) = \frac{C_1 + C_2}{C_2}$$

$$1 + h_{fe} = \frac{C_1 + C_2}{C_2}$$

where;

$$\omega^2 = \frac{C_1 + C_2}{C_1 C_2 \times L}$$

$$1 + h_{fe} = \frac{C_1}{C_2} + \frac{C_2}{C_2}$$

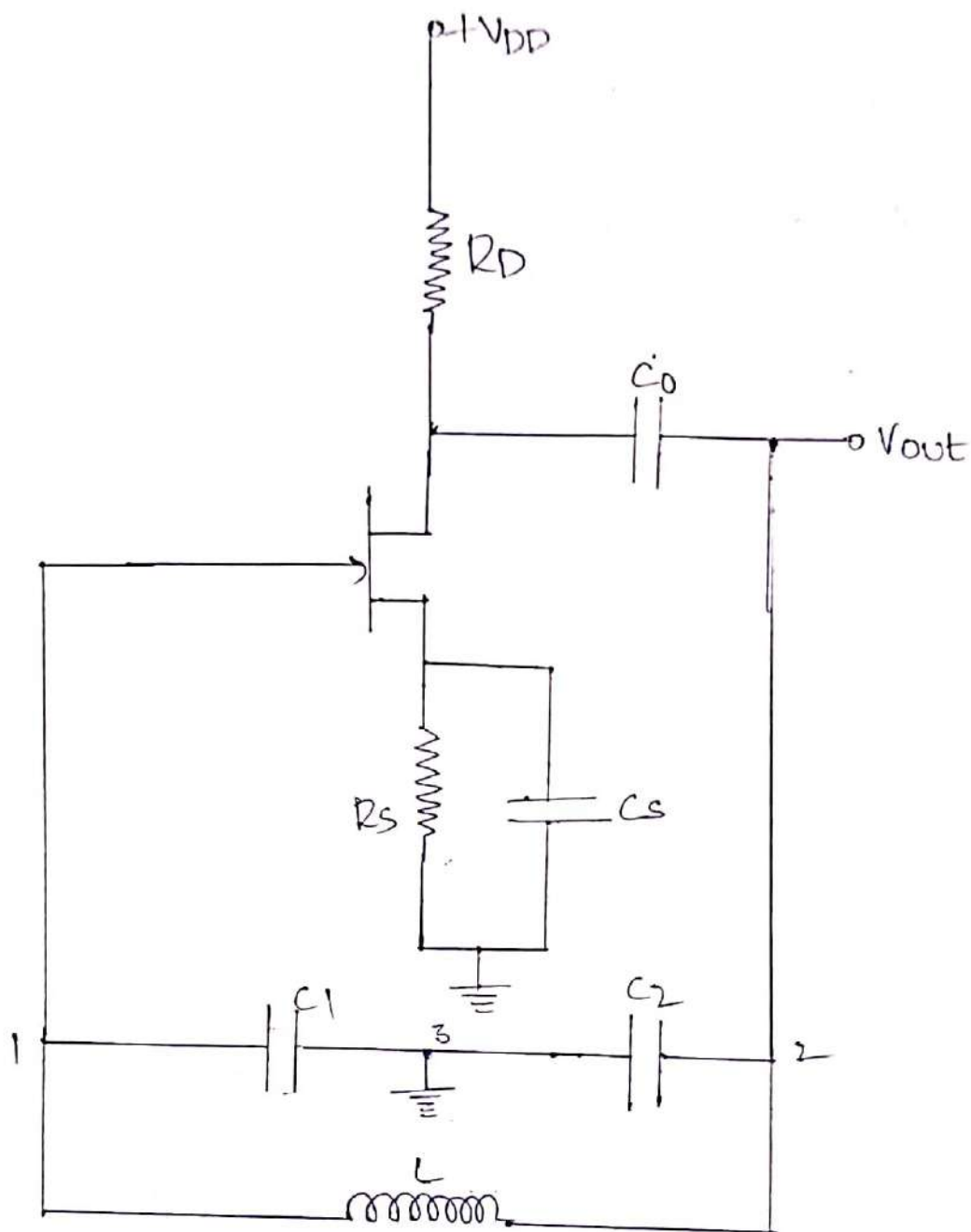
$$1 + h_{fe} = 1 + \frac{C_1}{C_2}$$

$$h_{fe} = \frac{C_1}{C_2}$$



Colpitt's oscillator using FET :-

(37)



Circuit diagram .

## \* crystal oscillators \*

A crystal oscillator, the usual electrical resonant circuit is placed by a mechanically vibrating crystal.

The crystal (usually quartz) has a high degree of stability in holding constant at whatever frequency the crystal is originally cut to operate.

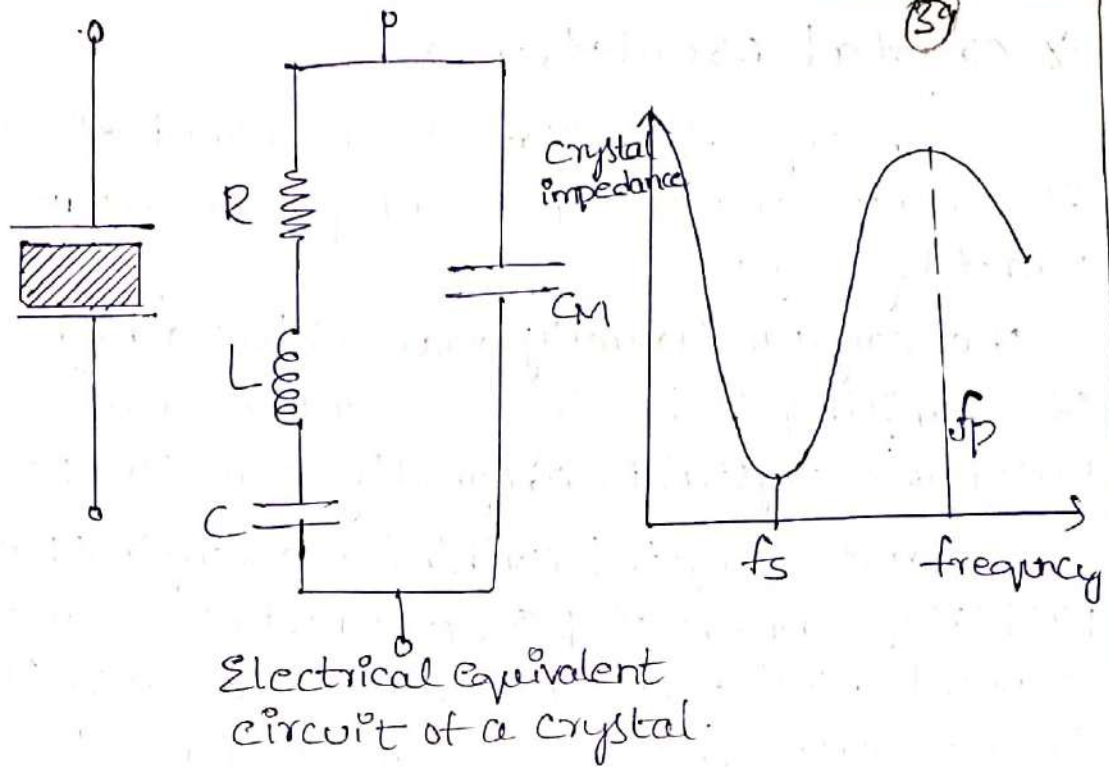
A quartz crystal exhibits a very important property known as piezo-electric effect. When a mechanical pressure is applied across the faces of the crystal, a voltage proportional to the applied mechanical pressure appears across the crystal surfaces. The crystal is distorted by an amount proportional to the applied voltage. An alternating voltage applied to a crystal causes it to vibrate at its natural frequency.

Besides quartz, the other substances that exhibit the piezo-electric effect are "Rochelle salt" and "tourmaline".

For use in electronic oscillators, the crystal is suitably cut and then mounted between two metal plates.

Although the crystal has electromechanical resonance but the crystal action can be represented by an electrical resonance circuit as shown in below figure.





The crystal actually behaves as a series R-L-C circuit in parallel, with  $C_M$  where  $C_M$  is the capacitance of the mounting electrodes.

Because of  $C_M$ , the crystal has two resonant frequency. one of these is the series resonant frequency  $f_s$  at which  $2\pi fL = \frac{1}{2\pi fC}$  and in this case crystal impedance is very low.

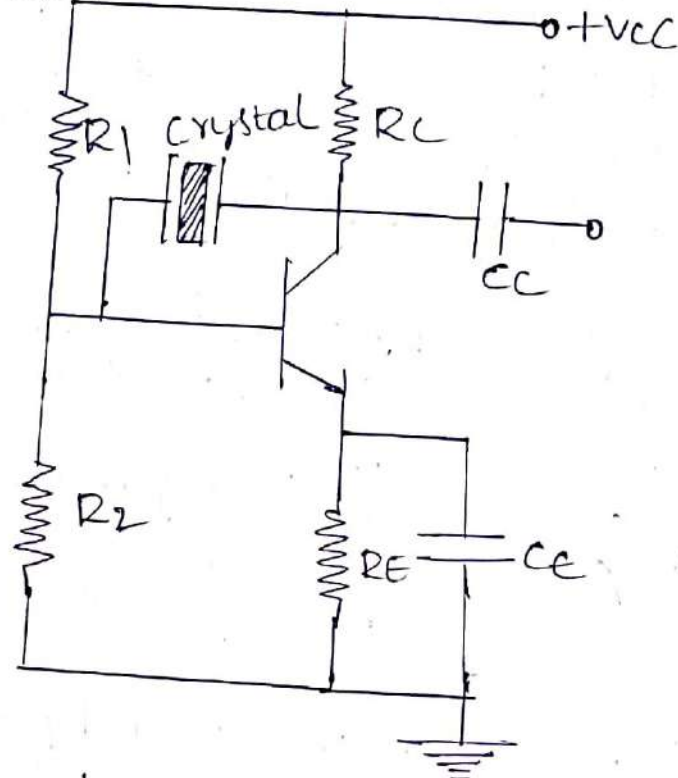
$$f_s = \frac{1}{2\pi\sqrt{LC}}$$

The other is parallel resonant frequency which is due to parallel resonance of capacitance of and resonance of the series circuit in.

In this case crystal impedance is very high.

$$f_p = \frac{1}{2\pi\sqrt{LC\left(1 + \frac{C}{C_M}\right)}}$$

\* Oscillator with crystal operating in series resonance :- (40)



In this mode of operation the crystal impedance is the smallest and the amount of positive feedback is the largest.

→ Resistors  $R_1$ ,  $R_2$  and  $R_E$  provides a voltage divider stabilized dc bias circuit.

The voltage feedback signal from the collector to the base is maximum when the crystal impedance is minimum (i.e; in series resonant mode). The coupling capacitor  $C_C$  has negligible impedance at this circuit operating frequency but blocks only dc between collector and base.

The circuit is generally called the "Pierce crystal".



The resulting circuit frequency of the (4) crystal. variations in supply voltage, transistor parameters, etc; have no effect on the circuit operating frequency which is held stabilized by the crystal.

The frequency of vibration is inversely proportional to the thickness of the crystal.

$$f = \frac{P}{2L} \sqrt{\frac{Y}{\rho}}$$

$Y$  = young modulus.

$\rho$  = density of material.

$P = 1, 2, 3, \dots$

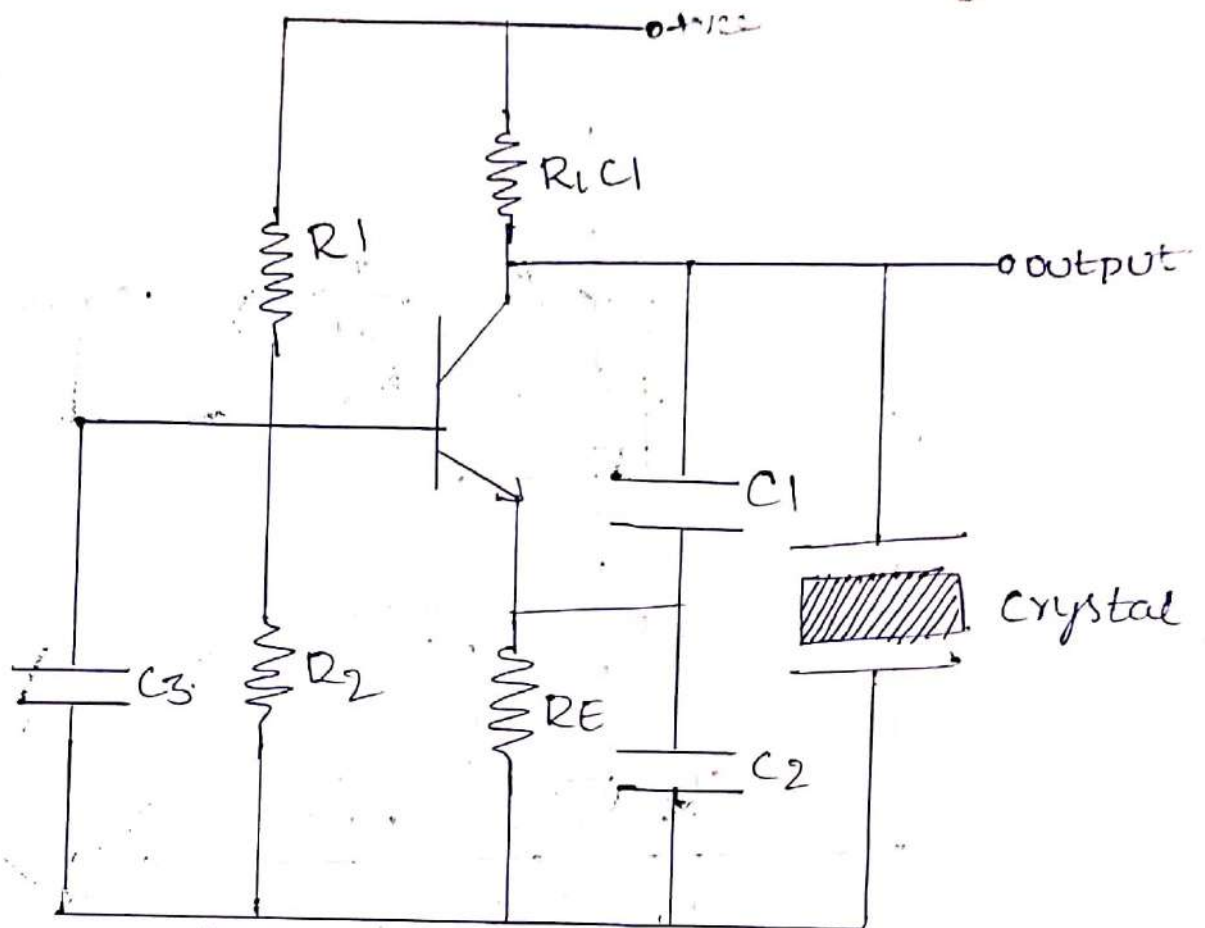
The crystal is suitably cut and polished to vibrate at a certain frequency and mounted between two metal plates.

\* Oscillator with crystal operating in parallel Resonance:-

Oscillator circuit with crystal operating in parallel resonance (a modified colpitt's oscillator circuit). Since the parallel-resonant impedance of a crystal is of a maximum value,  $C_1$  and  $C_2$  form a capacitive voltage divider which returns a portion of the output voltage divider which returns a pattern of the output voltage divider which returns a portion of the output voltage to the transistor emitter.

## Circuit diagram:-

(42)



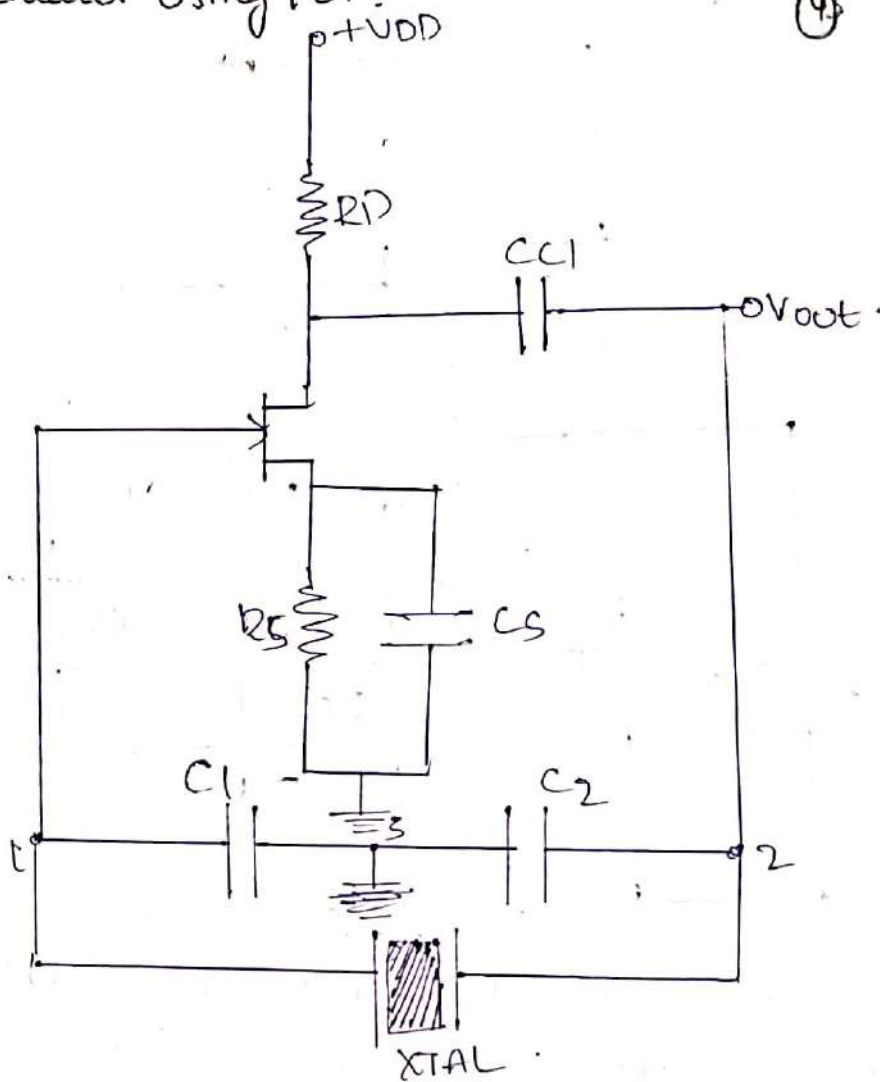
capacitor  $C_3$  provides an ac short circuit across  $R_2$  to ensure that the transistor base remains at a fixed voltage level. As the output voltage increases positively the emitter voltage also increases and since the base voltage is fixed, the base-emitter voltage is reduced.

The reduction in  $V_{BE}$  causes collector current  $I_C$  to diminishes and this in turn causes the collector voltage  $V_C$  to increase positively. Thus, the circuit is applying in our input, and a state of oscillation exists. The crystal in parallel with  $C_1$  and  $C_2$  permits maximum voltage feedback from the oscillator to emitter when its impedance is maximum.



Crystal oscillator using FET:-

④



## \* frequency stability of oscillators:- (44)

The frequency stability of an oscillator is a measure of its ability to maintain the required frequency as precisely as possible over as long a time interval as possible.

The accuracy of frequency calibration required may be anywhere between  $10^{-2}$  and  $10^{-10}$ . The main drawback in transistor oscillators is that the frequency of oscillation is not stable during a long time operation. The following are the factors which contribute to the change in frequency.

1. Due to change in temperature, the values of the frequency determining components, viz, resistors, inductors and capacitors change.
2. Due to variation in the power supply, unstable transistors, parameters, change in climatic conditions and again.
3. The effective resistance of the tank circuit is changed when the load is connected.
4. Due to variation in biasing conditions and loading condition.

The variation of frequency with temperature is given by,

$$S_{w,T} = \frac{\Delta w/w_0}{\Delta T/T_0} \text{ ppm/}^\circ\text{C} \text{ (parts per million per } ^\circ\text{C)}.$$



$\omega_0$  and  $T_0$  are the desired frequency of oscillation and the rating temperature respectively. In the absence of automatic temperature control, the effect of temperature on the resonant LC circuit can be reduced by selecting an inductance "L" with positive temperature coefficient and a capacitance "C" with negative temperature coefficient.

The loading effect may be minimised if the oscillator is coupled to the load loosely or by a circuit with high input resistance and low output resistance properties. The frequency stability is defined as.

$$S_w = \frac{d\theta}{d\omega}$$

where  $d\theta$  is the phase shift introduced for a small frequencies change in nominal frequency  $\omega_0$ , the circuit giving the largest value of  $\frac{d\theta}{d\omega}$  has the more stable oscillator frequency, if the  $\theta$  is infinite (an ideal inductor with zero series resistance) - this <sup>phase</sup> changes abruptly from  $-90^\circ$  to  $+90^\circ$ .

For tuned oscillators,  $S_w$  is directly proportional to the 'Q' of a tuned circuit. A frequency stability of one part in can be achieved with 'LC' ckt for LC oscillators, a tuned circuit must be lightly loaded to preserve high Q value.



(46)

As piezo-electric crystals have high  $Q$  values of the order of  $10^5$ , they can be used as parallel resonant circuits in oscillators to get very high-frequency stability of "1 ppm" (parts per million).

### \* Amplitude Stability of oscillators \*

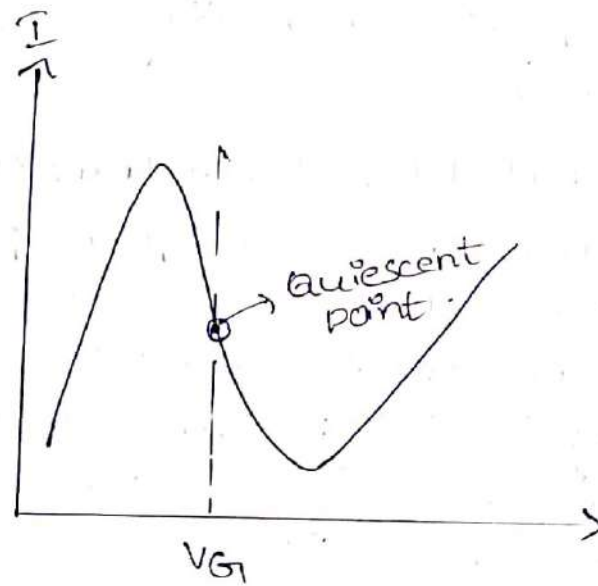
An oscillators do not require positive feedback for their operation. If the positive resistance of "LC" tank circuit is cancelled by introducing the right amount of negative resistance across the tank circuit, then the steady oscillation can be maintained.

The several devices such as dynatron, triatron, thermistor VJT and tunnel diode that exhibit a region of negative resistance with in the V-I characteristics, such devices operated in the negative resistance region are placed across a high  $Q$  for oscillation to occurs the negative resistance should be numerically less than the dynamic resistance of the tuned circuit.

In the case of "RC" circuit oscillators, the amplitude against variations due to fluctuation by aging of the transistors and other components can be stabilised by replacing the resistor in bridge by sensors which are temperature dependent resistors. Thus the



Stability in amplitude of the RC oscillators can easily be maintained. (4)



V-I characteristics of negative resistance oscillators.

Introduction:- Almost in all practical Amplifiers, Number of stages are cascaded to Amplify the weak signals to a sufficient level to operate the output device (loud speaker).

→ In such Amplifiers the function of first few stages is only to amplify the voltage but last stage is designed to provide maximum power to drive the output device (loud speaker). This final stage is called power Amplifier stage.

Multi-stage Amplifier.

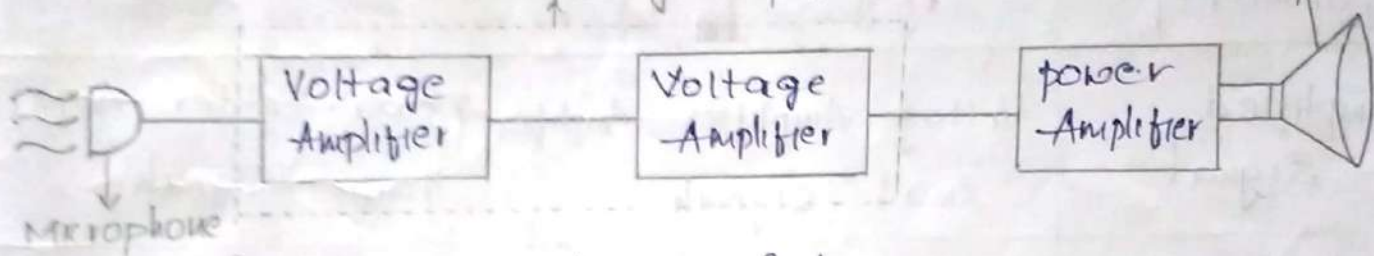


Fig:- Public Address System.

- Microphone Converts the sound to electrical signal and loud-speaker Converts electrical signal to sound signal again.
- When a person speaks in microphone, it converts sound signal into electrical signal.
- A electrical signal produced is of very low voltage (a few milli Volts).
- If this signal is fed to the speaker directly, it will not be in position to drive the speaker.
- Therefore voltage level of the signal is first increased to sufficient level (a few volts) by passing it through a number of stages of voltage amplifier.
- This amplified voltage signal is then fed to the final stage of multi stage amplifier which is capable to deliver the required power to drive the speaker.
- The speaker finally converts the electrical signal into sound signal.



→ So, we Conclude that In final stage, we have to apply the power Amplifier to transfer maximum power (or) to deliver maximum power to the output device.

\* Differences between Voltage Amplifier and power Amplifier:-

→ The main function of a voltage Amplifier is to amplifies the signals upto certain level.

→ The main function of power Amplifier is to deliver maximum power to the load (loud speaker.)

parameter	Voltage Amplifier	power Amplifier.
Amplified Signal	Voltage Amplifier Amplifies Small signals	Power Amplifier Amplifies Large signals.
Output power	power output is low	Power output is high
output impedance	output impedance is low	Output impedance is Very low.
input impedance	input impedance is high	input impedance is low
Size of the Amplifier	Size of the voltage Amplifier is small	Size of the power Amplifier is large.
Usage of Transistor	Normal transistor is used	power transistor is used.
output delivered to the load	unable to deliver maximum power to the load.	It delivers maximum power to the load.
Signals handled	It handles small signals	It handles large signals
Applications	used in voltage Amplifier	used in public address system (telephones.)



## \* Classification of power Amplifier:-

We have different types of power Amplifiers they are:

1. Class - A power Amplifier.
2. Class - B power Amplifier.
3. Class - C power Amplifier.
4. Class - AB power Amplifier.

(1.) Class - A power Amplifier:- The power Amplifier in which the operating point is adjusted as the Collector Current flows during whole cycle of the input signal is known as class-A power Amplifier.

→ The Q-point is kept at the centre of the active region.

→ When an ac input signal is applied, the Collector Voltage and the Collector Current varies simultaneously.

→ In class-A power Amplifier, distortion is low because output signal is reproduced during full cycle of the input signal.

→ Conduction Angle in class-A power Amplifier is  $360^\circ$ .

→ In class-A power Amplifier, efficiency is low.

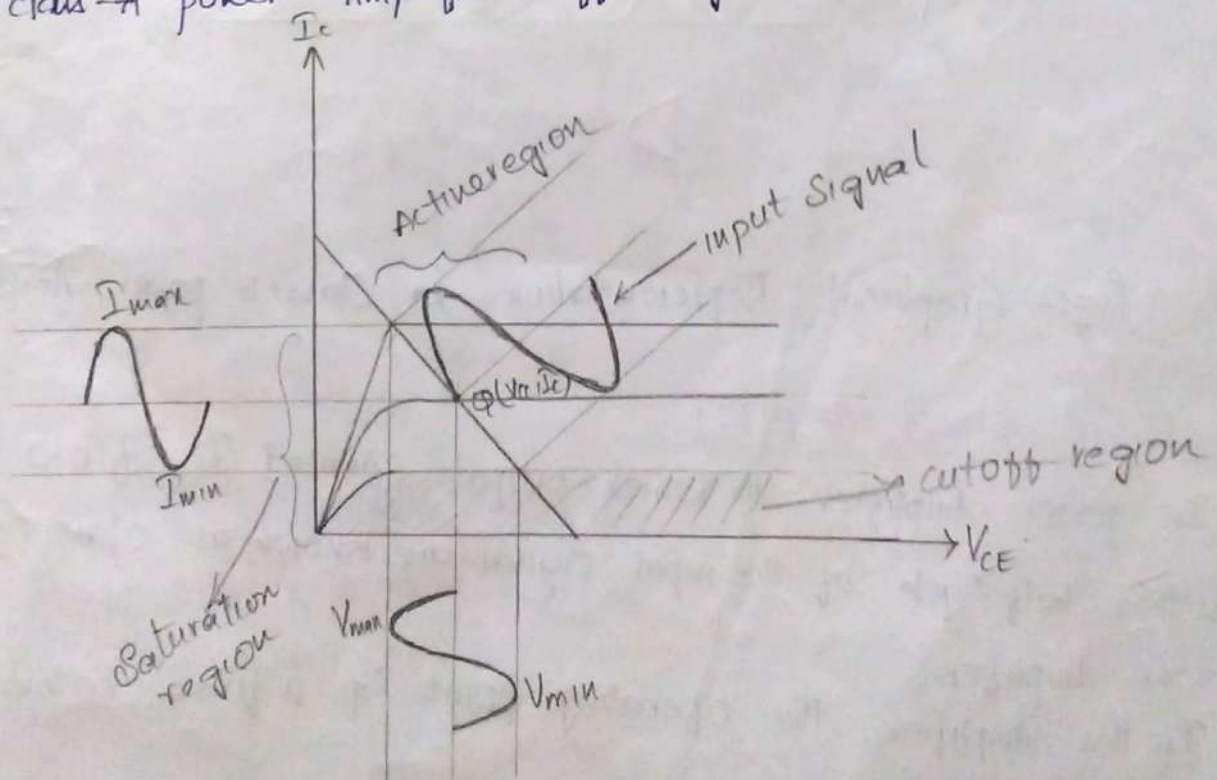


Fig: Graphical Representation of class-A power Amplifier



### (2.) Class-B power Amplifier:-

- The power Amplifier in which the operating point is so adjusted that the Collector Current flows during the positive half cycle of the input signal.
- Conduction Angle in class-B power Amplifier is  $180^\circ$ .
- The operating point ' $Q$ ' is fixed near to x-axis.  
i.e. the cycle of the input signal is in active region and Negative cycle of the input signal is in Cutoff region.
- The Graphical representation of class-B power Amplifier is shown in figure below.

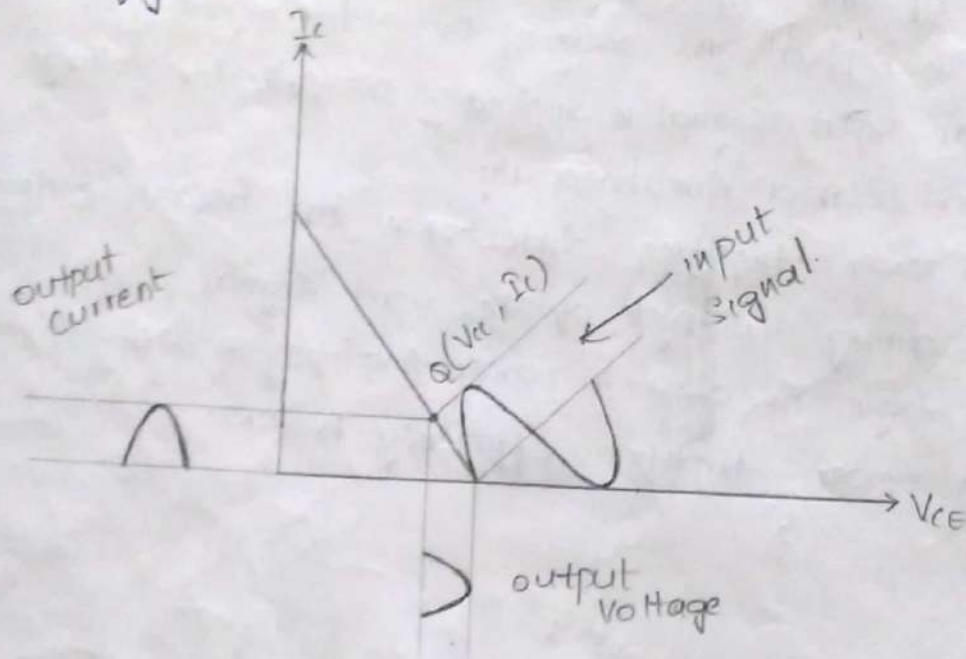


Fig:- Graphical Representation for Class-B power Amplifier.

### (3.) Class-C power Amplifier:-

- In power Amplifier in which output current  $I_c$  flows for less than half cycle of the input signal are known as class-C power Amplifier.
- In this Amplifier the operating point ' $Q$ ' is fixed below the x-axis.
- class-C power Amplifiers are used in tuned amplifiers to amplify the narrow band up frequencies.

Conduction angle of class-C power Amplifier is  $180^\circ$  less than  $180^\circ$ .

The Graphical representation is shown below.

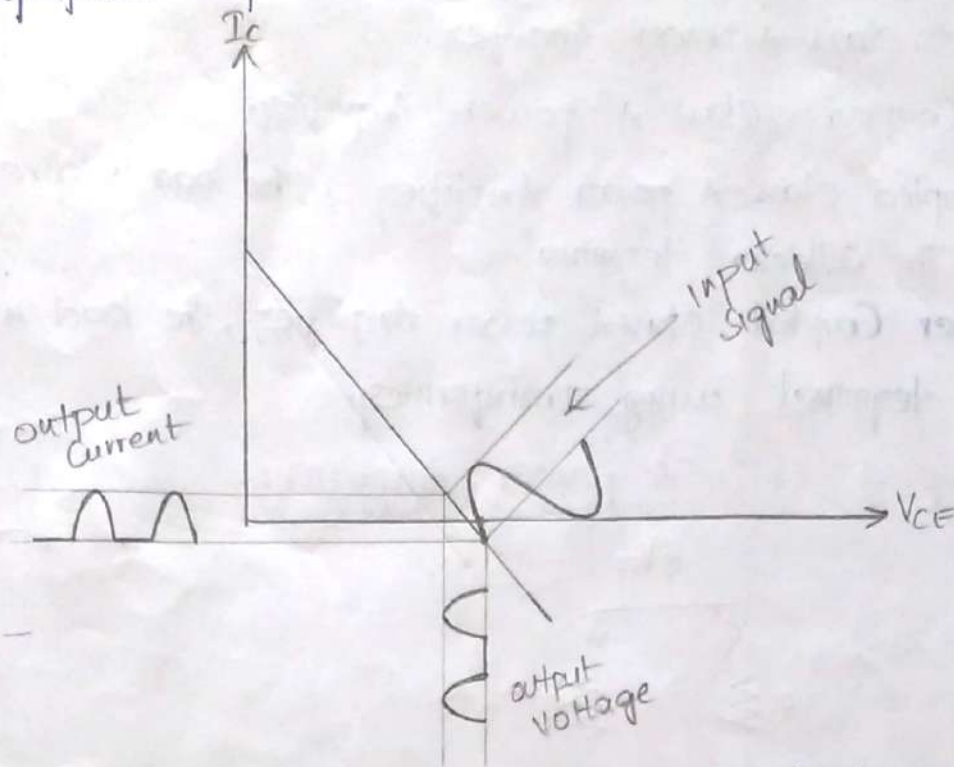


Fig:- The Graphical representation of class-C power Amplifier.

#### 4.) Class-AB power Amplifier:-

- The power Amplifier in which the output Current flows for more than half Cycle and less than full Cycle of the input signal.
- The Conduction Angle of class AB power Amplifier is between class-A and class-B power Amplifiers. Conduction angles (ie,  $< 360^\circ$  &  $> 180^\circ$ )
- The Graphical representation is shown in the graph below.

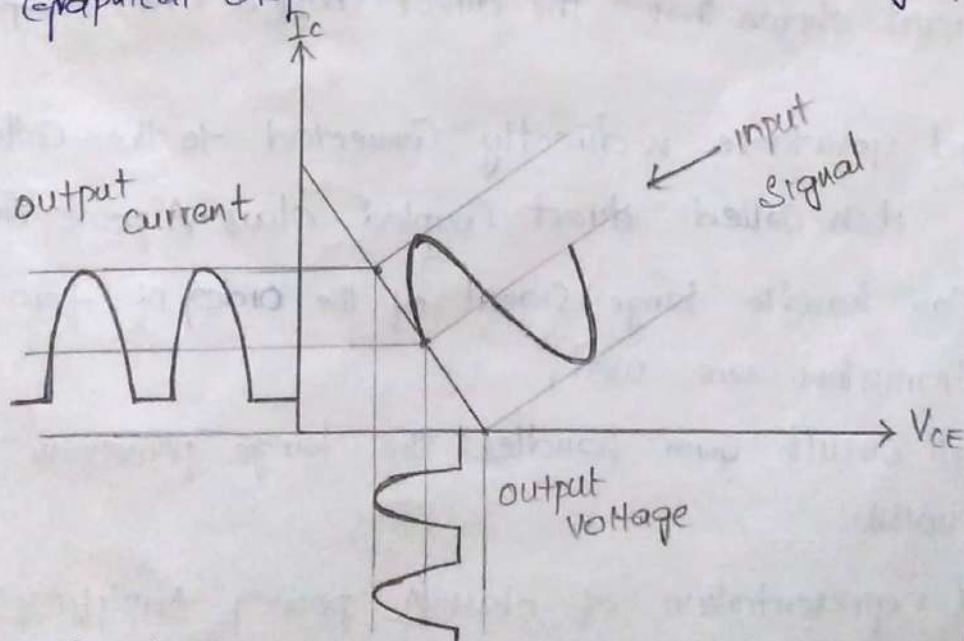


Fig:- Graphical representation of class-AB power Amplifier.



## (1.) Class-A power Amplifier:-

The class-A power Amplifier is classified into two types.

(a) Direct Coupled Class-A power Amplifier.

(b) Transformer Coupled Class-A power Amplifier.

→ In direct Coupled class-A power Amplifier, The load is directly connected to the Collector terminal.

→ In Transformer Coupled class-A power Amplifier, the load is connected to the Collector terminal using transformer.

### (A.) Direct Coupled class-A power Amplifier:-

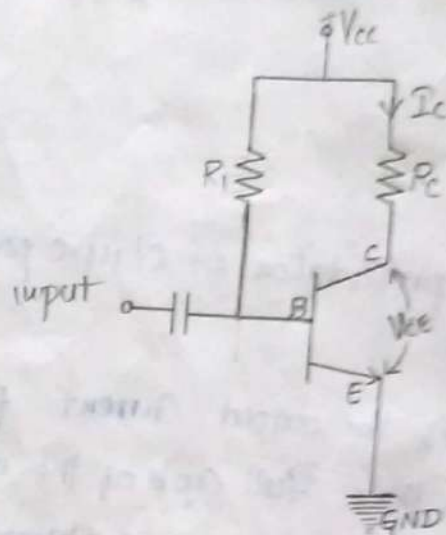


fig- Direct Coupled class-A power Amplifier.

### DC - Operation :-

- The above circuit shows that the direct Coupled class-A power Amplifier.
- Here, the load resistance is directly connected to the Collector terminal. Hence it is called direct Coupled class-A power Amplifier.
- This circuit can handle large signal of the order of few volts, hence power transistors are used.
- So, the overall circuit can handle the large power in the range of few volts.
- The graphical representation of class-A power Amplifier is shown in figure below.

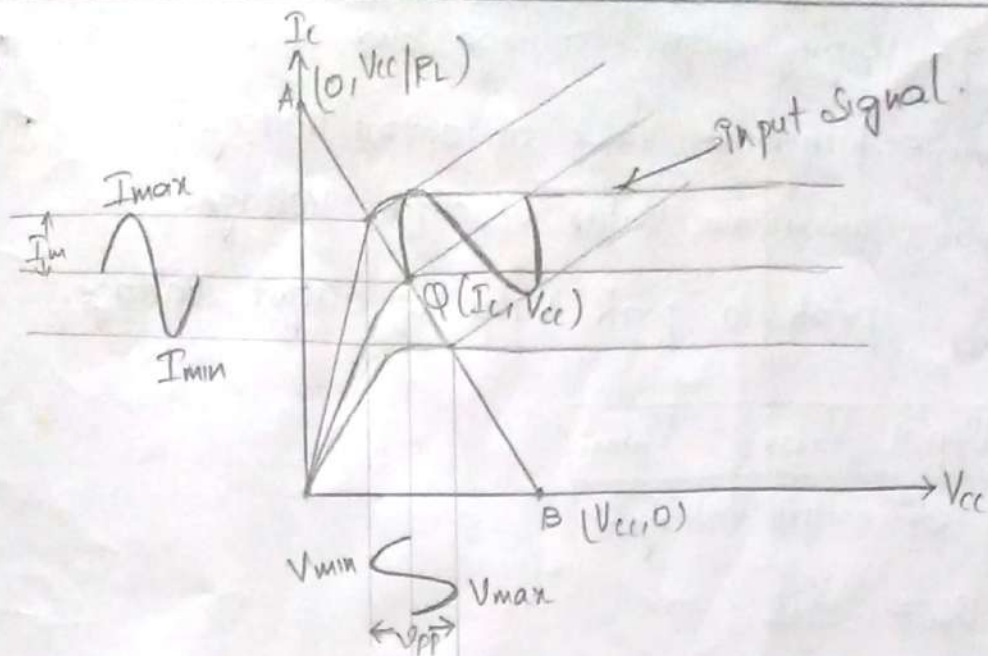


Fig 1: Graphical representation of Direct Coupled Class-A power Amplifier.

→ The Collector Supply voltage  $V_{cc}$  under resistance  $R_B$ , decides the DC base bias Current.

→ Apply Kirchhoff's voltage law to the output circuit, we get,

$$V_{cc} - I_c R_c - V_{ce} = 0$$

$$V_{cc} = I_c R_c + V_{ce}$$

$$I_c = \frac{V_{cc} - V_{ce}}{R_c}$$

→ The DC input power is provided by the Supply Voltage.

→ The Collector Current ( $I_c$ ) drawn the dc Current without AC input signal, hence the dc input power is given by.

$$P_{dc} = V_{cc} \times I_c$$

### AC-Operation:-

→ when an AC input signal is applied, the base Current varies sinusoidally, due to this Collector Current  $I_c$  and  $V_{ce}$  also varies sinusoidally,

→ The Varying output voltage and output Current delivers an AC power to the load.



→ From the graph Varying output voltage and output Current given by.

$V_{min}$  = minimum value of output voltage.

$V_{max}$  = maximum value of output voltage.

$V_{pp}$  = peak to peak value of output voltage.

$$V_{pp} = V_{max} - V_{min}$$

→  $V_m$  = peak of output voltage.

$$V_m = \frac{V_{pp}}{2}$$

So, we get.

$$V_m = \frac{V_{max} - V_{min}}{2} \rightarrow (1)$$

→ Similarly, the output Current can be given by.

$I_{min}$  = Minimum value of output Current.

$I_{max}$  = Maximum value of output Current.

$I_{pp}$  = Peak to peak value of output Current

$$I_{pp} = I_{max} - I_{min}$$

→  $I_m$  = peak of output Current

$$I_m = \frac{I_{pp}}{2}$$

$$I_m = \frac{I_{max} - I_{min}}{2} \rightarrow (2)$$

→ Hence, the RMS value of Alternating output voltage and output Current can be given as  $V_{rms} = \frac{V_m}{\sqrt{2}}$  and  $I_{rms} = \frac{I_m}{\sqrt{2}}$ .

∴ The RMS value of AC power is given by.

$$P_{AC} = V_{rms} \times I_{rms}$$

$$P_{AC} = \frac{V_m}{\sqrt{2}} \times \frac{I_m}{\sqrt{2}}$$

$$\therefore P_{AC} = \frac{V_m I_m}{2}$$

from equations ① & ②

$$P_{AC} = \frac{(V_{max} - V_{min})}{2} \times \frac{(I_{max} - I_{min})}{2}$$

$$\therefore P_{AC} = \frac{(V_{max} - V_{min})(I_{max} - I_{min})}{8}$$

Efficiency:- Efficiency is defined as the ratio of AC output power to the DC output power.

$$\eta = \frac{P_{AC}}{P_{DC}} = \frac{\text{a.c. power delivered to the load}}{\text{total power delivered by d.c. supply}}$$

$$\eta = \frac{P_{AC}}{P_{DC}} = \frac{(V_{max} - V_{min})(I_{max} - I_{min})}{8 V_{CC} I_C}$$

$$\eta = \frac{(V_{max} - V_{min})(I_{max} - I_{min})}{8 V_{CC} I_C}$$

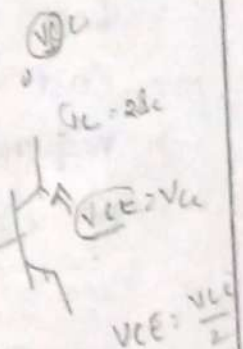
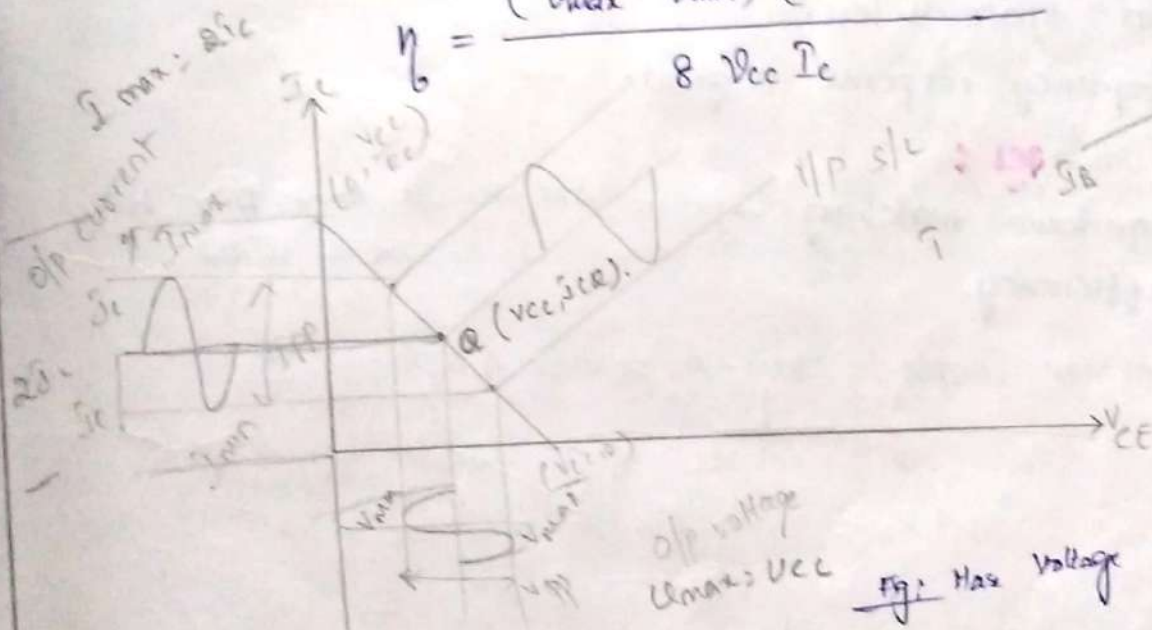


Fig: Max Voltage and Current swing

we know that,  $\eta = \frac{P_{AC}}{P_{DC}} \times 100$

from the above Graph  $V_{max} = V_{CC}$  and  $I_{max} = 2I_C$

Now,

$$\eta = \frac{(V_{max} - V_{min})(I_{max} - I_{min})}{V_{CC} \times I_C} \times 100$$



from equations ① & ②

$$P_{AC} = \frac{(V_{max} - V_{min})}{2} \times \frac{(I_{max} - I_{min})}{2}$$

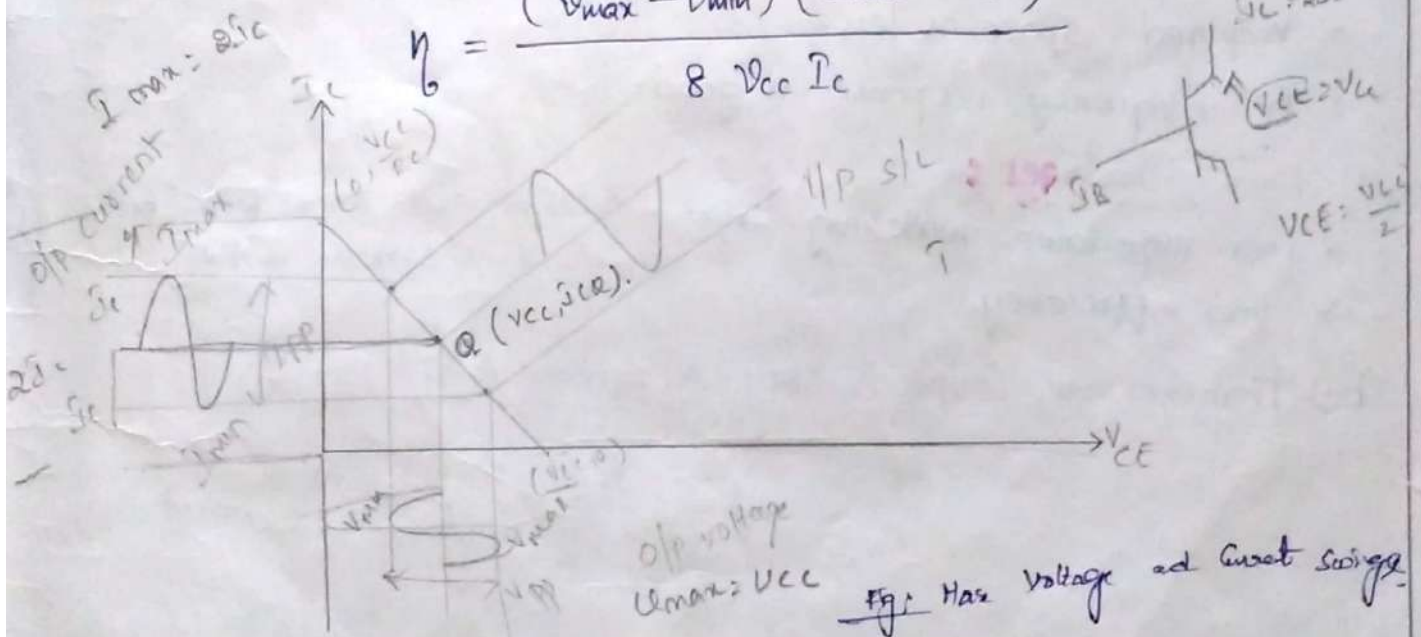
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Now,

$$\eta = \frac{(V_{max} - V_{min})(I_{max} - I_{min})}{8 V_{CC} I_C} \times 100$$

$$= \frac{(V_{cc} - 0)(2I_c - 0)}{8 V_{cc} \times I_c} \times 100$$

$$= \frac{\frac{2 V_{cc} I_c}{8 V_{cc} I_c}}{4} \times 100$$

$$= \frac{1}{4} \times 100$$

$$= 0.25 \times 100$$

$$\boxed{\eta = 25\%}$$

### \* Advantages:-

- Simple to design.
- Less number of Components are required.
- Cost is low.
- Required Space is less.
- The frequency response is good.

### \* Disadvantages:-

- poor impedance matching. → power dissipation is more. Heat sink is essential.
- low efficiency.

### (b) Transformer Coupled class-A power Amplifier:-

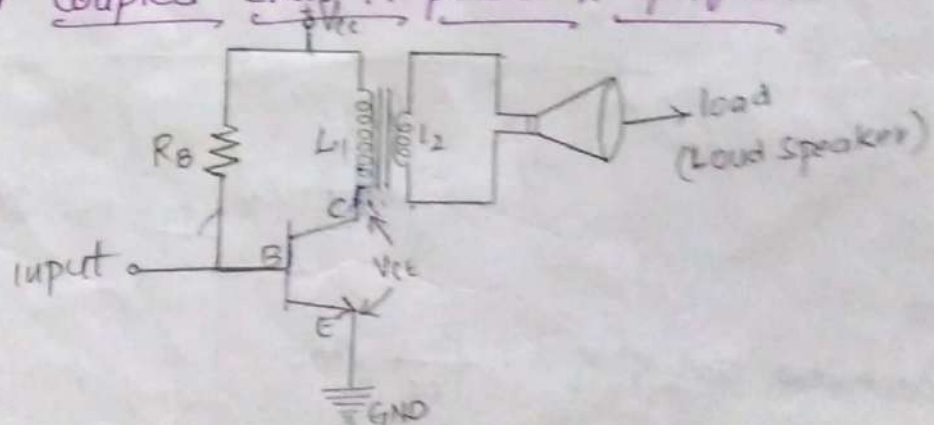


fig:- Circuit diagram for Transformer Coupled class-A power Amplifier.

- If maximum power is transferred to the load, the impedance matching is necessary.
- Impedance matching is poor in case of direct Coupled class-A power Amplifier. because, the loud speaker impedance (4- $\Omega$  to 16- $\Omega$ )



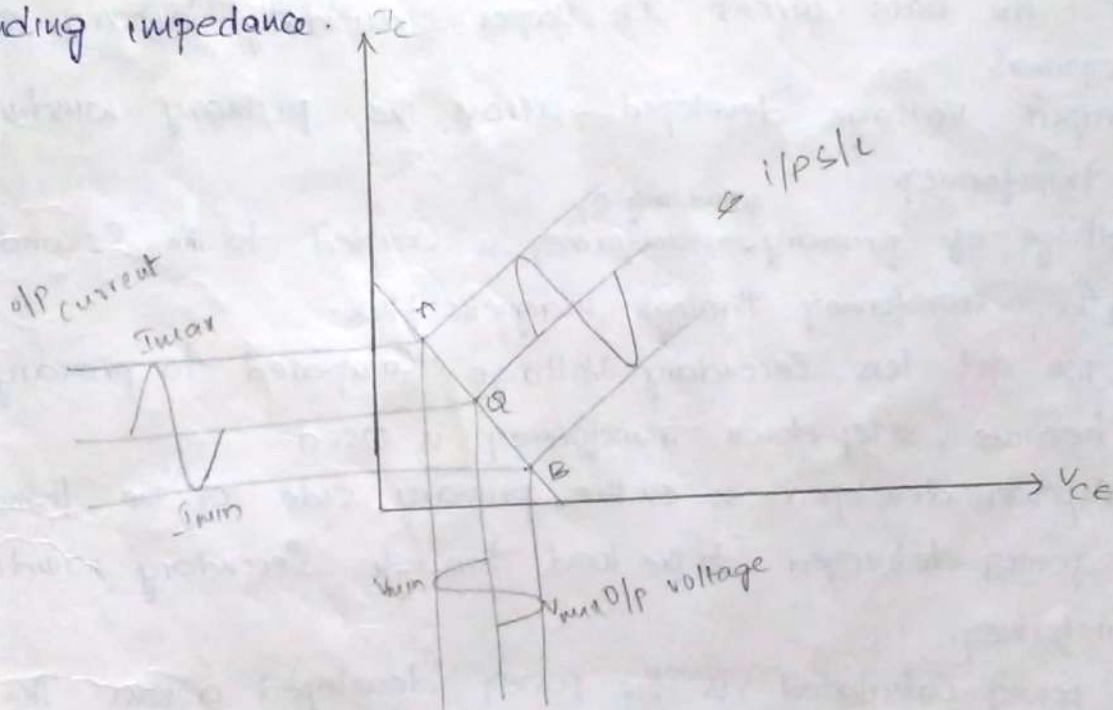
less than the output impedance of the direct Coupled class-A power Amplifier. To Overcome this problem by using Transformer coupled Class-A power Amplifier.

The circuit diagram for Transformer Coupled Class-A power Amplifier is shown in above figure.

→ Here the Transformer is directly Coupled to (or) Connected to the Collector terminal.

→ In the above figure we use, step down Transformer, in that Secondary Voltage is less than the primary voltage. So, high voltage side has always high impedance and the low voltage side has always low impedance.

→ So, the Secondary winding impedance is less than the primary winding impedance.



### \* DC - Operation :-

→ it is assumed that the winding resistance is ' $0\Omega$ '. because, no resistor is connected between Supply Voltage and the Collector terminal. So, no voltage drop across the primary winding of the Transformer.

→ The slope of the dc load line is reciprocal of the dc resistance.

→ In this circuit we have zero resistance in the winding. So, the



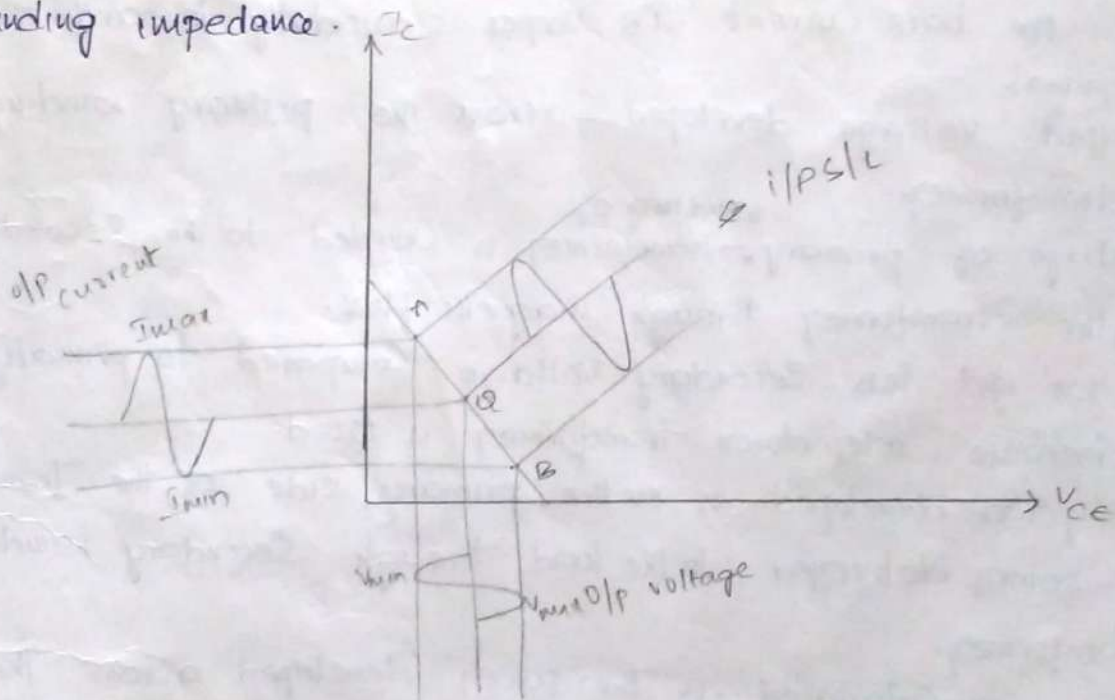
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→ The slope of the dc load line is reciprocal of the dc resistance.

→ In this circuit we have zero resistance in the winding. So, the



slope of the dc load line is ideally ' $\infty$ '.

→ This tells that dc load line in ideal condition is a vertical straight line.

→ Apply Kirchhoff's Voltage law to the output circuit.

$$V_{CC} - V_{CE} = 0$$

$$V_{CE} = V_{CC}$$

→ The dc input power is given by.

$$P_{dc} = V_{CC} \times I_C$$

### \* AC-Operation:-

→ When an AC input signal is applied across the base of the transistor, the base current  $I_B$  varies sinusoidally towards the base terminal.

→ The output voltage developed across the primary winding of the transformer.

→ The voltage of primary winding of transformer is coupled to the secondary winding of the transformer through magnetic flux.

→ Here we get less secondary voltage compared to primary voltage because, step down transformer is used.

→ The AC power developed is on the primary side of the transformer and this power delivered to the load through secondary winding of the transformer.

→ The AC power calculated is the power developed across the primary winding of the transformer.

→ Assuming the ideal transformer, the power delivered to the load on the secondary winding is same as that of power developed across the primary.

→ The AC power is given by

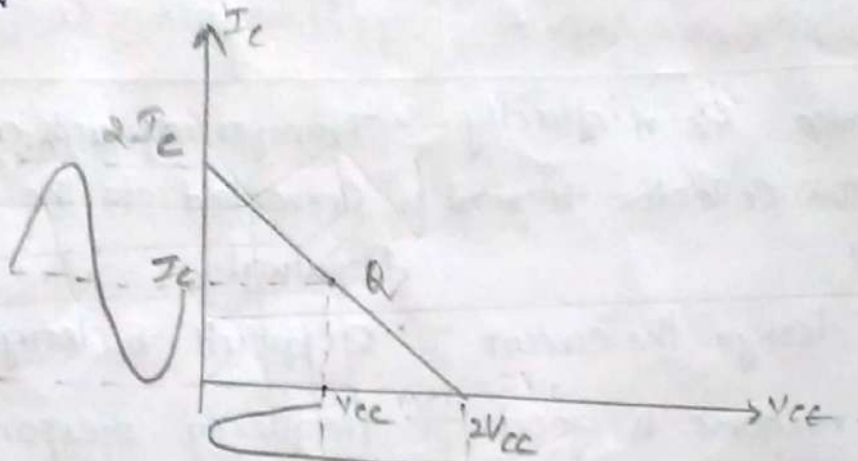
$$\therefore P_{AC} = \frac{(V_{max} - V_{min})(I_{max} - I_{min})}{8}$$



Efficiency:- The efficiency can be defined as the ratio of AC output power to the DC output power.

$$\eta = \frac{P_{AC}}{P_{DC}} \times 100$$

→ The maximum efficiency can be taken from maximum Vary output voltage and output Current is shown in the graph below.



$$\eta = \frac{(V_{max} - V_{min})(I_{max} - I_{min})}{8 V_{CC} \times I_C} \times 100$$

$$\begin{aligned} \eta &= \frac{(2V_{CC} - 0)(2I_C - 0)}{8 V_{CC} \times I_C} \times 100 \\ &= \frac{4 V_{CC} I_C}{8 V_{CC} I_C} \times 100 \\ &= 0.5 \times 100 \end{aligned}$$

$$\therefore \eta = 50\%$$

### \* Advantages:-

- Efficiency is high when Compare to direct Coupled class-A power Amplifier.
- Impedance matching is obtained. , → power dissipation is less

### \* Dis-advantages:-

- Difficult to design.
- More number of Components are required to design.



- Cost is high
- space required is also high.
- The frequency response is less.

\* Differences between direct coupled class-A power Amplifier and Transformer Coupled class-A power Amplifier:

Direct Coupled class-A p. A	Transformer Coupled class-A power Amplifier
<ul style="list-style-type: none"> <li>→ Load Resistance <math>R_L</math> is directly connected to the Collector terminal.</li> <li>→ Simple to design the circuit.</li> <li>→ frequency response is good.</li> <li>→ Less number of Components are required to design the circuit.</li> <li>→ Efficiency is less (25%)</li> <li>→ Output is less</li> <li>→ Impedance matching is poor.</li> <li>→ Cost is less.</li> <li>→ Occupation space is less</li> <li>→ power transferred is less to the load.</li> <li>→ Here, <math>V_{max} = V_{CC}</math> (Maximum Voltage is same as that of Supplied Voltage.)</li> <li>→ Here, <math>I_{max} = 2I_C</math> (Maximum Current is 2 times that of Collector Current.)</li> </ul>	<ul style="list-style-type: none"> <li>→ Transformer acts as a load which is connected to the Collector terminal.</li> <li>→ Difficult to design the circuit.</li> <li>→ frequency response is less.</li> <li>→ More number of Components are required to design the circuit.</li> <li>→ Efficiency is high when compared to Direct Coupled Class A power Amplifier (50%).</li> <li>→ Output is high.</li> <li>→ Impedance matching is high.</li> <li>→ Cost is high.</li> <li>→ Occupation space is high.</li> <li>→ Maximum power is transferred to the load.</li> <li>→ Here, <math>V_{max} = 2V_{CC}</math> (Maximum Voltage is two times of Supplied Voltage.)</li> <li>→ Here, <math>I_{max} = 2I_C</math> (Maximum Current is 2 times to that of Collector Current.)</li> </ul>



## class-B power Amplifier:-

class-B power Amplifier is defined as the output signal flows for the half cycle (or)  $180^\circ$  of the input signal.

→ In class-B power Amplifier, for the Negative half Cycle the transistor will be in off state. So that output current should be zero for Negative half Cycle. So we get distortion during the Negative half Cycle.

→ To avoid this distortion we go for push-pull class-B Amplifier.

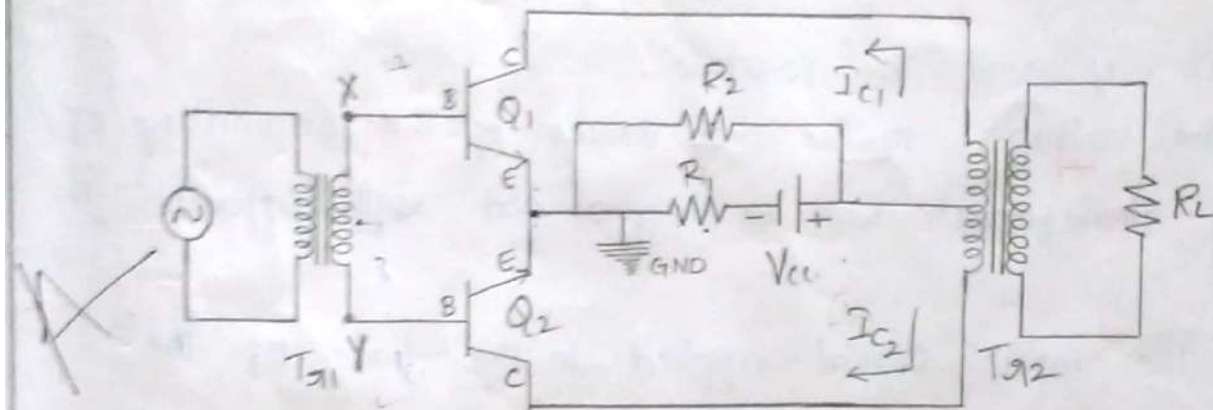


Fig:- push-pull class-B power Amplifier.

- The above figure shows the class-B push pull Amplifier.
- In this circuit both the ~~transistors~~ transistors  $Q_1$  &  $Q_2$  are of N-p-n type.
- These both transistors are in CE Configuration.
- It consists of two transformers  $T_{11}$  and  $T_{12}$  one is input transformer  $T_{11}$  and other is output transformer  $T_{12}$ .
- Input transformer is also called as driving transformer which drives the circuit.
- $R_1$  and  $R_2$  resistors are the biasing resistors which provides the biasing to the transistors.
- The input signal is applied to the primary of the driver transformer.
- The Centre tap on the Secondary of the transformer is grounded.



- The Centre tap on the primary of the output transformer is connected to the supply voltage ( $V_{cc}$ ).
  - With respect to the centre tap for a positive half cycle of input signal, the point 'x' shown on the secondary of the driver transformer will be positive while the point 'y' will be negative.
  - Similarly, with respect to the centre tap for a negative half cycle of input signal, the point 'x' shown on the secondary of the driver transformer will be negative while the point 'y' will be positive.
  - Thus the voltages in the two halves of the secondary of the driver transformer will be equal but with opposite polarities.
  - Hence the input signal applied to the base of the transistors  $Q_1$  &  $Q_2$  will be  $180^\circ$  out of phase.
  - Each transistor output is in the form of half rectified waveform.
  - Hence the peak value of the output current of each transistor is ' $I_m$ '. So, the average value (dc value) of output current of each transistor is ' $\frac{I_m}{\pi}$ ' (due to half rectified wave form.)
  - The two currents drawn by the two transistors from the dc supply voltage. Hence, the total dc current of both transistors is given by  $I_{dc} = \frac{I_m}{\pi} + \frac{I_m}{\pi}$ .
- $$I_{dc} = \frac{2 I_m}{\pi}$$
- The dc input power is given by  $P_{dc} = V_{cc} \times I_c$  where  $I_c = I_{dc}$
- $$P_{dc} = V_{cc} \times \frac{2 I_m}{\pi}$$



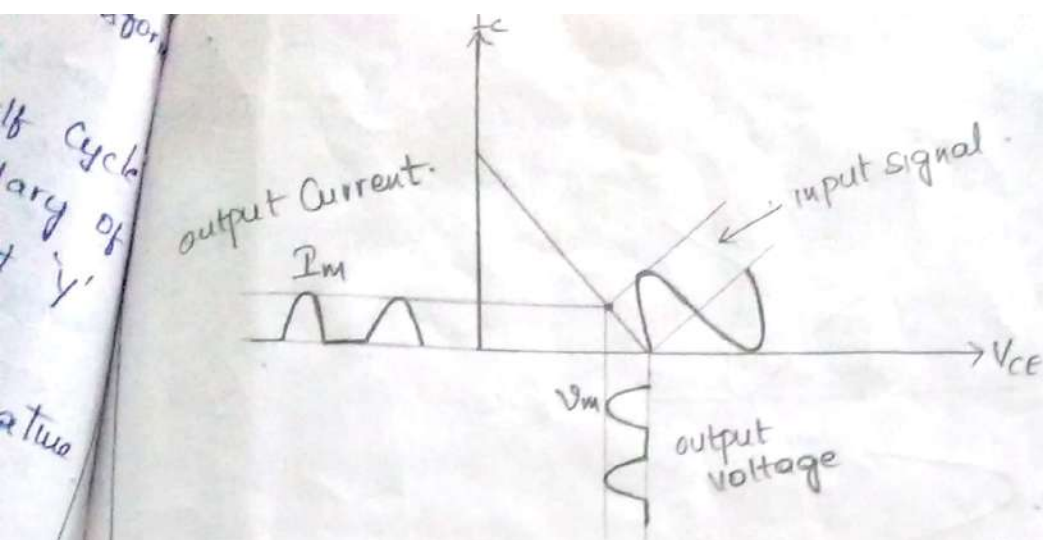


Fig: Graphical Representation for push-pull Class-B power Amplifier.

- where  $I_m$  and  $V_m$  are the peak values of output current and output voltage respectively.
- The RMS value of voltages and currents are given by

$$V_{RMS} = \frac{V_m}{\sqrt{2}}, \quad I_{RMS} = \frac{I_m}{\sqrt{2}}.$$

- The AC power is given by.

$$P_{AC} = \frac{V_m}{\sqrt{2}} \times \frac{I_m}{\sqrt{2}}$$

$$P_{AC} = \frac{V_m I_m}{2}$$

Efficiency:- Efficiency is defined as the ratio of AC output power to the DC input power, & it is denoted by ' $\eta$ '.

$$\eta = \frac{P_{AC}}{P_{DC}} \times 100$$

- The maximum value of ' $\eta$ ' can be taken from maximum value of output voltage and output current, and is shown in the figure below.



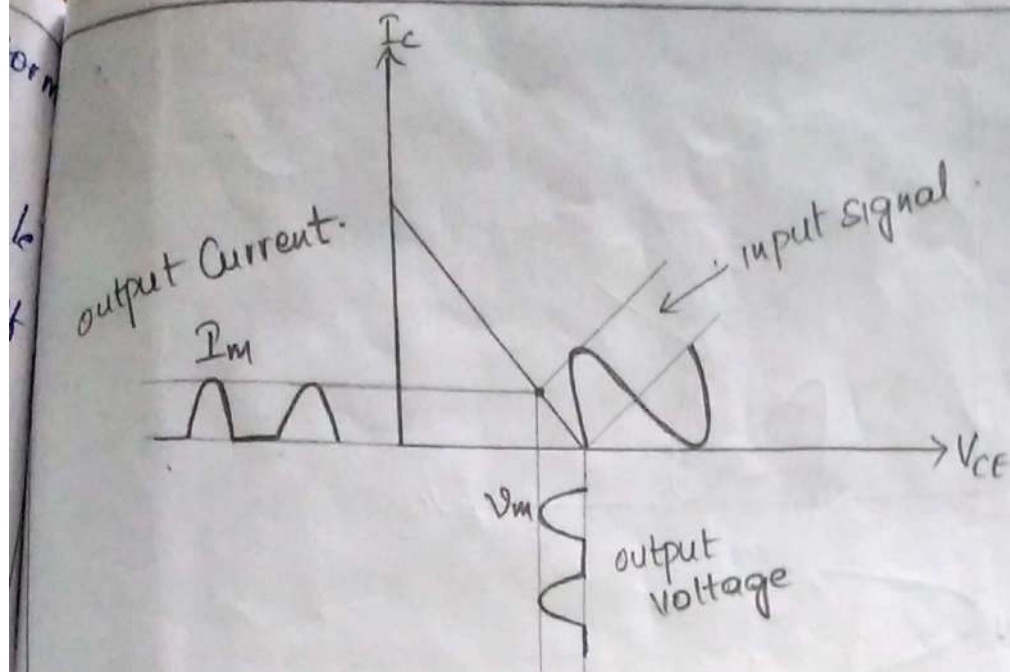


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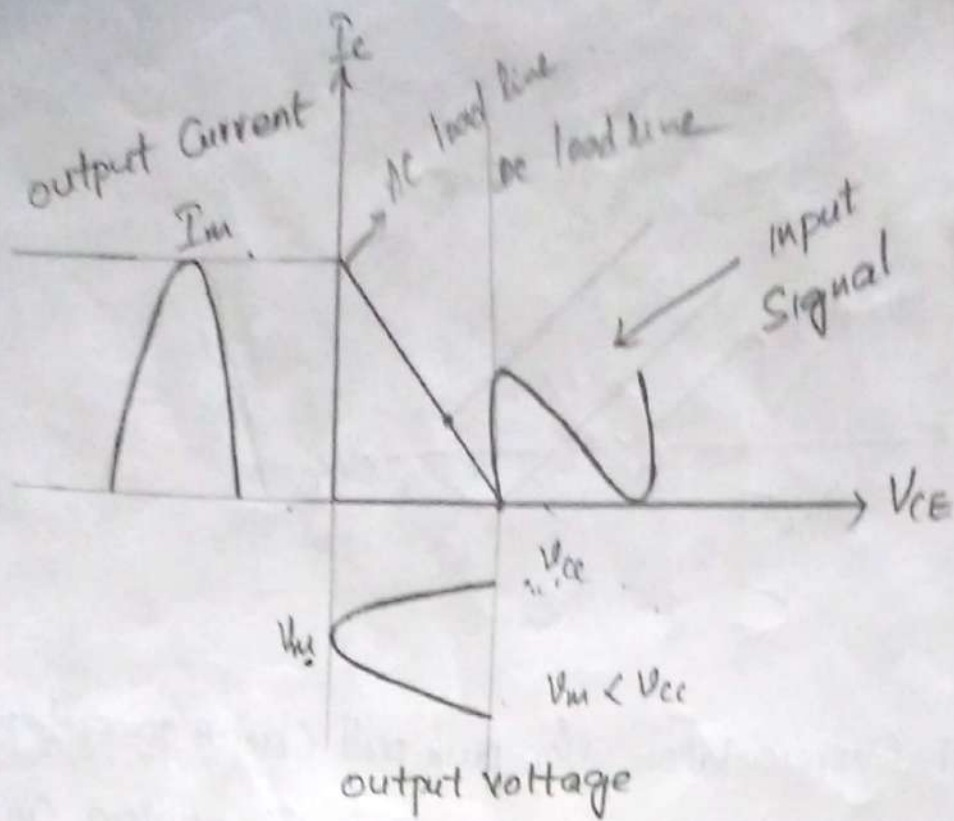
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Efficiency:- Efficiency is defined as the ratio of AC output power to the DC input power, & it is denoted by ' $\eta$ '.

$$\eta = \frac{P_{AC}}{P_{DC}} \times 100$$

→ The maximum value of ' $\eta$ ' can be taken from maximum Vary of output voltage and output current. and is shown in the figure below.



$$\eta = \frac{\frac{V_m I_m}{2}}{\frac{V_{ce} \times 2 I_m}{\pi}} \times 100$$

$$\eta = \frac{\frac{V_m \pi}{2}}{V_{ce} \times 2} \times 100$$

$$\eta = \frac{V_m \pi}{4 V_{ce}} \times 100$$

from graph  $V_m = V_{ce}$

$$\text{Now } \eta = \frac{V_{ce} \times \pi}{4 V_{ce}} \times 100$$

$$\eta = \frac{\pi}{4} \times 100$$

$$= 0.785 \times 100$$

$$\therefore \eta = 78.5 \%$$

→ Thus, we conclude that the efficiency is maximum in class-B push pull power Amplifier.

→ So, the distortion may be reduced by using the push-pull operation of transistors.



### Advantages :-

- Efficiency is high
- power dissipation is low.
- Impedance matching is good.
- distortion is less.

In push-pull power Amplifier one transistor is being pushed deep into conduction while the other is being pulled out (Non-conducting), hence the name push-pull amplifier.

### \* Dis-advantages :-

- circuit design is Complex.
- Cost is high.
- More number of Components is required to design the circuit.
- frequency response is low.
- Occupation space is more.

### \* Complimentary Symmetry class-B power Amplifier :-

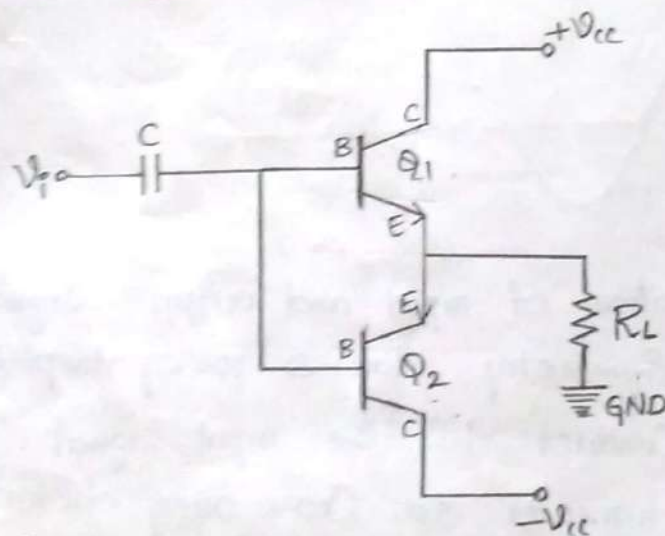


Fig. 8 Circuit diagram for Complementary Symmetry Class-B power Amplifier.

- In class-B push-pull amplifier requires two centre trapped transformers, due to that Cost is high, circuit design is difficult, and occupation space is more. This draw backs can be Overcome by using Complementary Symmetry class-B power Amplifier.
- The above figure shows the Complementary Symmetry class-B power Amplifier.



- It has one N-p-N and one p-N-p transistors  $Q_1$  and  $Q_2$ .
- The input voltage is applied to the base of  $Q_1$  and  $Q_2$  transistors.

### \* Operation :-

- During positive half cycle of the input signal, transistor  $Q_1$  will conduct and transistor  $Q_2$  does not conduct. So, the positive cycle appears across the load resistance ' $R_L$ '.
- Similarly, during Negative half cycle of the input signal, transistor  $Q_1$  does not conduct and transistor  $Q_2$  conducts. So, the Negative half cycle appears across ' $R_L$ '.

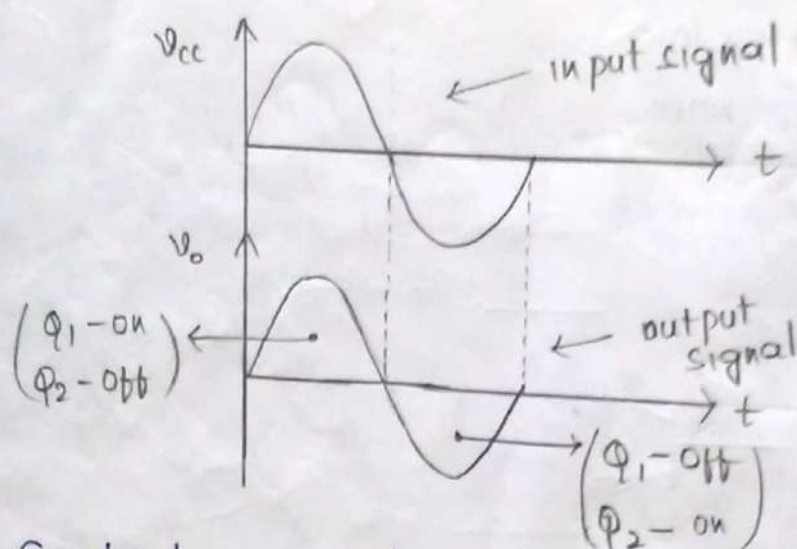


Fig:- Graphical representation of input and output signals of Complementary Symmetry Class-B power Amplifier.

- Transistor will not conduct till the input signal exceeds cut in voltage of the transistor. So, Crossover distortion will be present in the output signal of Complementary Symmetry power Amplifier.

### \* Advantages:-

- The circuit is transformer less, due to this cost is low and size is small and weight is less.
- frequency response is improved.
- Due to common collector configuration impedance matching is possible.

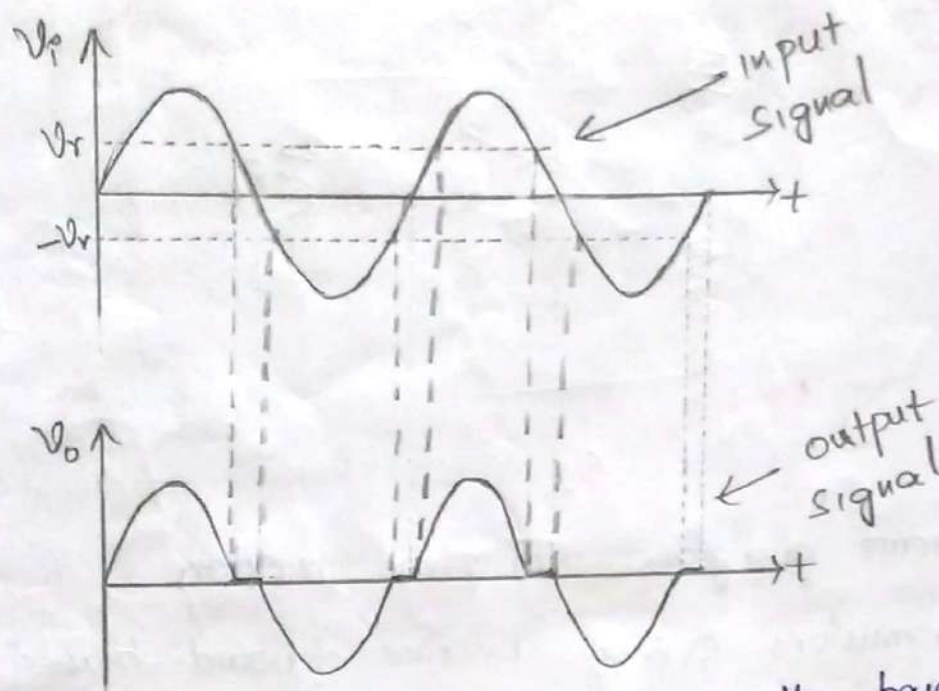


## Dis-advantages:-

Two power Supplies is necessary.

Cross over distortion will be present in the output signal.

## Cross Over distortion:-

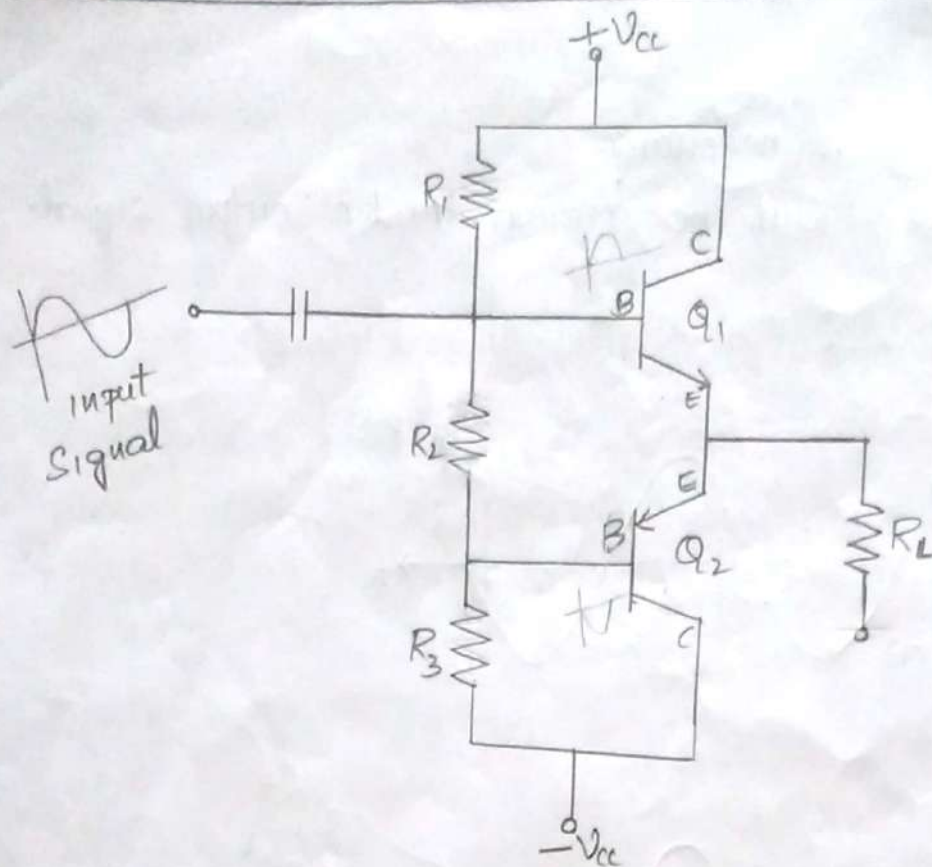


- The transistor to be in active region, the base-Emitter junction is forward biased and Collector-Base junction is reverse biased.
- The transistor will ~~can~~ not conduct till the input voltage exceeds the cut in voltage of the transistor. So, we get distortion in the output signal. It is called as cross over distortion.
- The corresponding waveforms of cross over distortion is shown in the above graph.
- To eliminate cross over distortion DC bias voltage should be provided for base-Emitter junction of each transistor.
- By using phase inverter circuits. we can overcome the cross over distortion in the Class-B power Amplifier.

## Phase Invertors:-

- By using phase inverter circuit, we can reduce the cross over distortion by providing dc voltage across the base of two transistors.

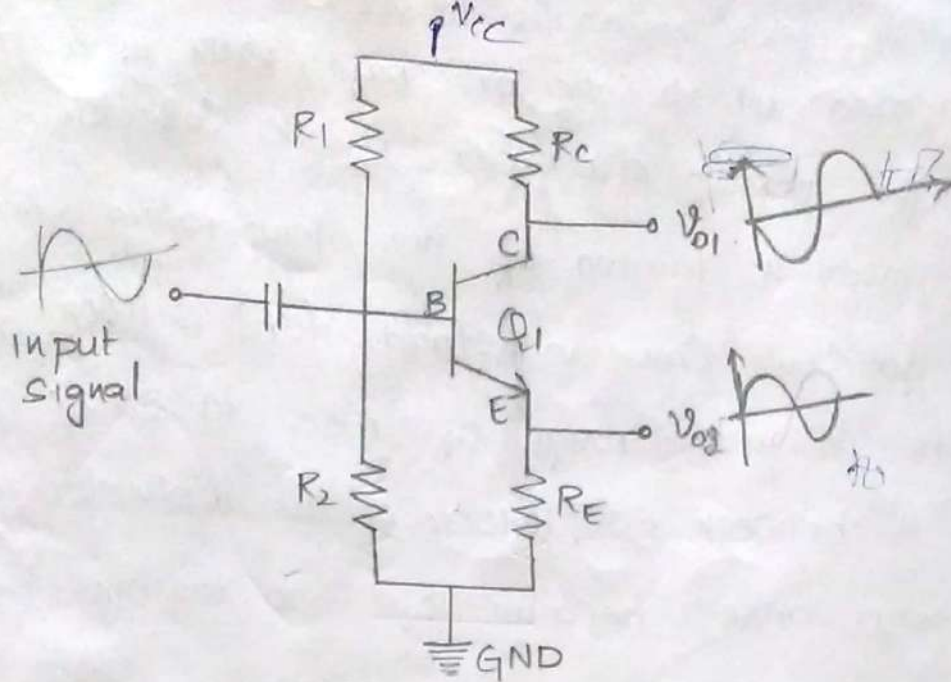




fig(a): Circuit diagram for phase inverter.

- Both the transistors  $Q_1$  &  $Q_2$  become forward biased due to the voltage drop across ' $R_3$ '.
- The phase inverter circuit is shown in the above figure (a).
- It consists of one N-P-N and one P-N-P transistors named  $Q_1$  and  $Q_2$  respectively.
- Collector-base junction of  $Q_1$  transistor is in reverse bias due to positive voltage is connected by supply.
- Collector-base junction of  $Q_2$  transistor is in reverse bias due to negative voltage is connected to the ground.
- Therefore, both collector-base junctions of transistors  $Q_1$  &  $Q_2$  are in reverse bias.
- During positive peak voltage ' $Q_1$ ' transistor is turned on and ' $Q_2$ ' transistor is turned off. So, the positive voltage appears across the load resistance ' $R_L$ '.
- During negative peak voltage ' $Q_1$ ' transistor is turned off and ' $Q_2$ ' transistor is turned on. So, the negative voltage appears across the load resistance ' $R_L$ '.





fig(b):- Circuit diagram for modified phase inverter.

- The modified phase inverter circuit is shown in the figure (b).
- This circuit is to obtain the two out of phase signals.
- Here ' $R_1$ ' and ' $R_2$ ' are biasing resistors, ' $R_C$ ' is a load resistance of Collector terminal and ' $R_E$ ' is a load resistance of Emitter terminal.
- When the input is applied to the base terminal, the output is taken at the Collector and Emitter terminals ( $V_{01}$  &  $V_{02}$ ) will be out of phase by  $180^\circ$ .
- The output impedance of the circuit from Collector terminal is that of CE Configuration.
- The output impedance of the circuit from Emitter terminal is that of CC Configuration.
- When the bypass capacitor across ' $R_E$ ' is not present, the voltage  $V_{02}$  across ' $R_E$ ' is in phase to the input signal.
- The output voltage  $V_{01}$  at the Collector terminal will be  $180^\circ$  out of phase to the input signal.
- Therefore  $V_{01}$  and  $V_{02}$  voltages are out of phase by  $180^\circ$ .

### Thermal Runaway:-

- The maximum average power  $P_D(\text{max})$ , which a transistor can dissipate depends upon the transistor construction and may lie



in the range from a few milliwatts to 800W as mentioned earlier. The power dissipated at its collector base with in a transistor predominantly the power dissipated at its collector-base junction. Thus maximum power is limited by the temperature that the collector-base junction can withstand for silicon transistor, this temperature is in the range of  $150^{\circ}\text{C}$  to  $225^{\circ}\text{C}$  and for germanium it is between  $60^{\circ}$  to  $100^{\circ}\text{C}$ . The collector-base junction temperature may arise because of two reasons.

1. Due to rise in the ambient temperature.
2. Due to self heating.

\* The self heating can be explained as follows:-

→ The increase in the collector current increases the power dissipated at the collector junction. This in turn further increases the temperature of the junction and hence increases collector current. The process of the cumulative and it is referred to as self heating. This excess heat produced at the collector base junction may even burn and destroy the transistor. This situation is called Thermal Run away of the transistor.

\* Heat Sinks:-

→ Heat sink is basically a large metallic heat conducting device which when placed near a transistor cools it by increasing its effective surface area.

\* Requirements of heat sinks:- For transistor operating at a high level, the heat sinks must be designed to remove the heat by metallic conduction (or) forced air cooling.

→ The purpose of heat sinks is to keep the operating point temperature of the transistors prevent thermal breakdown.



increase in temperature. It measures  $\Delta$  due to increase in  $\Delta$  with result in the increase in power dissipation and by temperature increases. This is a cumulative process. Due to the transistor break down. In order to prevent this, heat sinks are used which maintain low temperature and to dissipate power.

If heat sinks are used, the heat is transferred from due to the surface of package and from package to heat sink and from heat sink to the ambient. Heat sink facilitates the power dissipation and prevents breakdown of the device.

Types of heat sinks:-

Heat sinks are broadly classified as

\* Low power transistor type \* High power transistor type

Low power transistor type:-

→ Low power transistor can be mounted directly on the metal case to increase the heat dissipation capacity.

→ The casing the transistor must be insulated from metal case to prevent short circuit.

→ Eucrylone oxide, insulating washers are used for insulating casing from the case which gives good thermal conductivity.

→ Fine oxide film, silicon compound between washers improves the heat transfer from semiconductor device to case of circuit.

→ A low power transistor heat sink is shown in the figure below.

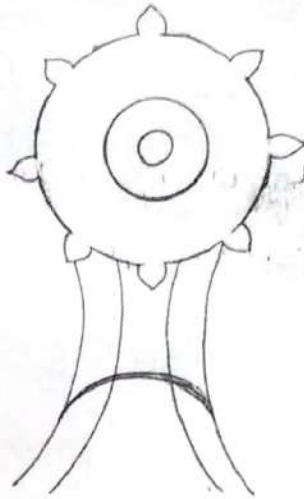


fig: Fin-type Heat Sink.

\* High power transistor type :-

- To-3, To-66 are the two types of high power transistors. These transistors are of diamond shape and dissipates power in order to be
- The transistor heat sinks shown in figure below perform cooling by conducting convection and radiating methods.
- The thermal resistance of heat sinks will typically  $3^{\circ}\text{C}/\text{W}$ .

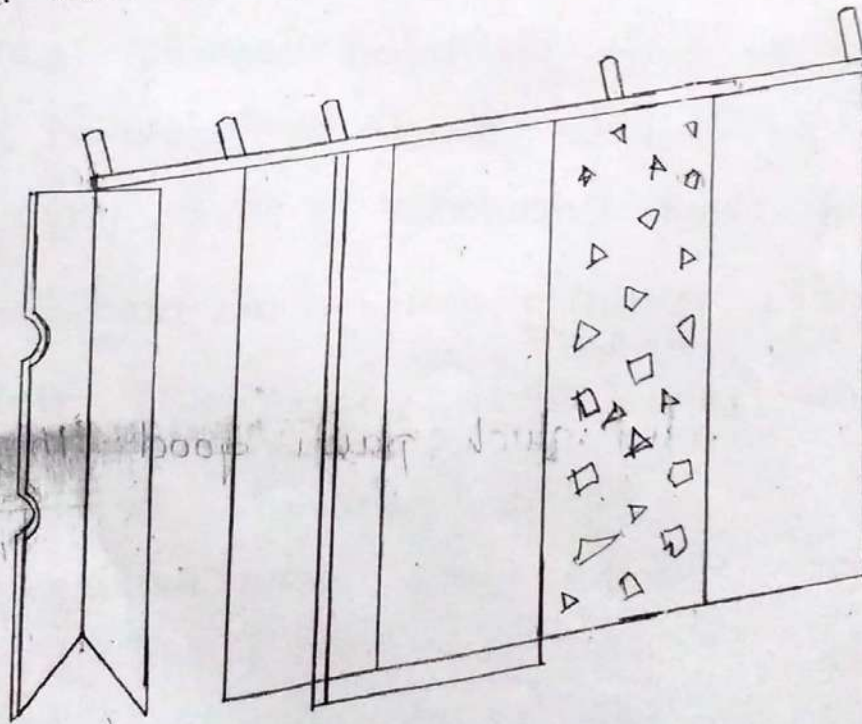


fig:- power transistor heat sink.



## DISTORTION:

UNIT-4

\* In power amplifiers, the Input signal applied is alternating in nature. The basic features of any alternating signals are Amplitude, Frequency and phase.

### FREQUENCY DISTORTION:

\* The change in gain of the amplifier with respect to the frequency is called Frequency distortion.

### AMPLITUDE DISTORTION:

\* A transistor is a perfectly linear device i.e., the dynamic characteristics of a transistor is a straight line over the operating ranges.

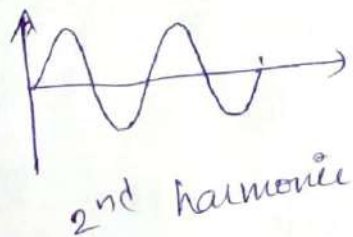
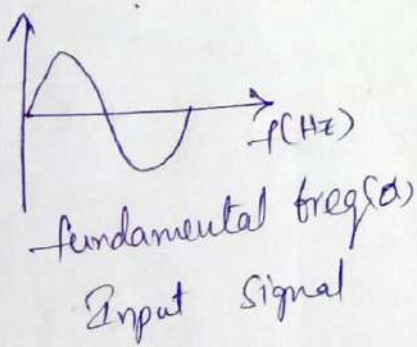
\* But in practical cases the dynamic characteristics is not perfectly linear due to such non-linearity the wave form of O/P Voltage differs from that of the i/p signal.

\* Such a distortion is called Non-linear distortion or Harmonic distortion (&) Amplitude distortion

## HARMONIC DISTORTION:

- \* The presence of the frequency components in the output waveform which are not present in the input waveform
- \* The frequency component in the o/p, whose frequency is same as the i/p signal frequency then it is called as Fundamental frequency.
- \* The additional frequency components present in the o/p signal are the integral multiples of fundamental frequency these components are called as "Harmonics".

Eg:  $f$  Hz is the fundamental frequency  
 $2f$  Hz is the second harmonic etc.



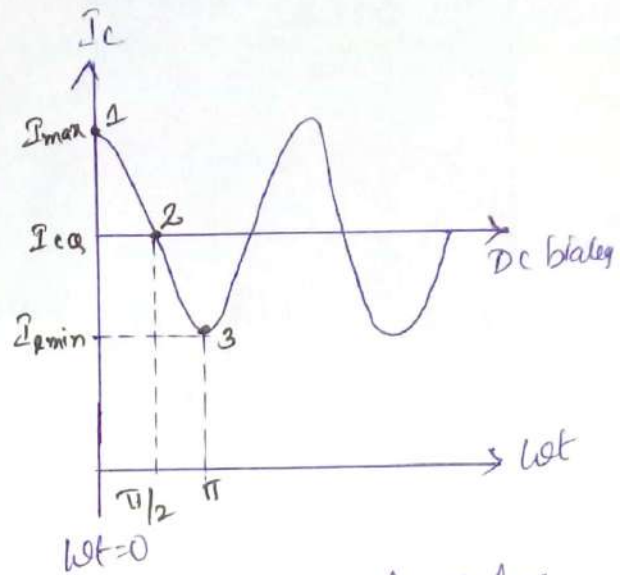
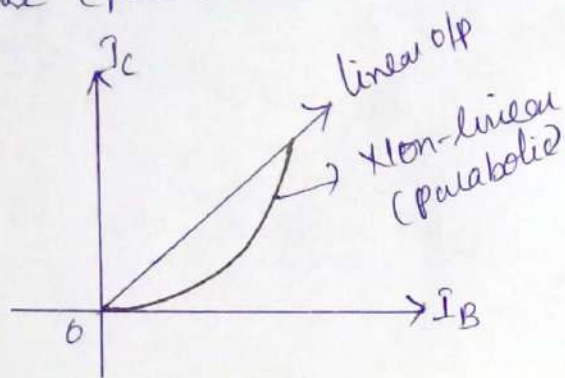
↓ If the number of harmonics increases then the amplitude decreases.

## SECOND-ORDER HARMONIC DISTORTION (3-POINT METHOD)

↓ Let us consider AC Input signal causes the base current swing which is cosine in nature.



\* To analyze Second harmonic distortion, assume that the dynamic characteristics are non-linear non-linear in nature (parabolic).



\* Due to this collector current swing, the operating point value and relation between  $I_c$  and  $I_b$  is non-linear.

$$i_b = I_{Bm} \cos \omega t$$

Mathematically it can be expressed as,

$$i_c = G_1 i_b + G_2 i_b^2$$

$$i_c = G_1 (I_{Bm} \cos \omega t) + G_2 (I_{Bm} \cos \omega t)^2$$

$$= G_1 I_{Bm} \cos \omega t + G_2 I_{Bm}^2 \cos^2 \omega t$$

$$(\because \cos^2 \omega t = 1 + \frac{\cos 2\omega t}{2})$$

$$= G_1 I_{Bm} \cos \omega t + G_2 I_{Bm}^2 \left[ 1 + \frac{\cos 2\omega t}{2} \right]$$

$$= G_1 I_{Bm} \cos \omega t + \frac{1}{2} G_2 I_{Bm}^2 + \frac{1}{2} G_2 I_{Bm}^2 \cos 2\omega t$$

$$i_c = B_0 + B_1 \cos \omega t + B_2 \cos 2\omega t$$

\* The total current can be expressed in terms of second harmonic component, DC signal component.

$$\text{i.e., } I_c = I_{cq} + B_0 + B_1 \cos \omega t + B_2 \cos 2\omega t \rightarrow (1)$$

where  $(I_{cq} + B_0)$  = DC component independent of time

$B_1$  = Amplitude of fundamental freq

$B_2$  = Amplitude of second harmonic.

\* Let us find the value of total collector current at the various instances 1, 2 & 3.

At point 1,  $\omega t = 0$ , then eq (1) becomes

$$I_c = I_{cq} + B_0 + B_1 + B_2 \rightarrow (2)$$

At point 2,  $\omega t = \frac{\pi}{2}$  then eq (1) becomes

$$I_c = I_{cq} + B_0 - B_2 \rightarrow (3)$$

At point 3,  $\omega t = \pi$  then eq (1) becomes

$$I_c = I_{cq} + B_0 + B_1 + B_2 \rightarrow (4)$$

From eq (3) &  $I_c = I_{cq}$

$$B_0 = B_2$$

From eq (2) & (4)

$$I_{\max} - I_{\min} = I_{cq} + B_0 + B_1 + B_2 - (I_{cq} - B_0 + B_1 - B_2)$$

$$I_{\max} - I_{\min} = 2B_1$$

$$B_1 = \frac{I_{\max} - I_{\min}}{2}$$



from eq (3) & eq (4)

$$I_{\max} + I_{\min} = I_{CQ} + B_0 + B_1 + B_2 + I_{CQ} + B_0 - B_1 + B_2$$

$$I_{\max} + I_{\min} = 2I_{CQ} + 2B_0 + 2B_2$$

$$I_{\max} + I_{\min} = 2I_{CQ} + 2(B_0 + B_2) \quad (\because B_0 = B_2)$$

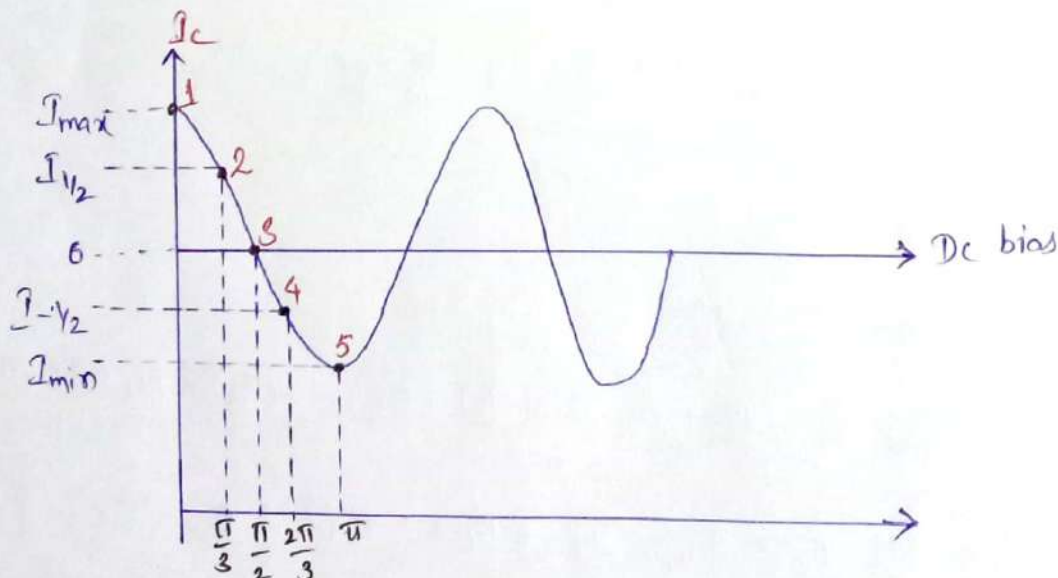
$$I_{\max} + I_{\min} = 2I_{CQ} + 4B_2$$

$$4B_2 = I_{\max} + I_{\min} - 2I_{CQ}$$

$$B_2 = \frac{I_{\max} + I_{\min} - 2I_{CQ}}{4}$$

### HIGHER ORDER HARMONIC DISTORTION (5 POINT METHOD):

\* As the non-linearity increased in the dynamic characteristics of a transistor then the harmonic distortion also increases.



\*  $I_b = I_{Bm} \cos \omega t$

\* The mathematical expression for the collector current due to higher order harmonics,

$$I_c = G_1 I_{Bm} \cos \omega t + G_2 I_{Bm}^2 \cos^2 \omega t + G_3 I_{Bm}^3 \cos^3 \omega t + G_4 I_{Bm}^4 \cos^4 \omega t + \dots$$

$$I_c = G_1 I_b + G_2 I_b^2 + G_3 I_b^3 + G_4 I_b^4 + \dots \quad (\because I_b = I_{Bm} \cos \omega t)$$

$$I_c = B_0 + B_1 \cos \omega t + B_2 \cos 2\omega t + B_3 \cos 3\omega t + B_4 \cos 4\omega t + \dots$$

$$I_c = I_{c0} + B_0 + B_1 \cos \omega t + B_2 \cos 2\omega t + B_3 \cos 3\omega t + B_4 \cos 4\omega t + \dots \rightarrow \text{eq ①}$$

Where  $(I_{c0} + B_0)$  is D.C component independent of time

$B_1$  = Amplitude of fundamental frequency

$B_2$  = Amplitude of second harmonic

$B_3$  = Amplitude of third harmonic

At point 1  $\omega t = 0$  :

Then equation ① becomes

$$I_c = I_{c0} + B_0 + B_1 \cos(0) + B_2 \cos 2(0) + B_3 \cos 3(0) + B_4 \cos 4(0)$$

$$I_c = I_{c0} + B_0 + B_1 + B_2 + B_3 + B_4 \rightarrow \text{eq ②}$$

At point 2  $\omega t = \pi/3$

Then equation ① becomes

$$I_c = I_{c0} + B_0 + B_1 \cos \pi/3 + B_2 \cos 2(\pi/3) + B_3 \cos 3(\pi/3) + B_4 \cos 4(\pi/3)$$

$$I_{1/2} = I_c = I_{c0} + B_0 \quad I_c = I_{1/2} = I_{c0} + B_0 + \frac{1}{2} B_1 - \frac{1}{2} B_2 - B_3 - \frac{1}{2} B_4 \rightarrow \text{③}$$

At point 3  $\omega t = \pi/2$

Then equation ① becomes

$$I_c = I_{c0} + B_0 + B_1 \cos \pi/2 + B_2 \cos 2(\pi/2) + B_3 \cos 3(\pi/2) + B_4 \cos 4(\pi/2)$$



$$I_c = I_{c0} + B_0 - B_2 + B_4 \rightarrow \text{eq (4)}$$

At point 4  $\omega t = 2\pi/3$

Then eq (1) becomes

$$I_c = I_{c0} + B_0 + B_1 \cos 2\pi/3 + B_2 \cos 2(2\pi/3) + B_3 \cos 3(2\pi/3) + B_4 \cos 4(2\pi/3)$$

$$I_c = I_{c0} + B_0 - \frac{1}{2} B_1 - \frac{1}{2} B_2 + B_3 - \frac{1}{2} B_4 \rightarrow \text{(5)}$$

At point 5  $\omega t = \pi$

Then eq (1) becomes

$$I_c = I_{c0} + B_0 + B_1 \cos \pi + B_2 \cos 2\pi + B_3 \cos 3\pi + B_4 \cos 4\pi$$

$$I_c = I_{c0} + B_0 - B_1 + B_2 - B_3 + B_4 \rightarrow \text{(6)}$$

By solving above equations

$$B_0 = \frac{1}{6} [I_{\max} + 2I_{1/2} + 2I_{-1/2} + I_{\min}]$$

$$B_1 = \frac{1}{3} [I_{\max} + 2I_{1/2} - 2I_{-1/2} - I_{\min}]$$

$$B_2 = \frac{1}{4} [I_{\max} - 2I_{c0} + I_{\min}]$$

$$B_3 = \frac{1}{6} [I_{\max} - 2I_{1/2} + 2I_{-1/2} - I_{\min}]$$

$$B_4 = \frac{1}{12} [I_{\max} - 4I_{1/2} + 6I_{c0} - 4I_{-1/2} + I_{\min}]$$

- \* The power amplifier in which the output current flows for more than half cycle and less than full cycle of i/p signal is known as Class-AB power amplifier.
- \* The conduction angle of class AB power amplifier is between class-A and class-B power amplifier i.e., ( $< 360^\circ$  &  $> 180^\circ$ ).
- \* As, class A has the problem of low efficiency and class-B has distortion problem, this class AB is emerged to eliminate these two problems, by utilizing the advantages of both the classes.
- \* The cross-over distortion is the problem that occurs when both the transistors are OFF at the same instant, during the transition period.
- \* In order to eliminate this, the condition has to be chosen for more than one half cycle. Hence the other transistor gets into conduction, before the operating transistor switches to cut-off state.
- \* This is achieved by class-AB power amplifier

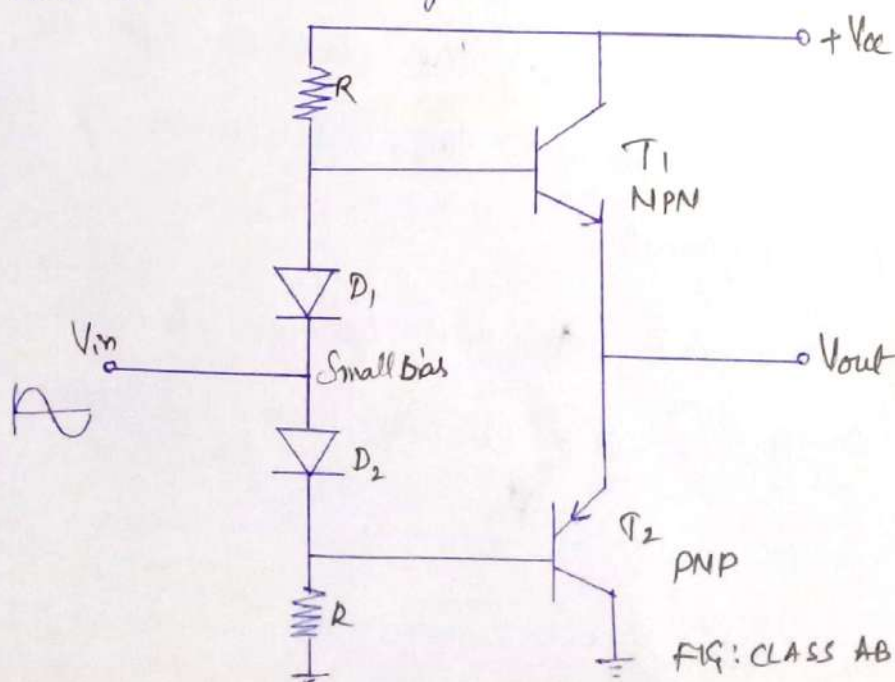


FIG: CLASS AB POWER AMPLIFIER



\* In class AB amplifier design, each of the push-pull transistors is conducting for slightly more than the half cycle of conduction in class B, but much less than the full cycle of conduction of class A.

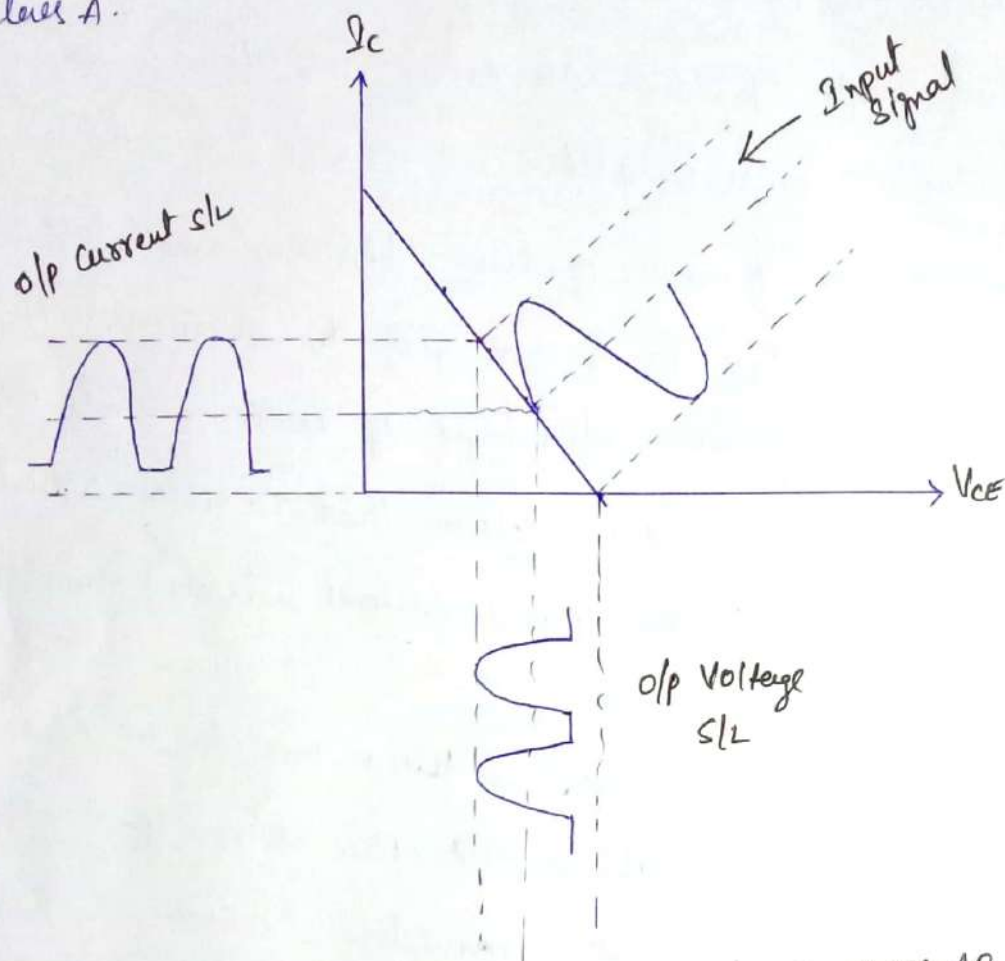


FIG: GRAPHICAL REPRESENTATION OF CLASS-AB POWER AMPLIFIER

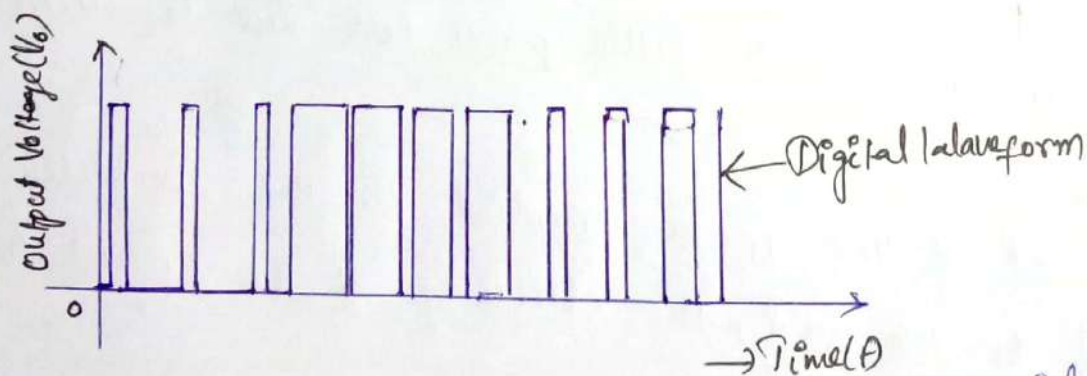
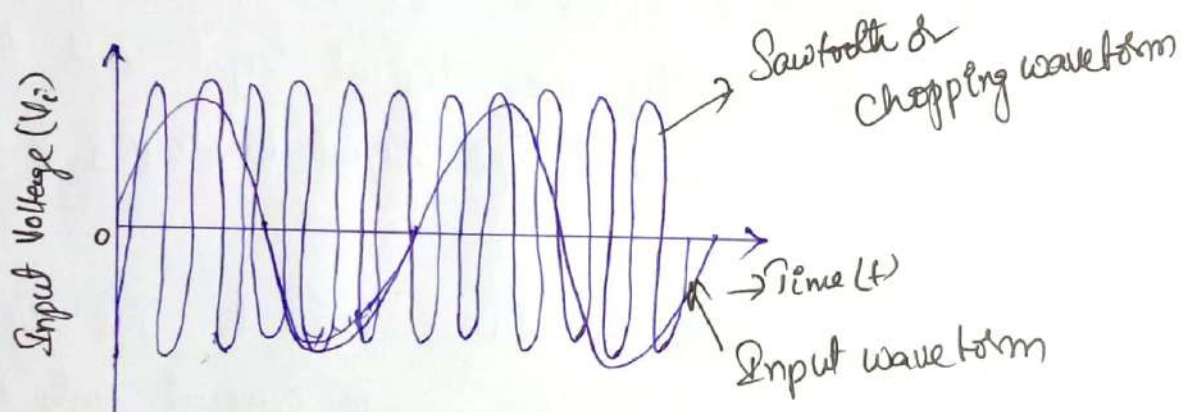
\* The small bias voltage given using diodes  $D_1$  &  $D_2$  helps the operating point to be above the cut-off point.

\* The cross-over distortion created by the class-B amplifier is overcome by this class-AB as well the inefficiencies of class-A and B don't affect the circuit.

\* So, the class AB is a good compromise between class-A and class-B in terms of efficiency and linearity having the efficiency reaching about 50% to 60%.

## CLASS-D POWER AMPLIFIER:

- \* A class-D power amplifier is designed to operate with Digital or pulse type signals.
- \* The letter 'D' stands for "Digital" since that is the nature of signals provided to the class-D amplifier.
- \* It is necessary to convert any input signal into a pulse type waveform before using it to drive a large power load and to convert the signal back to the sinusoidal signal.



- \* The above figures illustrate the how a sinusoidal signal may be converted into a pulse type signal.
- \* Using some form of Sawtooth or chopping waveform to be applied.
- \* The Block diagram of class-D power amplifier is as shown.



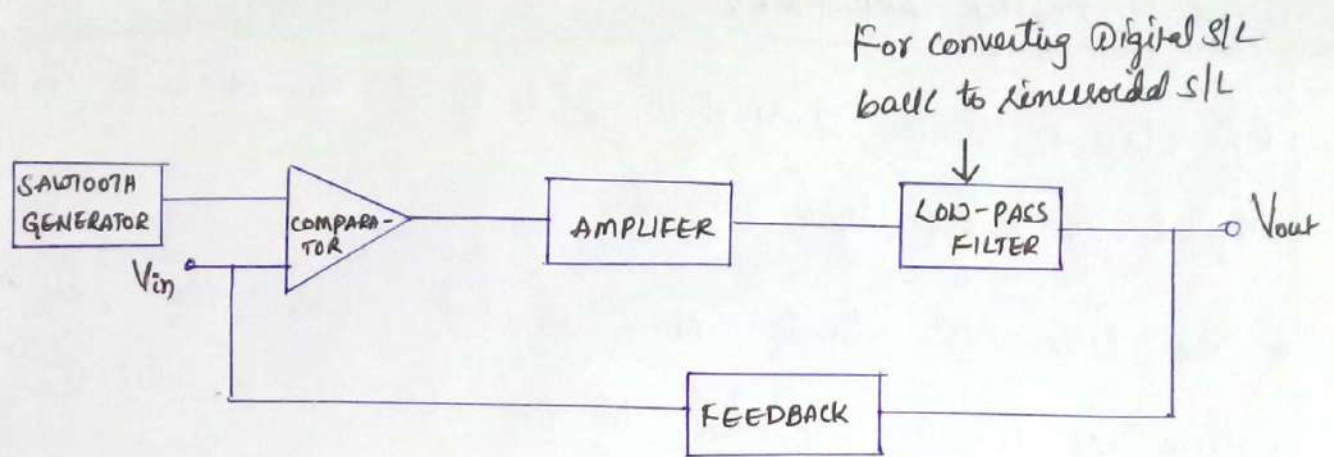


FIG: BLOCK DIAGRAM OF CLASS-D POWER AMPLIFIER

- \* The Sawtooth & Chopping waveform to be applied with the input into a comparator type op-amp circuit so that a representative pulse-type signal is generated.
- \* The amplifier will amplify the digital signal and then convert back to the sinusoidal type signal employing a low-pass filter.
- \* Since the amplifier's transistor devices used to give the output are basically either off or on, they provide current only when they are turned on, with little power loss due to their low on-voltage.
- \* Thus most of the power supplied to the amplifier is transferred to the load, the efficiency of the circuit is typically very high.
- \* Class-D amplifiers can attain efficiencies of 90% and with careful component choices can exceed 95% even.
- \* Power MOSFET's devices have been quite popular as the driver devices for the class D power amplifier.

## CLASS-S AMPLIFIER:

\* The class-S amplifier has an input a pulse-width modulated (PWM) signal to turn  $Q_1$  and  $Q_2$  ON or OFF or switches with a switching frequency much higher than the signal frequency.

\* The circuit diagram for class-S amplifier is given by,

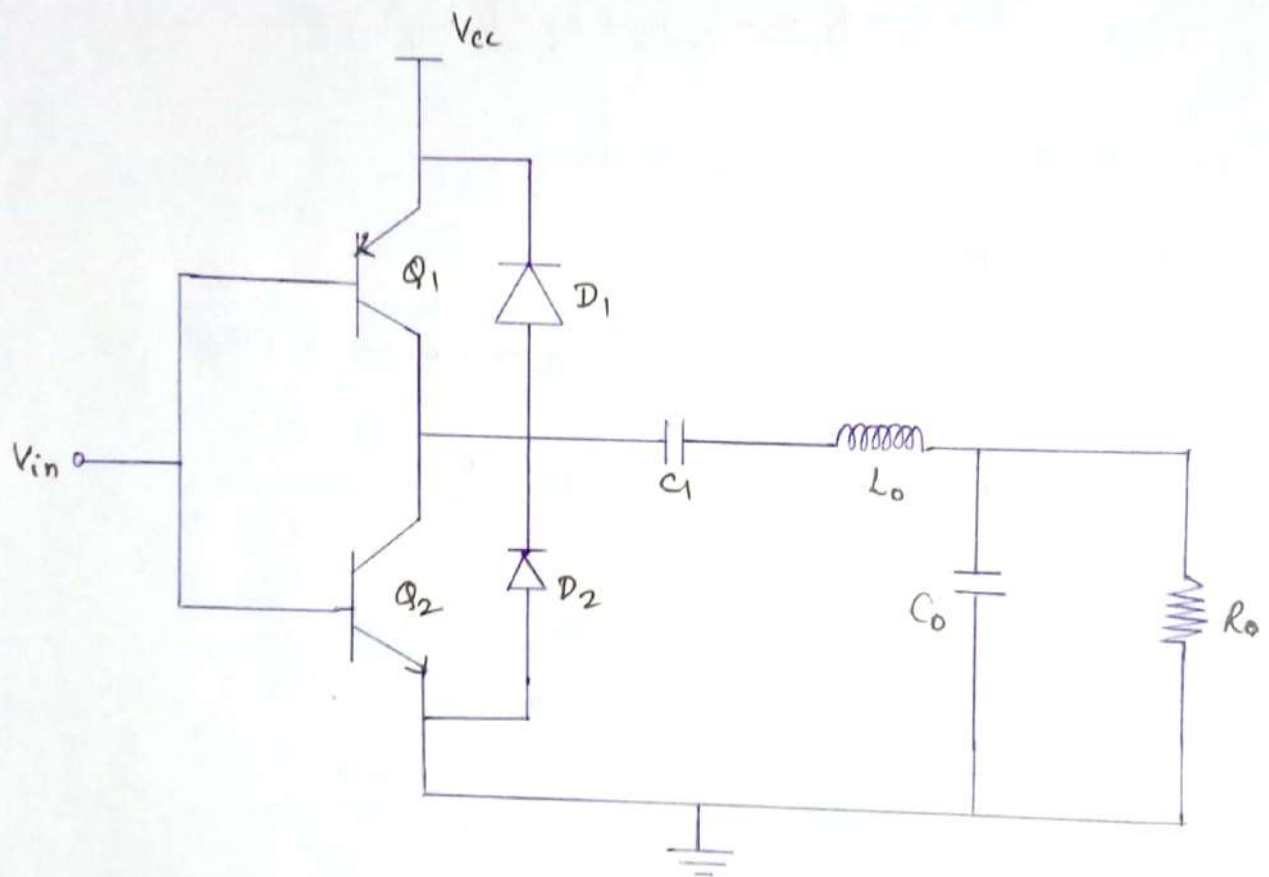
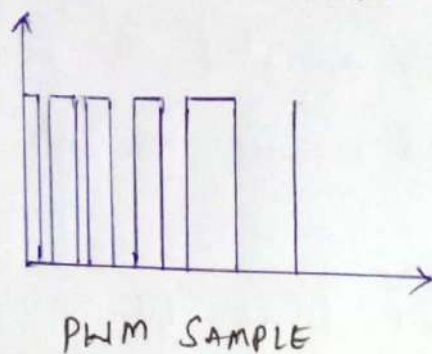
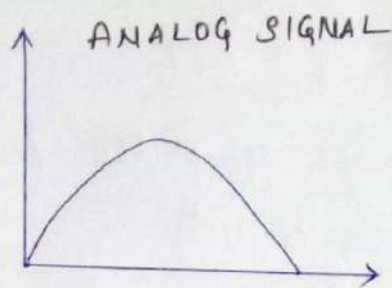


FIG: CIRCUIT DIAGRAM FOR CLASS-S AMPLIFIER

\* It is used for both amplification and Amplitude Modulation.  
\* It is similar to class D except the rectangular PWM Voltage waveform is applied to a low-pass filter that allows only the slowly varying DC or average Voltage component to appear across the load.





- \* The  $L_o$  and  $C_o$  in the circuit forms a low pass filter that turns the PWM signal into an analog waveform.
- \* If only positive outputs are needed, only  $Q_1$  and  $D_2$  are required. For negative signals, only  $D_1$  and  $Q_2$  are necessary.
- \* The switching frequency must be significantly higher than the signal frequency, this technique is not viable for amplification in the GHz frequency range.

### MOSFET POWER AMPLIFIER:

- \* Power amplifiers designed to switch large currents ON and OFF make use of MOSFET devices.
- \* MOSFET based class-D amplifiers is commonly used. Other applications include line drivers for digital switching circuits, switched mode voltage regulators.
- \* Power MOSFET's have several advantages over bipolar transistors for power amplifier applications.

- \* One of their most important advantages is a transfer characteristic which is more linear than that of a BJT.
- \* This property makes MOSFET power amplifiers to have much less output distortion than bipolar circuits.
- \* Power MOSFET's can readily be operated in parallel to reduce the total channel resistance and increase the output current level.
- \* The advantage of using MOSFET device for switching is that the "turn OFF" time is not delayed by minority-carrier storage, as it is in a bipolar transistor.
- \* Further current in a MOSFET is due to majority charge carriers only and they are not subjected to thermal runaway.
- \* In addition, very large input impedance of MOSFET, makes the design of driver circuitry less complex.
- \* Though MOSFET amplifiers can be used in class A mode, it is advantageous to use in class D mode.



$$= 5 + \frac{85 \times 8}{85 + 8} = 5 + 7.31 = 12.31^\circ\text{C/W}$$

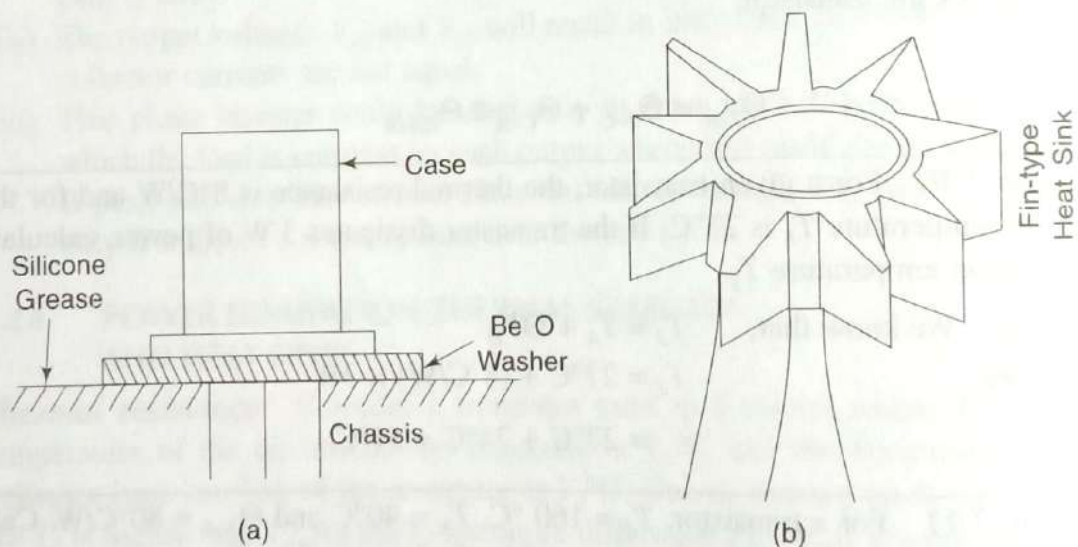
$$P_D = \frac{T_J - T_A}{Q_{J-A}} = \frac{160 - 40}{12.31} = \frac{120}{12.31} = 9.75 \text{ W}$$

### Types of heat sinks

**Low power transistor type** The small signal low power transistors can be mounted directly on the metal chassis to increase the sufficient heat dissipation capability. Care should be taken while doing this because very often the collector of the transistor is connected to the transistor case to increase heat-dissipation capabilities. Hence, some provision for insulating the case from the chassis, which is usually at ground potential, must be provided unless a common collector is being employed.

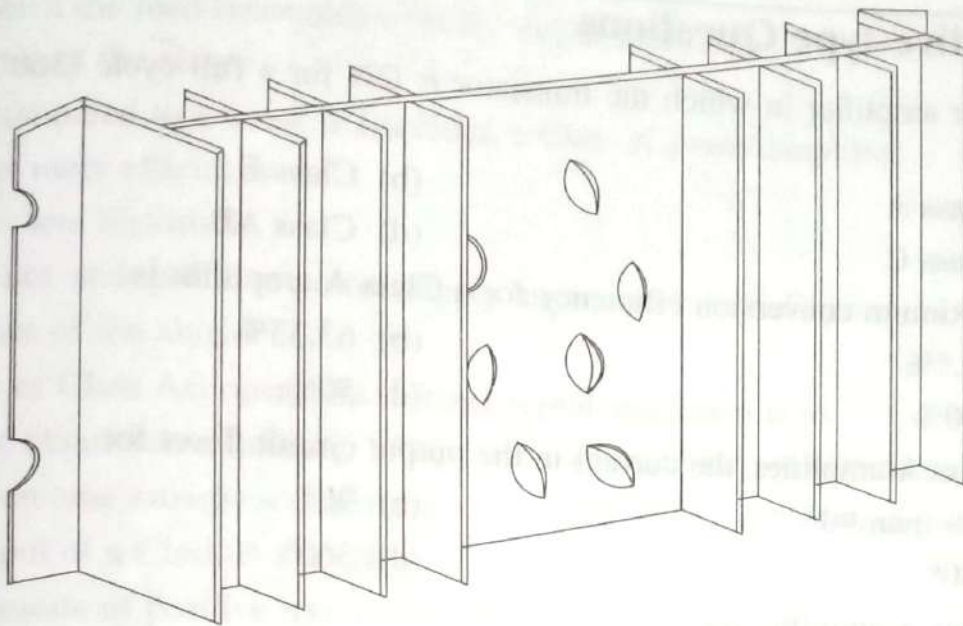
One method of achieving this is to use a beryllium oxide insulating washer which has a good thermal conductivity, as shown in Fig. 7.14(a). By using a zinc oxide film Silicon compound between the washer and the chassis, heat transfer from the transistor case to the chassis may be improved. An insulated clamp over the top of the transistor may be used to help improve thermal dissipation and increase pressure.

When the transistor is mounted in Teflon (PTFE—Poly Tere Fluoro Ethylene) sockets, it does not provide thermal conduction from transistor case to chassis. Therefore, a press-on fin type of a black anodized heat sink may be used, as shown in Fig. 7.14(b), for mounting transistors that are encased in a metal TO-5 package.



**Fig. 7.14** (a) Mounting the transistor case close to the chassis using a beryllium oxide insulating washer (b) using a separate heat sink pressed onto the transistor

**Power transistor heat sinks** The diamond shaped TO-3 and TO-66 types are the popular mounting packages used for the power transistors which have dissipation in the order of 100 W. These have two leads for emitter and base but the case, or the mounting flange of the case, is the collector terminal. So, it is necessary to insulate the case from the heat sink by the use of an insulating washer. Figure 7.15 shows a typical heat sink that can accommodate a TO-3 power transistor package that provides cooling by conduction, convection and radiation. Although measuring only 11.5 cm



**Fig. 7.15** *Power transistor heat sink*

by 7.8 cm, it has a thermal dissipation equal to that of a flat aluminium sheet 25 cm  $\times$  20 cm  $\times$  0.32 cm. The thermal resistance of this heat sink is 3°C/W.



### Introduction:-

- \* A audio amplifier amplifies a wide band of frequencies equally well and doesnot permit the selection of a particular desired frequency while rejecting all other frequencies.
- \* For instance, radio and television transmission are carried on a specific radiofrequency assigned to the broadcasting station.
- \* The radio receiver is required to pick up and amplify the radio frequency desired while discriminating all others.
- \* To achieve this, the simple resistive load is replaced by a parallel tuned circuit whose impedance strongly depends upon frequency.
- \* Therefore, the use of tuned circuits in conjunction with a transistor makes possible the selection and efficient amplification of a particular desired radio-frequency.
- \* Such an amplifier is called a Tuned amplifier.

### Advantages of Tuned Amplifiers:-

- \* In high frequency applications, it is generally required to amplify a single frequency, rejecting all other frequencies present.
- \* For such purposes, tuned amplifiers are used. These amplifiers use tuned parallel circuit as the collector load and offer the following advantages:

#### i) Small power loss:-

- A tuned parallel circuit employs reactive components L and C.
- Consequently, the power loss in such a circuit is quite low.
- \* on the other hand, if a resistive load is used in the



collector circuit, there will be considerable loss of power.  
• Therefore, tuned amplifiers are highly efficient.

### i) High selectivity:-

- \* A tuned circuit has the property of selectivity i.e. it can select the desired frequency for amplification out of a large number of frequencies simultaneously impressed upon it.
- \* For instance, if a mixture of frequencies simultaneously ~~imposed~~ upon it. It is fed to the input of a tuned amplifier, then maximum amplification occurs for  $f_s$ .
- \* For all other frequencies, the tuned circuit offers very low impedance and hence these are amplified to a little extent and may be thought as rejected by the circuit.
- \* On the other hand, if we use resistive load in the collector, all the frequencies will be amplified equally well i.e. the circuit will not have the ability to select the desired frequency.

### ii) Smaller collector supply voltage:-

- \* Because of little resistance in the parallel tuned circuit. it requires small collector supply voltage  $V_{CC}$ .
- \* On the other hand, if a high load resistance is used in the collector for amplifying even one frequency, it would mean large voltage drop across it due to zero signal collector current. Consequently, a higher collector supply will be needed.

### Why not tuned circuits for low frequency amplification:-

- \* The tuned amplifiers are used to select and amplify a specific high frequency or narrow band of frequencies.
- The reader may be inclined to think as to why tuned circuits are not used to amplify low frequencies. This is due to the following reasons:-



## i) Low frequencies are never single:

- A tuned amplifier selects and amplifies a single frequency.
- However, the low frequencies found in practice are the audio frequencies which are a mixture of frequencies from 20 Hz to 20 kHz and are not single.
- It is desired that all these frequencies should be equally amplified for proper reproduction of the signal.
- Consequently, tuned amplifiers cannot be used for the purpose.

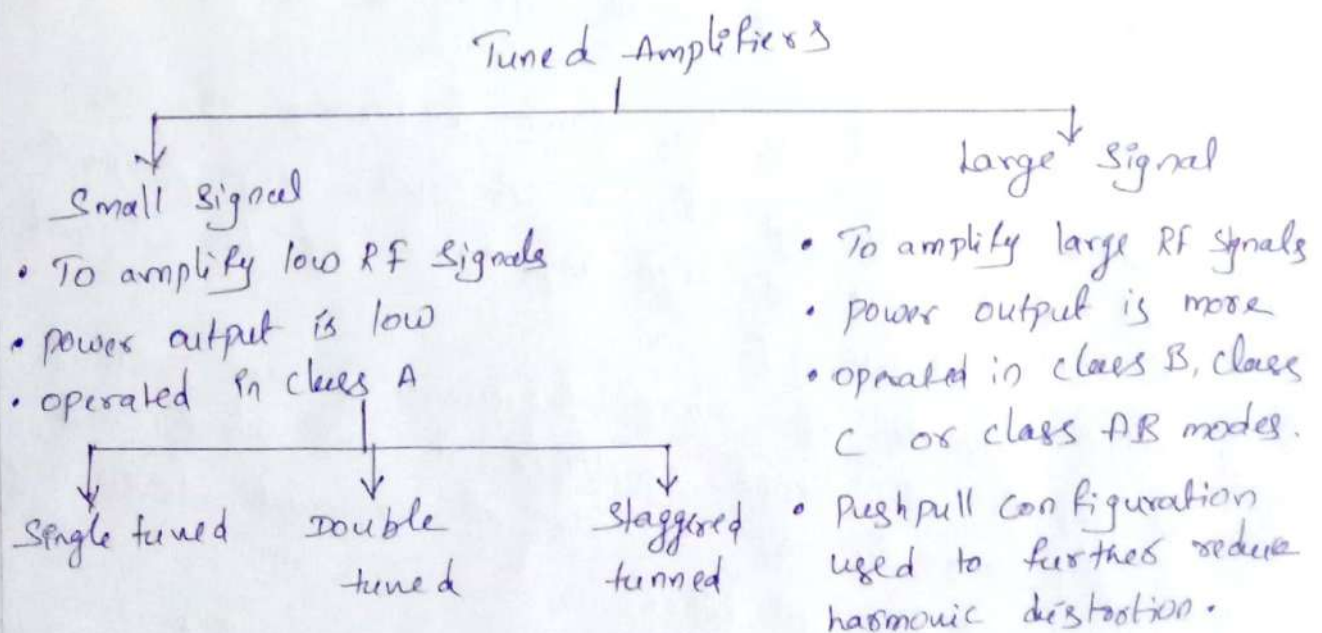
## ii) High values of L and C:

- The resonant frequency of a parallel tuned circuit is given by;

$$f_r = \frac{1}{2\pi\sqrt{LC}}$$

- For low frequency amplification, we require large values of L and C.
- This will make the tuned circuit bulky and expensive.
- It is worthwhile to mention here that R-C and transformer coupled amplifiers, which are comparatively cheap, can be conveniently used for low frequency.

## Classification:-



## Tuned amplifiers.

- Amplifiers which amplify a specific frequency or narrow band of frequencies are called Tuned amplifiers.
- Tuned amplifiers are mostly used for the amplification of high or radio frequencies.
- It is because radio frequencies are generally single and the tuned circuit permits their selection and efficient amplification.
- However, such amplifiers are not suitable for the amplification of audio frequencies as they are mixture of frequencies from  $20\text{ Hz}$  to  $20\text{ kHz}$  and not single.
- Tuned amplifiers are widely used in radio and television circuits where they are called upon to handle radio frequencies.
- The impedance of this tuned circuit strongly depends upon frequency.
- It offers a very high impedance of this tuned circuit strongly depends upon frequency.
- It offers a very high impedance of this at resonant frequency and very small impedance at all other frequencies.
- If the signal has the same frequency as the resonant frequency of LC circuit, large amplification will result due to high impedance of LC circuit at this frequency.
- When signals of many frequencies are present at the input of tuned amplifiers, it will select and strongly amplify the signals of resonant frequency while rejecting all others.
- Therefore, such amplifiers are very useful in radio receivers to select the signal from one particular



broadcasting station when signals of many other frequencies are present at the receiving aerial.

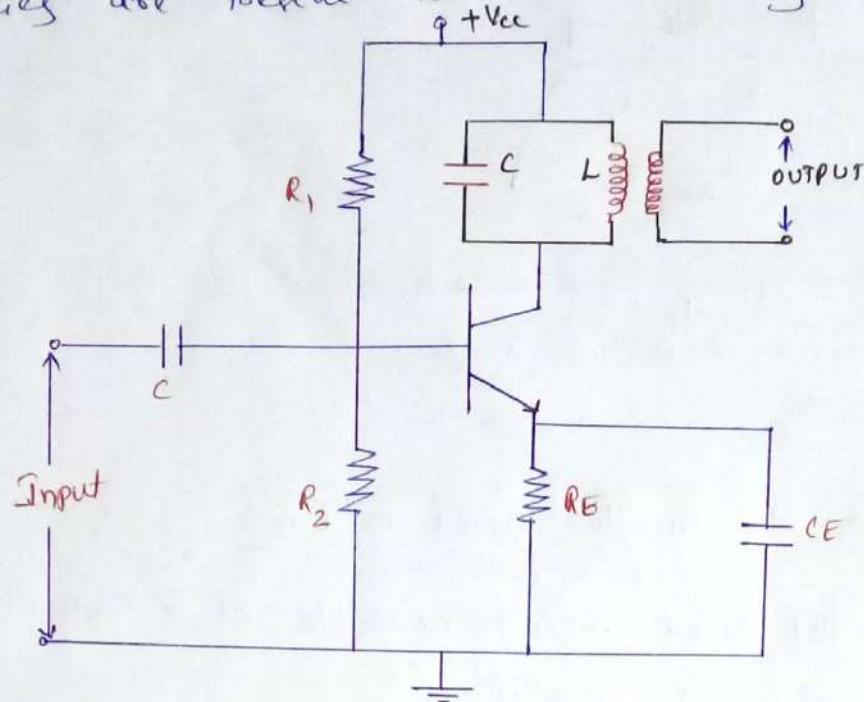
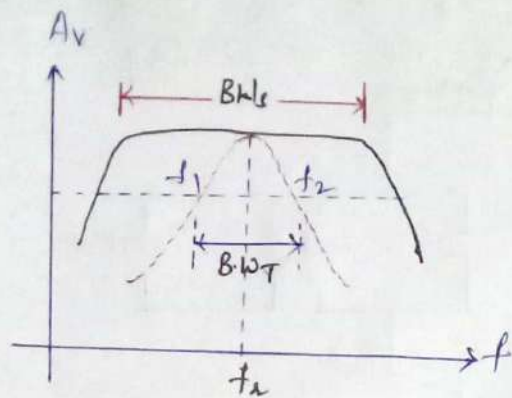


FIG: TUNED AMPLIFIER

### Distinction between Tuned Amplifiers and other Amplifiers:-

- we have seen that amplifiers (e.g., voltage amplifier, power amplifiers etc) provide the constant gain over a limited band of frequencies i.e., from lower cut-off frequency  $f_1$  to upper cut-off frequency  $f_2$ .
- Now bandwidth of the amplifier,  $BW = f_2 - f_1$ .
- The reader may wonder, then, what distinguishes a tuned amplifier from other amplifiers? The difference is that tuned amplifiers are designed to have specific, usually narrow bandwidth.
- This point is illustrated in Fig 5.2. Note that BWs is the bandwidth of standard frequency response while BWt is the bandwidth of the tuned amplifiers.
- In many applications, the narrower the bandwidth of a tuned amplifier, the better it is.



## Analysis of Parallel Tuned circuit :-

• A parallel tuned circuit consists of a capacitor  $C$  and inductor  $L$  in parallel.

i) In Practice, some resistance  $R$  is always present with the coil. If an alternating voltage is applied across this parallel circuit, the frequency of oscillations will be that of the parallel applied voltage.

• However, if the frequency of applied voltage is equal to the natural or resonant frequency of LC circuit, then electrical resonance will occur.

• Under such conditions, the impedance of the tuned circuit becomes maximum and the line current is minimum.

• The circuit then draws just enough energy from a.c. supply necessary to overcome the losses in the resistance  $R$ .

## Parallel resonance :-

• A parallel circuit containing reactive elements ( $L$  and  $C$ ) is resonant when the circuit power factor is unity i.e. applied voltage and the supply current are in phase.

• The phasor diagram of the parallel circuit.

ie) The coil current  $I_L$  has two rectangular components viz active component  $I_L \cos \omega L$  and reactive component  $I_L \sin \omega L$ . This parallel circuit will resonate when



$$f_r = \frac{1}{2\pi L} \sqrt{\frac{L}{C} - R^2}$$

$$\text{Resonant frequency } f_r = \frac{1}{2\pi} \sqrt{\frac{1}{LC} - \frac{R^2}{L^2}}$$

If coil resistance  $R$  is small (as is generally the case), then,

$$f_r = \frac{1}{2\pi\sqrt{LC}}$$

The resonant frequency will be in Hz if  $R$ ,  $L$  &  $C$  are in ohms, henry and farad respectively.

### Characteristics of Parallel Resonant Circuit :-

It is now desirable to discuss some important characteristics of parallel resonant circuit.

#### 1) Impedance of tuned circuit :-

- The impedance offered by the parallel LC circuit is given by the supply voltage divided by the line current i.e.  $V/I$ .
- Since at resonance, line current is minimum, therefore, impedance is maximum at resonant frequency. This fact is shown by the impedance - frequency curve.
- It is clear from impedance - frequency curve that impedance rises to a steep peak at resonant frequency  $f_r$ .
- However, the impedance of the circuit decreases rapidly when the frequency is changed above or below the resonant frequency.
- This characteristic of parallel tuned circuit provides it the selective properties i.e. to select the resonant frequency and reject all others.

Line current,  $I = I_L \cos \phi_L$

$$\frac{V}{Z_r} = \frac{V}{Z_L} \times \frac{R}{Z_L}$$

$$\frac{1}{Z_r} = \frac{R}{Z_L^2}$$

$$\frac{1}{Z_r} = \frac{R}{4C} = \frac{CR}{L}$$

$$[R Z_L^2 = \frac{L}{C} \text{ from eq. i)]}$$

the circuit power factor is unity. This is possible only when the net reactive component of the circuit current is zero i.e.

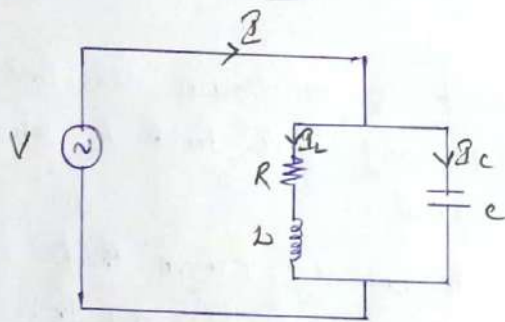
$$I_C - I_L \sin \phi_L = 0$$

$$I_C = I_L \sin \phi_L$$

- Resonance in parallel circuit can be obtained by changing the supply frequency.
- At some frequency  $f_r$  (called resonant frequency),  $I_C = I_L \sin \phi_L$  and resonance occurs.

**Resonant frequency:-**

- The frequency at which parallel resonance occurs (i.e., reactive component of circuit current becomes zero) is called the resonant frequency  $f_r$ .



At parallel resonance, we have,

$$I_C = I_L \sin \phi_L$$

$$I_L = V/Z_L; \sin \phi_L = X_L/Z_L \text{ and } I_C = V/X_C$$

$$\frac{V}{X_C} = \frac{V}{Z_L} \times \frac{X_L}{Z_L}$$

$$X_L X_C = Z_L^2$$

$$\frac{\omega L}{\omega C} = Z_L^2 = R^2 + X_L^2$$

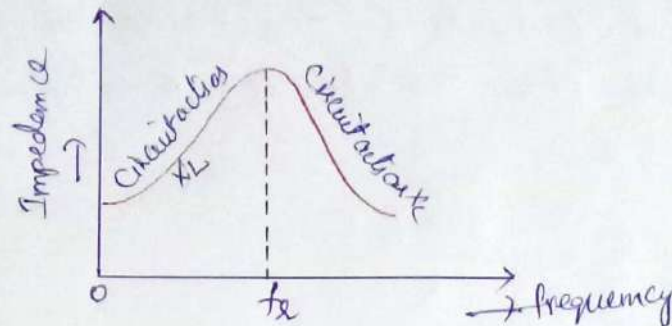
$$\frac{L}{C} = R^2 + (2\pi f_r L)^2$$

$$(2\pi f_r L)^2 = \frac{L}{C} - R^2$$

$$2\pi f_r L = \sqrt{\frac{L}{C} - R^2}$$



$\therefore$  circuit impedance,  $Z_r = \frac{L}{CR}$



- Thus at parallel resonance, the circuit impedance is equal to  $L/CR$ .
- It may be noted that  $Z_r$  will be in ohms if  $R, L$  &  $C$  are measured in ohms, henry and farad respectively.

### i) circuit current :-

At parallel resonance, the circuit or line current  $I$  is given by the applied voltage divided by the circuit impedance  $Z_r$  i.e.,

Line current,  $I = \frac{V}{Z_r}$  where  $Z_r = \frac{L}{CR}$

- Because  $Z_r$  is very high, the line current  $I$  will be very small.

### ii) Quality factor $Q$ :-

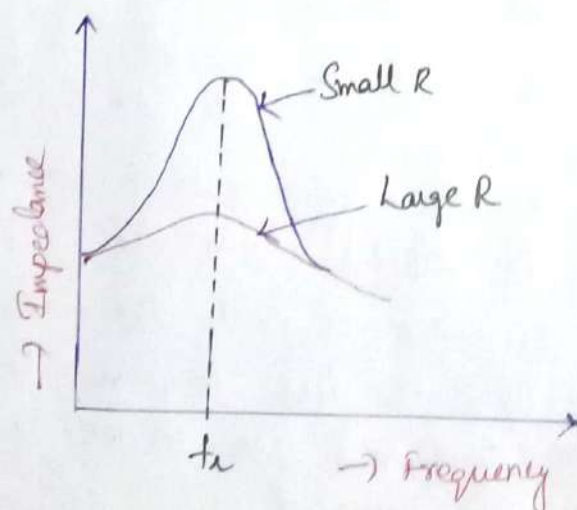
- It is desired that resonance curve of a parallel tuned circuit should be as sharp as possible in order to provide selectivity.
- The sharp resonance curve means that impedance falls rapidly as the frequency is varied from the resonant frequency.
- The smaller the resistance of coil, the more sharp is the resonance curve. This is due to the fact that a small resistance consumes less power and draws a relatively small line current.
- The ratio of inductive reactance and resistance of the coil at resonance, therefore, becomes a measure of the quality of the tuned circuit. This is called quality factor and may be defined as

under :

- The ratio of inductive reactance of the coil at resonance to its resistance is known as quality factor  $Q$  i.e.,

$$Q = \frac{X_L}{R} = \frac{2\pi f_s L}{R}$$

- The quality factor  $Q$  of a parallel tuned circuit is very important because the sharpness of resonance curve and hence selectivity of the circuit depends upon it.
- The higher the value of  $Q$ , the more selective is the tuned circuit.
- It is clear that when the resistance is small, the resonance curve is very sharp.
- However, if the coil has large resistance, the resonance curve is less sharp. It may be emphasised that where high selectivity is desired, the value of  $Q$  should be very large.



Q factor (or) Quality factor :-

It is defined as the ratio of reactance of coil to resistance of the coil.



$$Q = \frac{\omega L}{R} \quad \text{Ex: } \frac{XL}{R}$$

$$Q = 2\pi \times \frac{\text{maximum energy stored per cycle}}{\text{power dissipated per cycle}}$$

$$[\text{Energy stored in the inductor} = \frac{1}{2} LI^2 = \frac{1}{2} L I_m^2]$$

$$\text{Energy stored in the capacitor} = \frac{1}{2} CV^2$$

Power dissipation per cycle in inductor

$$\text{Energy} = \text{Power} \times \text{time}$$

$$= \left( \frac{I_m}{\sqrt{2}} \right)^2 \times R \times T$$

$$= \frac{I_m^2}{2} \times R \times T$$

$$= \frac{I^2 m R}{2f}$$

$$Q = 2\pi \times \frac{\text{max. energy stored per cycle}}{\text{power dissipation per cycle}}$$

$$Q = 2\pi \times \frac{\frac{1}{2} L I_m^2}{\frac{I^2 m R}{2f}}$$

$$= \frac{2\pi L f}{R}$$

$$= \frac{2\pi f L}{R}$$

$$Q = \frac{\omega L}{R}$$

This is the Q-factor for inductor.

$$Q\text{-factor for capacitor} = \frac{1}{\omega R C}$$

### Frequency Response of Tuned Amplifiers.

- The voltage gain of an amplifier depends upon  $D$ , input impedance and effective collector load.
- In a tuned amplifier, tuned circuit is used in the collector. Therefore, voltage gain of such an amplifier

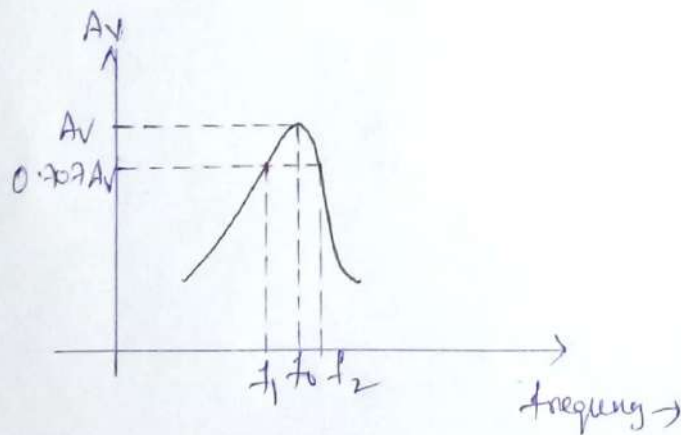
is given by :

$$\text{voltage gain} = \frac{\beta Z_c}{Z_{in}}$$

where  $Z_c$  = effective collector load

$Z_{in}$  = input impedance of the amplifier

- The value of  $Z_c$  and hence gain strongly depends upon frequency in the tuned amplifiers.
- As  $Z_c$  is maximum at resonant frequency, therefore, voltage gain will be maximum at this frequency.
- The value of  $Z_c$  and gain decrease as the frequency is varied above and below the resonant frequency.
- It is clear that voltage gain is maximum at resonant frequency and falls off as the frequency is varied in either direction from resonance.



### Band width :

- The range of frequencies at which the voltage gain of the tuned amplifier falls to 70.7% of the maximum gain is called its bandwidth.
- The amplifier will amplify nicely any signal in this frequency range.
- The bandwidth of tuned amplifiers depends upon the value of  $Q$  of LC circuit i.e. upon the sharpness



of the frequency response.

- The greater the value of  $Q$  of tuned circuit, the lesser is the bandwidth of the amplifier and vice-versa.
- In practice, the value of  $Q$  of LC circuit is made such so as to permit the amplification of desired narrow band of high frequencies.
- The practical importance of bandwidth of tuned amplifiers is found in communication system.
- In radio and TV transmission, a very high frequency wave, called carrier wave is used to carry the audio or picture signal.
- In radio transmission, the audio signal has a frequency range of 10 kHz.
- If the carrier wave frequency is 710 kHz, then the resultant radio wave has a frequency range between  $(710-5)$  kHz and  $(710+5)$  kHz.
- Consequently, the tuned amplifiers must have a bandwidth of 705 kHz to 715 kHz (i.e. 10 kHz).
- The  $Q$  of the tuned circuit should be such that bandwidth of the amplifiers lies in this range.

### Relation between $Q$ & bandwidth:

- The quality factor  $Q$  of a tuned amplifier is equal to the ratio of resonant frequency ( $f_r$ ) to bandwidth (BW) i.e.,

$$Q = \frac{f_r}{BW}$$

- The  $Q$  of an amplifier is determined by the circuit component values. It may be noted here that  $Q$  of a tuned amplifier is generally greater than 10.
- When this condition is met, the resonant frequency

at parallel resonance is approximately given by: <sup>19</sup>

$$f_r = \frac{1}{2\pi\sqrt{LC}}$$



\* An amplifier circuit with a single tuned section being at the collector of the amplifier circuit is called as single tuned amplifier circuit.

## CONSTRUCTION:

\* A simple transistor amplifier circuit consisting of a parallel tuned circuit in its collector load, makes a single tuned amplifier circuit.

\* The values of capacitance and inductance of the tuned circuit are selected such that its resonant frequency is equal to the frequency to be amplified.

\* The following circuit diagram shows a single tuned amplifier circuit.

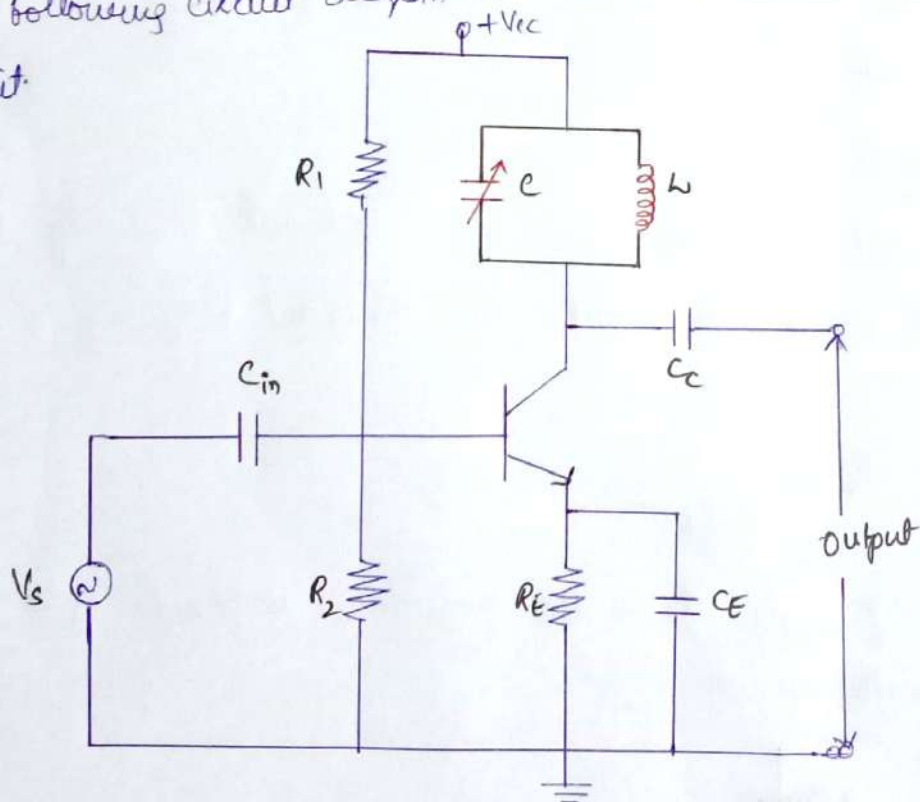


FIG: SINGLE TUNED AMPLIFIER

\* The output can be obtained from the coupling capacitor  $C_c$  as shown above or from a secondary winding placed at  $L$ .

### OPERATION:

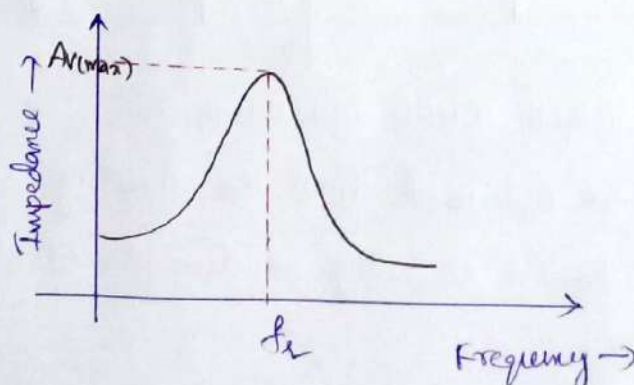
- \* The high frequency signal that has to be amplified is applied at the input of the amplifier.
- \* The resonant frequency of the parallel tuned circuit is made equal to the frequency of the signal applied by altering the Capacitance value of the Capacitor C, in the tuned circuit.
- \* At this stage, the tuned circuit offers high impedance to the signal frequency, which helps to offer high output across the tuned circuit.
- \* As high impedance is offered only for the tuned frequency, all the other frequencies which get lower impedance are rejected by the tuned circuit.
- \* Hence the tuned amplifier selects and amplifies the desired frequency signal.

### FREQUENCY RESPONSE:

- \* The parallel resonance occurs at resonant frequency  $f_r$  when the circuit has a high Q-factor, the resonant frequency  $f_r$ , is given by,

$$f_r = \frac{1}{2\pi\sqrt{LC}}$$

- \* The following graph shows the frequency response of a single tuned amplifier circuit.





\* At resonant frequency  $f_r$ , the impedance of parallel tuned circuit is very high and is purely resistive.

\* The Voltage across  $R_L$  is therefore maximum, when the circuit is tuned to resonant frequency.

\* Hence the Voltage gain is maximum at resonant frequency and drops off above and below resonant frequency.

\* The higher the  $Q$ , the narrower will the curve.

### ANALYSIS:

\* The tuned amplifiers are used for high frequency signals so hybrid- $\pi$  model is used for analysis.

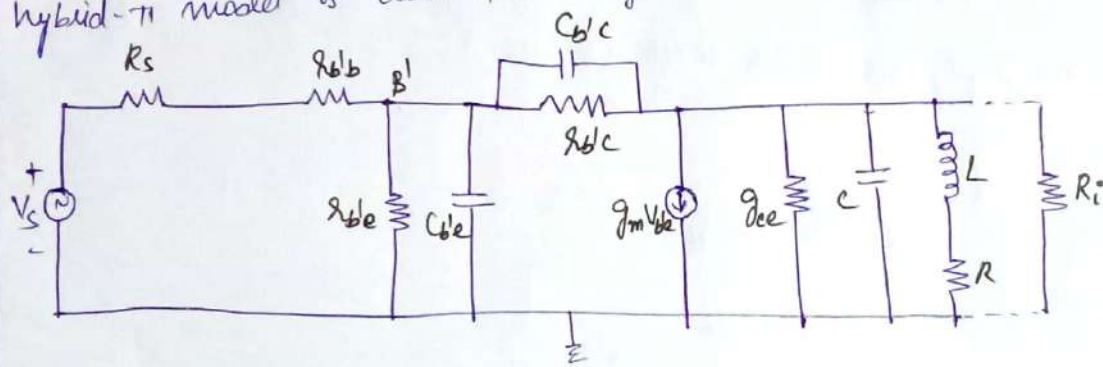
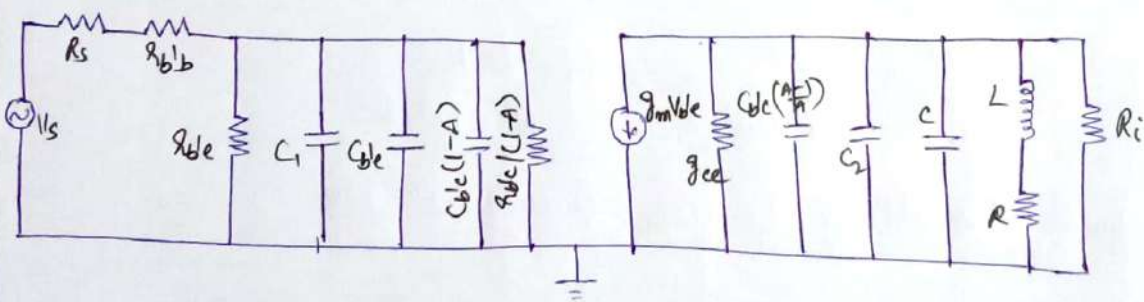


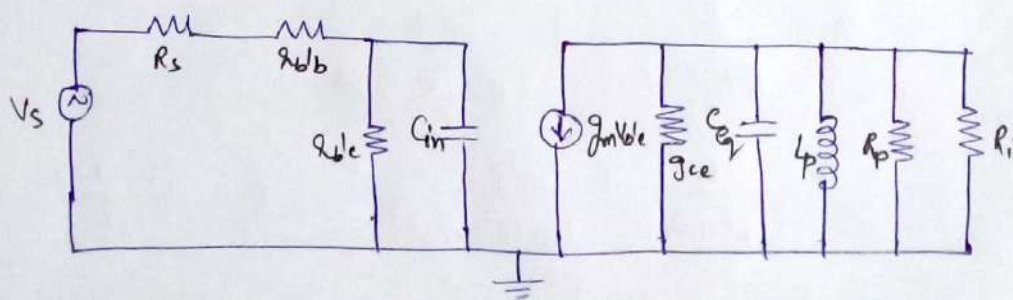
FIG: HYBRID  $\pi$  MODEL FOR SINGLE TUNED AMPLIFIER

\* Apply miller's theorem then the circuit will be



\* By neglecting  $C_1$ ,  $r_{bb}$

\* upon approximations the modified circuit will be,



From the circuit

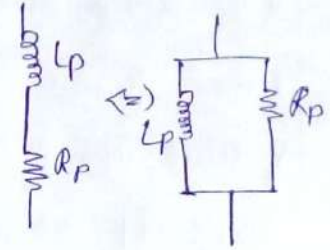
$$1. g_{ce} = \frac{1}{r_{ce}} = h_{oe} - g_m h_{re} = h_{oe}$$

$$h_{oe} = \frac{1}{R_o}$$

$$2. C_m = C_{b'e} + C_{b'c} (1-A)$$

$$3. C_{eq} = C_{b'c} \left( \frac{A-1}{A} \right) + C$$

where  $C$  = tuned circuit capacitance



### ADMITTANCE:

$$Y = \frac{1}{R + j\omega L}$$

multiply and divide with  $(R - j\omega L)$

$$Y = \frac{R - j\omega L}{(R + j\omega L)(R - j\omega L)}$$

$$= \frac{R - j\omega L}{R^2 - (j\omega L)^2}$$

$$= \frac{R - j\omega L}{R^2 + \omega^2 L^2}$$

$$Y = \frac{R}{R^2 + \omega^2 L^2} - j \frac{\omega L}{R^2 + \omega^2 L^2}$$

divide and multiply the second term with  $\omega$

$$Y = \frac{R}{R^2 + \omega^2 L^2} - j \frac{(\omega L)(\omega)}{(R^2 + \omega^2 L^2)\omega}$$

$$Y = \frac{R}{R^2 + \omega^2 L^2} - j \frac{\omega^2 L}{(R^2 + \omega^2 L^2)\omega}$$

$$Y = \frac{R}{R^2 + \omega^2 L^2} + \frac{\omega^2 L}{j\omega(R^2 + \omega^2 L^2)}$$

$$(\because -j = \frac{1}{j})$$

$$Y = \frac{1}{R_p} + \frac{1}{j\omega L_p}$$



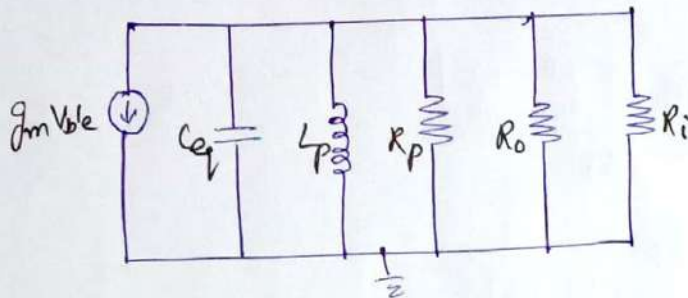
where  $R_p = \frac{R^2 + \omega^2 L^2}{R}$  and  $L_p = \frac{R^2 + \omega^2 L^2}{\omega^2 L}$  49

$$R_p = \frac{R^2 + \omega^2 L^2}{R} = \frac{R^2}{R} + \frac{\omega^2 L^2}{R} = \frac{\omega^2 L^2}{R} \quad (\because \omega L \gg R)$$

$$L_p = \frac{R^2 + \omega^2 L^2}{\omega^2 L} = \frac{R^2}{\omega^2 L} + \frac{\omega^2 L^2}{\omega^2 L} = L \quad (\because \omega L \gg R)$$

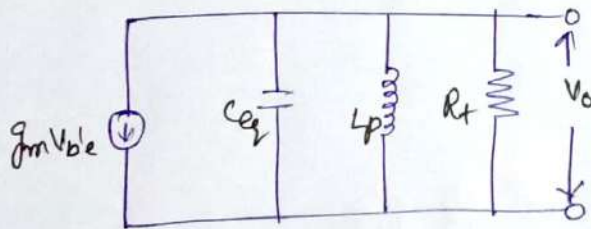
$$\frac{R^2}{\omega^2 L} \ll L$$

\* From the output circuit



$$\frac{1}{R_t} = \frac{1}{R_p} + \frac{1}{R_o} + \frac{1}{R_i}$$

where  $R_t$  is the total resistance.



\* The effective Q-factor of the output circuit (loaded Q-factor) is defined as  $Q_{\text{eff}}$ .

$$Q_{\text{eff}} = \frac{\text{Susceptance of inductance (or) capacitance}}{\text{Conductance of shunt resistance (R}_t\text{)}}$$

$$Q_{\text{eff}} \text{ for inductor } L = \frac{R_t}{\omega L} \quad Q_{\text{eff}} \text{ for capacitor } C = \omega C R_t$$

$$Y = \frac{1}{Z} = \frac{1}{R_t} + \frac{1}{j\omega L} + j\omega C$$

$$= \frac{1}{R_t} \left[ 1 + \frac{R_t}{j\omega L} + j\omega C R_t \right]$$

multiply numerator and denominator by  $\omega_0$

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$$Y = \frac{\omega_0}{R_t \omega_0} \left[ \frac{1}{R_t} + \frac{R_t}{j\omega L} + j\omega C R_t \right]$$

$$= \frac{1}{R_t} \left[ 1 + \frac{\omega_0 R_t}{\omega_0 j\omega L} + \frac{j\omega \omega_0 C R_t}{\omega_0} \right]$$

$$= \frac{1}{R_t} \left[ 1 + \frac{Q_{eff} \omega_0}{j\omega} + j \frac{Q_{eff} \omega}{\omega_0} \right] \quad [\because Q_{eff} \text{ for inductor} = \frac{R_t}{\omega_0 L}]$$

$Q_{eff} \text{ for capacitor} = \omega_0 C R_t$

$$= \frac{1}{R_t} \left[ 1 + jQ_{eff} \left[ -\frac{\omega_0}{\omega} + \frac{\omega}{\omega_0} \right] \right]$$

$$Y = \frac{1 + jQ_{eff} \left[ \frac{\omega}{\omega_0} - \frac{\omega_0}{\omega} \right]}{R_t}$$

$$Z = \frac{1}{Y} = \frac{R_t}{1 + jQ_{eff} \left[ \frac{\omega}{\omega_0} - \frac{\omega_0}{\omega} \right]}$$

\*  $\delta$  (delta) is the fractional change of the resonant frequency  $\omega_0$ .

$$\delta = \frac{\omega - \omega_0}{\omega_0} = \frac{\omega}{\omega_0} - \frac{\omega_0}{\omega_0} = \frac{\omega}{\omega_0} - 1$$

$$\delta = \frac{\omega}{\omega_0} - 1$$

$$\frac{\omega}{\omega_0} = \delta + 1 \quad \text{and} \quad \frac{\omega_0}{\omega} = \frac{1}{\delta + 1}$$

$$\text{Now, } Z = \frac{R_t}{1 + jQ_{eff} \left[ \delta + 1 - \frac{1}{\delta + 1} \right]}$$

$$= \frac{R_t}{1 + jQ_{eff} \left[ \frac{(\delta + 1)^2 - 1}{(\delta + 1)} \right]}$$

$$= \frac{R_t}{1 + jQ_{eff} \left[ \frac{\delta^2 + 2\delta + 1 - 1}{\delta + 1} \right]}$$

$$= \frac{R_t}{1 + jQ_{eff} \left[ \frac{\delta^2 + 2\delta}{\delta + 1} \right]} = \frac{R_t}{1 + jQ_{eff} \delta \left[ \frac{\delta + 2}{\delta + 1} \right]}$$



\* At any frequency, we close to the resonant frequency ( $\omega_0$ ) <sup>2</sup>

$$\delta \ll 1 \quad (\because \omega - \omega_0 \ll 1)$$

$$Z = \frac{R_t}{1 + j\omega C_{eff}\delta}$$

\* From the circuit output voltage is given by,

$$V_o = -g_m V_{b'e} * Z = -g_m V_{b'e} * \frac{R_t}{1 + j\omega C_{eff}\delta}$$

$$V_{b'e} = V_i * \frac{R_{b'e}}{R_{b'e} + R_{b'b}}$$

$$A = \frac{V_o}{V_i} = \frac{-g_m * V_i * \frac{R_{b'e}}{R_{b'e} + R_{b'b}} * \frac{R_t}{1 + j\omega C_{eff}\delta}}{V_i}$$

$$A = \frac{-g_m R_{b'e} R_t}{(R_{b'e} + R_{b'b})(1 + j\omega C_{eff}\delta)}$$

→ This is the gain without resonance.

\* The Voltage gain at resonance is  $\delta = 0$

$$A_{res} = \frac{-g_m R_{b'e} R_t}{(R_{b'e} + R_{b'b})(1 + j\omega C_{eff}(0))}$$

$$A_{res} = \frac{-g_m R_{b'e}}{R_{b'e} + R_{b'b}}$$

$$\frac{A}{A_{res}} = \frac{-g_m R_{b'e} R_t / (R_{b'e} + R_{b'b})(1 + j\omega C_{eff}\delta)}{-g_m R_{b'e} R_t / (R_{b'e} + R_{b'b})}$$

$$\frac{A}{A_{res}} = \frac{1}{1 + j\omega C_{eff}\delta}$$

$$\left| \frac{A}{A_{res}} \right| = \frac{1}{\sqrt{1 + \omega^2 C_{eff}^2 \delta^2}} \quad (\because \left| \frac{A}{A_{res}} \right| = \frac{1}{\sqrt{1 + (\omega C_{eff}\delta)^2}})$$

\* When two or more stages are cascaded then overall gain will be increased and bandwidth will be decreased

$$Q = \frac{f_c}{BW}$$

\* We know that  $\left| \frac{A}{A_{res}} \right| = \frac{1}{\sqrt{1 + (2SQ_{eff})^2}}$

\* The relative gain of n-stage Cascaded Amplifier is

$$\left| \frac{A}{A_{res}} \right|^n = \left[ \frac{1}{\sqrt{1 + (2SQ_{eff})^2}} \right]^n$$

The 3-dB frequency for cascaded amplifier is,

$$\left| \frac{A}{A_{res}} \right|^n = \frac{1}{\sqrt{1 + 2S}}$$

$$\left| \frac{A}{A_{res}} \right|^n = \frac{1}{\sqrt{2}}$$

$$\left[ \frac{1}{\sqrt{1 + (2SQ_{eff})^2}} \right]^n = \frac{1}{\sqrt{2}}$$

$$\left[ \frac{1}{1 + (2SQ_{eff})^2} \right]^{n \times \frac{1}{2}} = \left( \frac{1}{2} \right)^{\frac{1}{2}}$$

$$\left[ \frac{1}{1 + (2SQ_{eff})^2} \right]^n = \frac{1}{2}$$

$$(1 + (2SQ_{eff})^2)^n = 2$$

$$1 + (2SQ_{eff})^2 = 2^{\frac{1}{n}}$$

$$(2SQ_{eff})^2 = 2^{\frac{1}{n}} - 1$$

$$2SQ_{eff} = \sqrt{2^{\frac{1}{n}} - 1}$$

$$S = \frac{\omega - \omega_0}{\omega_0} \quad (\text{or}) \quad \frac{f - f_0}{f_0}$$



$$2\left(\frac{f-f_0}{f_0}\right) Q_{eff} = \pm \sqrt{2^{1/n} - 1}$$

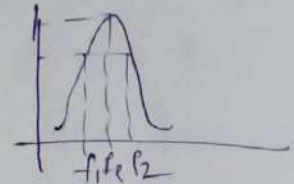
$$Q_{eff} = \pm \frac{f_0}{f-f_0}$$

$$(f-f_0) = \pm \frac{f_0}{2 Q_{eff}} \sqrt{2^{1/n} - 1}$$

\* Let us assume  $f_1$  and  $f_2$  are upper and lower cut-off frequencies then

$$\text{Bandwidth} = f_2 - f_1$$

from the fig,  $(f_2 - f_1) = (f_2 - f_0) - (f_0 - f_1)$



$$f_2 - f_0 = + \frac{f_0}{2 Q_{eff}} \sqrt{2^{1/n} - 1}$$

$$f_0 - f_1 = - \frac{f_0}{2 Q_{eff}} \sqrt{2^{1/n} - 1}$$

$$\therefore f_2 - f_1 = \frac{f_0}{2 Q_{eff}} - \left( - \frac{f_0}{2 Q_{eff}} \sqrt{2^{1/n} - 1} \right)$$

$$= \left( \frac{f_0}{2 Q_{eff}} + \frac{f_0}{2 Q_{eff}} \right) \sqrt{2^{1/n} - 1}$$

$$= \frac{2 f_0}{2 Q_{eff}} \sqrt{2^{1/n} - 1}$$

$$f_2 - f_1 = \frac{f_0}{Q_{eff}} \sqrt{2^{1/n} - 1}$$

$$\boxed{\therefore (B.W)_n = B.W \sqrt{2^{1/n} - 1}}$$

$$(\because B.W = \frac{f_0}{Q_{eff}})$$

### ADVANTAGES:

1. The power loss is less due to the lack of collector resistance.
2. Selectivity is high
3. Voltage Supply of the collector is small due to lack of  $R_c$ .

## LIMITATIONS:

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1. This tuned amplifier is required to be high selective. But high selectivity requires a tuned circuit with a high  $Q$ -factor.
2. A high  $Q$ -factor circuit will give a high  $A_v$  but at the same time, it will give much reduced bandwidth.
3. It means that tuned amplifier with reduced bandwidth may not be able to amplify equally the complete band of signals and result in poor reproduction. This is called "potential instability in tuned amplifier".

## DOUBLE TUNED AMPLIFIER:

- \* An amplifier circuit with a double tuned circuit at the collector of the amplifier circuit is called as double tuned amplifier circuit.
- \* The problem of potential instability with a single tuned amplifier is overcome in a double tuned amplifier which consists of independently coupled two tuned circuits.

## CONSTRUCTION:

- \* The circuit diagram for double tuned amplifier is as shown

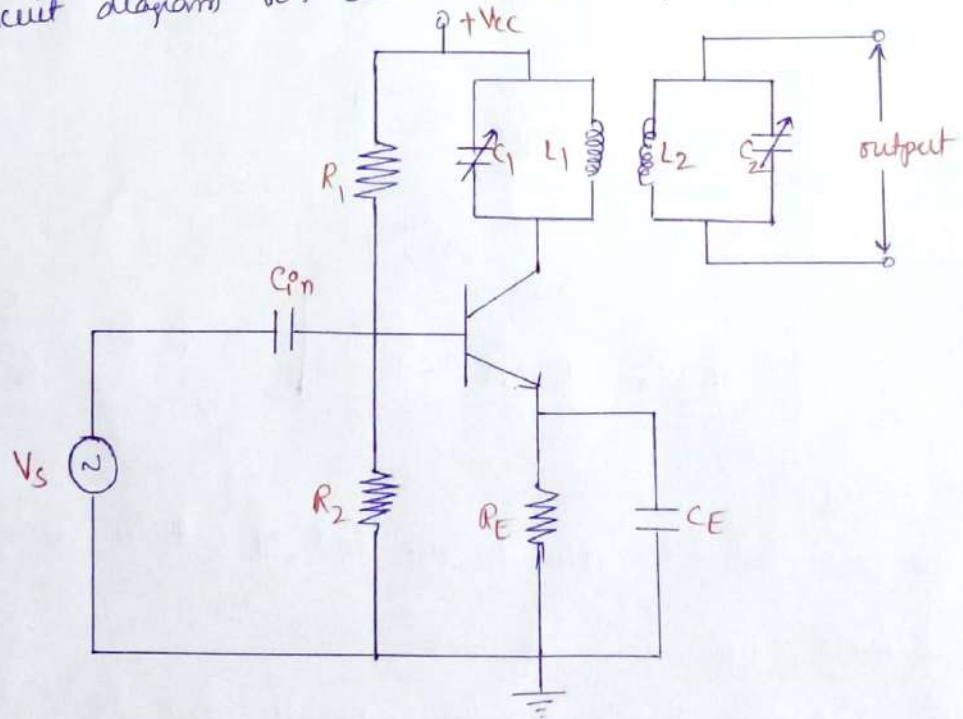


FIG: DOUBLE TUNED AMPLIFIER



\* This circuit consists of two tuned circuits  $L_1C_1$  and  $L_2C_2$  in the collector section of the amplifier.

\* The signal at the output of the tuned circuit  $L_1C_1$  is coupled to the other tuned circuit  $L_2C_2$  through mutual inductance.

#### OPERATION:

\* The high frequency signal to be amplified is applied. The resonant frequency of tuned circuit  $L_1C_1$  is made equal to the signal frequency.

\* Consequently large output appears across the tuned circuit  $L_1C_1$ . The o/p from this tuned circuit is transferred to the second tuned circuit  $L_2C_2$  through mutual induction.

\* Double tuned circuits are extensively used for coupling the various circuits of radio and television receivers.

#### FREQUENCY RESPONSE:

\* The frequency response of a double tuned circuit depends upon the degree of coupling i.e., upon the amount of mutual inductance between the two tuned circuits.

\* When coil  $L_2$  is coupled to coil  $L_1$ , a portion of load resistance has been added is coupled into the primary tank circuit  $L_1C_1$  and affects the primary circuit.

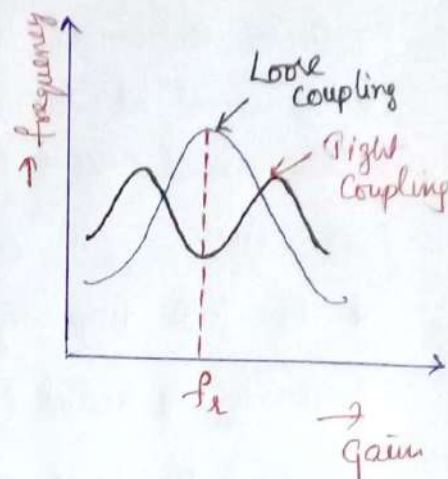
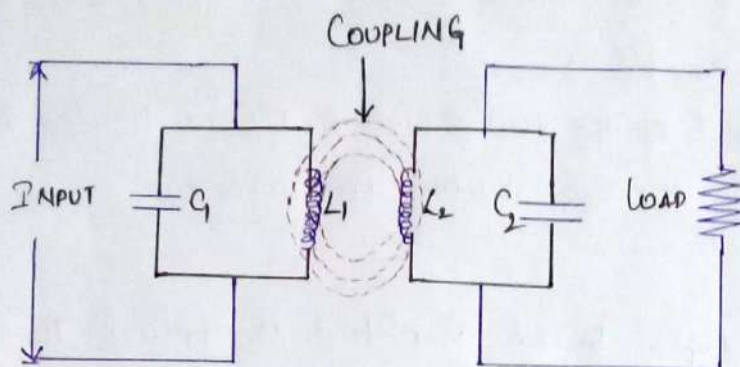
\* When the coils are spaced apart, all the primary coil  $L_1$  flux will not link the secondary coil  $L_2$ . The coils are said to have "Loose coupling".

\* Under such conditions, the resistance reflected from the load i.e., secondary circuit is small.

\* The resonance curve will be sharp and the circuit Q is high.

\* When the primary and secondary coils are very close together, then it is called "Tight coupling".

\* Under such conditions, the reflected resistance will be large and the circuit Q is lower.



### BANDWIDTH :

- \* From the frequency response of double tuned amplifier, it is clear that bandwidth increases with the degree of coupling.
- \* Obviously, the determining factor in a double tuned circuit is not  $Q$ -factor but the coupling. For a given frequency, the tighter the coupling, the greater is the bandwidth.

$$B.W = K f_r$$

Here,  $K$  is the Coefficient of Coupling.

### Effect of Cascading double tuned amplifiers on Bandwidth:

- \* When a number of identical double tuned amplifiers are connected in cascade the overall bandwidth of a system is narrowed.

$$B.W_n = B.W (2^{1/n} - 1)^{1/4}$$

### ADVANTAGES:

- \* Steeper sides in the curve
- \* ~~flat top~~ The main advantage of a double-tuned amplifier is an amplifier including a tuned circuit on the i/p and o/p.
- \* It has narrow bandwidth.
- \* Impedance matching using previous phase
- \* 3dB Bandwidth is large.
- \* Selectivity is improved.



- \* There are not suitable for amplifying audio frequencies.
- \* As the frequency band increases, then this design becomes complex.
- \* Two LC circuits tunes separately. The alignment is difficult.
- \* The design uses tuning elements like capacitors and inductors, then the circuit is costly and bulky.

### STAGGER TUNED AMPLIFIERS:

- \* Stagger tuned amplifier is a cascaded stage single tuned amplifier designed to improve the total frequency response of the tuned amplifier.
- \* The total frequency response of this amplifier can be achieved by adding up the separate response as one.
- \* When the different tuned circuits resonant frequencies are staggered otherwise displaced, then it is known as staggered tuned amplifier.

### WORKING:

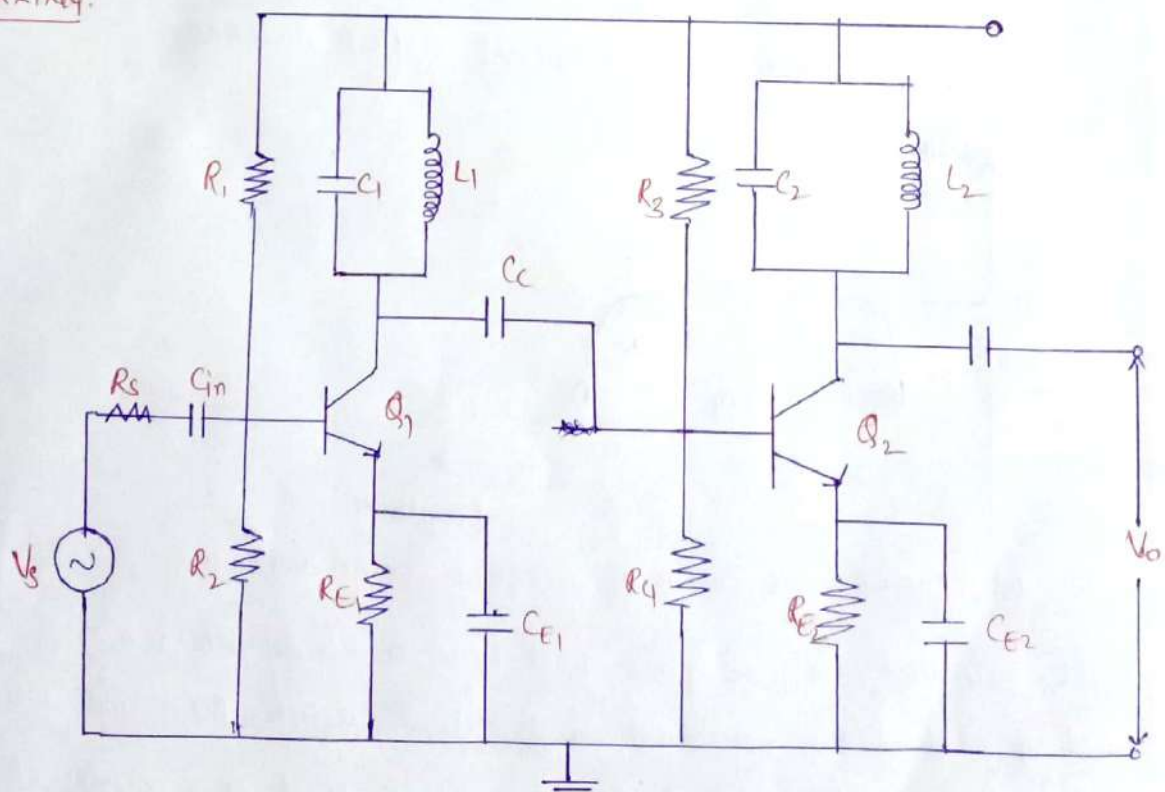


FIG: STAGGER TUNED AMPLIFIER

\* In order to increase bandwidth, double tuned amplifiers are preferred, but alignment of double tuned amplifiers is difficult. 28

\* In stagger tuned circuits, two single tuned cascade amplifiers having a certain bandwidth are taken.

\* The resonant frequencies of the two tuned circuits are so adjusted that they are separated by an amount equal to the bandwidth of each stage.

\* Since the resonant frequencies are displaced & staggered, they are known as stagger tuned circuits.

### FREQUENCY RESPONSE:

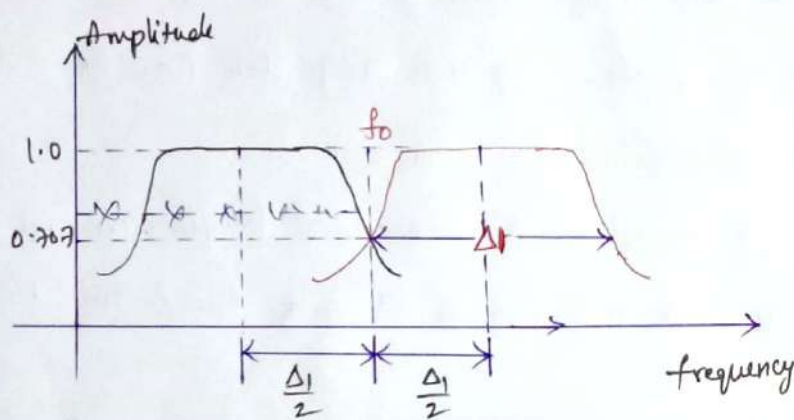


FIG: RESPONSE OF INDIVIDUAL TUNED AMPLIFIER

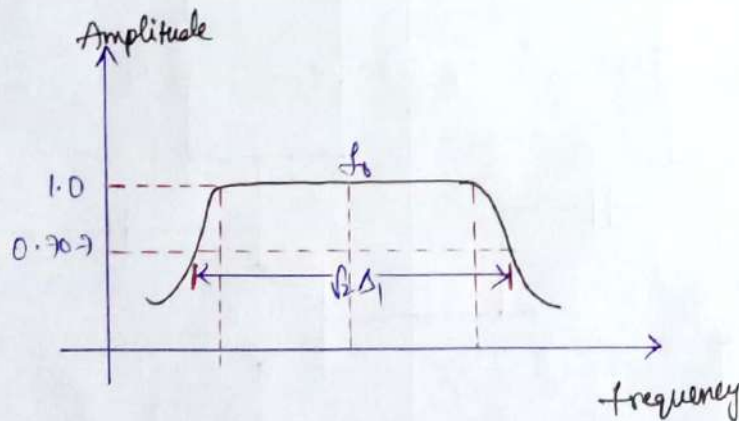


FIG: OVERALL RESPONSE OF STAGGER TUNED AMPLIFIER

\* The resultant staggered pair will have a bandwidth i.e.,  $B_2$  times that of each of the individual single tuned circuits. The overall selectivity function will be identical in form with that of a single stage double tuned system.



\* The relative gain of a single tuned direct coupled amplifier is given by, 29

$$\frac{A}{A_{res}} = \frac{1}{1 + j2S Q_{eff}}$$

$$\text{Let } \frac{A}{A_{res}} = \frac{1}{1 + jx} \quad \text{where } x = 2SQ_{eff}$$

\* As Bandwidth is  $B = \frac{f_0}{Q_{eff}}$  and under 3db frequency  $S = \frac{1}{2Q_{eff}}$ , the equation for Bandwidth can be written as  $B = 2Sf_0$ .

\* Since one stage is tuned to a frequency  $f_0$  below  $f_0$  and the other stage is tuned to a frequency  $f_0$  above  $f_0$ .

\* The corresponding selectivity function of the circuits are

$$\left(\frac{A}{A_{res}}\right)_1 = \frac{1}{1 + j(x-1)} \quad \text{and} \quad \left(\frac{A}{A_{res}}\right)_2 = \frac{1}{1 + j(x+1)}$$

\* The overall gain function becomes,

$$\begin{aligned} \left(\frac{A}{A_{res}}\right)_{\text{pair}} &= \left(\frac{A}{A_{res}}\right)_1 * \left(\frac{A}{A_{res}}\right)_2 \\ &= \left[\frac{1}{1 + j(x-1)}\right] * \left[\frac{1}{1 + j(x+1)}\right] \\ &= \frac{1}{(1 + j(x-1))(1 + j(x+1))} \\ &= \frac{1}{1 + j(x+1) + j(x-1) + j^2(x+1)(x-1)} \\ &= \frac{1}{1 + j(x+1+x-1) + j^2(x^2-1)} \\ &= \frac{1}{1 + j2x - x^2 + 1} \\ &= \frac{1}{2 + j2x - x^2} \\ &= \frac{1}{(-x^2 + 2) + j2x} \end{aligned}$$

The magnitude of the resulting function is,

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$$\left| \left( \frac{A}{A_{res}} \right)_{pair} \right| = \frac{1}{\sqrt{(2-x^2)^2 + (2x)^2}}$$

$$= \frac{1}{\sqrt{4+x^4-\cancel{4x^2}+\cancel{4x^2}}}$$

$$= \frac{1}{\sqrt{4+x^4}}$$

$$= \frac{1}{\sqrt{4+(2SQ_{eff})^4}} \quad (\because x = 2SQ_{eff})$$

$$= \frac{1}{\sqrt{4(1+SQ_{eff}^4)}}$$

$$\boxed{\left| \left( \frac{A}{A_{res}} \right)_{pair} \right| = \frac{1}{2} \frac{1}{\sqrt{1+4SQ_{eff}^4}}}$$

#### ADVANTAGES:

- \* Increased Bandwidth compared to single tuned amplifiers
- \* High gain Bandwidth product.
- \* Enhanced Stability



## Stability of Tuned Amplifiers.

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Stability  $\rightarrow$  Frequency  $\begin{cases} \text{Low} \\ \text{High} \end{cases}$  distortion will occur because of non-linear transistor

Distortion occur in low frequency

1. Non-linear  $\begin{cases} \text{Amplitude harmonic distortion} \\ \text{Frequency Harmonic distortion} \end{cases}$

2. Linear

$\Rightarrow$  we are used  $C_{bc}$  in Radio frequency Tuned Circuit.

From this we are provided positive (or) negative feedback.

If we provided positive feed back we get instability.

$\Rightarrow$  To Avoid instability we are Connected another Component from base to Collected. It provides neutralization.

The Neutralization process introduced by L.A. Hazeltine.

There are ~~three~~ types of Neutralization.

1. Hazeltine Neutralization.

2. Neutrodyne Neutralization

3. Neutralization with Coil.

✍

## 8.9 NEUTRALIZATION

The technique used for the elimination of potential oscillations is called neutralization. *BJT* and *FET* are potentially unstable over some frequency range due to the feedback parameter ( $Y_N$ ) present in them. If the feedback can be cancelled by an additional feedback signal that is equal in amplitude and opposite in sign, the transistor becomes unilateral from input to output the oscillations completely stop. This is achieved by Neutralization. The small-signal equivalent circuit of a BJT is shown in Fig. 8.28.



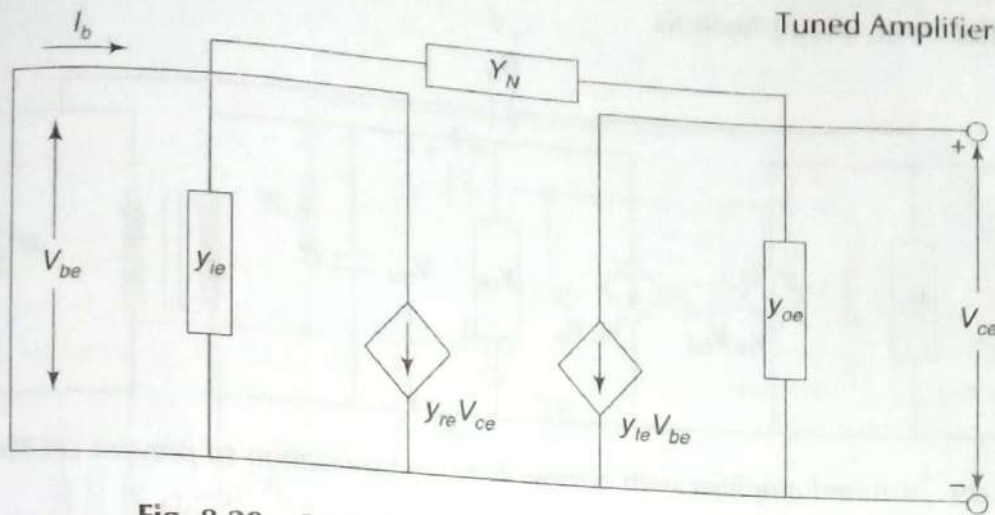


Fig. 8.28 Equivalent circuit of a neutralized transistor

From the definition of admittance parameters,

$$y'_{re} = \left. \frac{I_b}{V_{ce}} \right|_{V_{be} = 0}$$

and with the input terminal shorted

$$I_b = y_{re} V_{ce} - Y_N V_{ce}$$

Therefore,

$$I_b = V_{ce} [y_{re} - Y_N]$$

i.e.

$$\frac{I_b}{V_{ce}} = y_{re} - Y_N$$

Comparing the above equations, we get

$$y'_{re} = y_{re} - Y_N$$

For perfect neutralization,  $y'_{re} = 0$ . Therefore,  $y_{re} = Y_N$ . This indicates that oscillation does not exist if the designed circuit element matches  $y_{re}$  for all values of frequency and operating conditions.  $y_{re}$  is a nonlinear parameter which is given by

$$y_{re} \approx -j\omega C_{re}$$

which implies  $C_{re}$  is a slow varying function of both operating point and frequency. Hence the desired component that is used for neutralization is a *negative capacitor*. Since the fabrication of capacitor is complex, an inductor with negative susceptance,  $B = -j(1/\omega L)$  is preferred, which has the inverse frequency dependence characteristics. Moreover, the inductor acts as a short circuit at dc condition and need not be considered. One practical approach to this problem is to use a fixed capacitance that is transformer coupled for 180° phase shift to produce neutralization over a limited frequency range. One such circuit is shown in Fig. 8.29. Here, perfect neutralization is not possible and the problems created by limited neutralization may exceed their benefits.

**Hazeltine neutralization method** This is a neutralization technique employed in tuned RF amplifiers to maintain stability. In the circuit shown in Fig. 8.30, the undesired effect of the collector to base capacitance of the transistor is neutralized

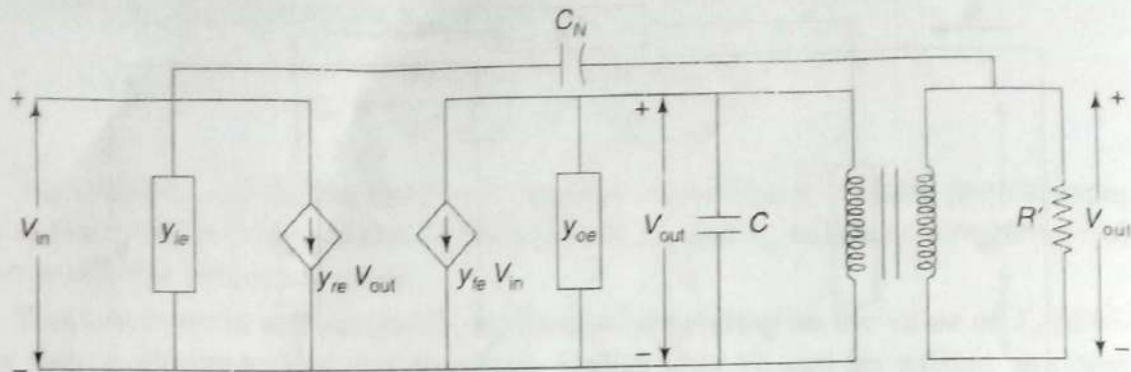


Fig. 8.29 A tuned amplifier with narrow band neutralization to prevent oscillations

by introducing a signal which cancels the signal coupled through the collector to base capacitance.

The Fig. 8.30 shows that a small variable capacitance  $C_N$  is connected from the bottom of the coil to the base of the transistor. The neutralization process is achieved by  $C_N$ . It introduces a signal to the base of the transistor such that it cancels out the signal fed to the base by  $C_{bc}$ . Generally a variable capacitor is used for neutralization as the value of  $C_{bc}$  changes with time. By properly adjusting  $C_N$ , exact neutralization is achieved.

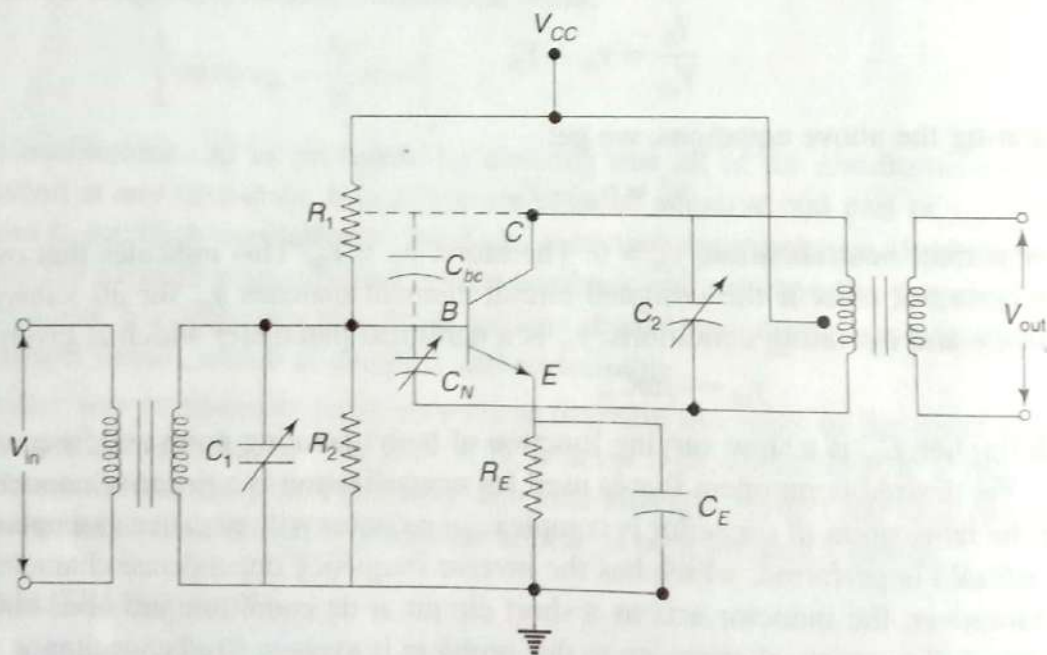
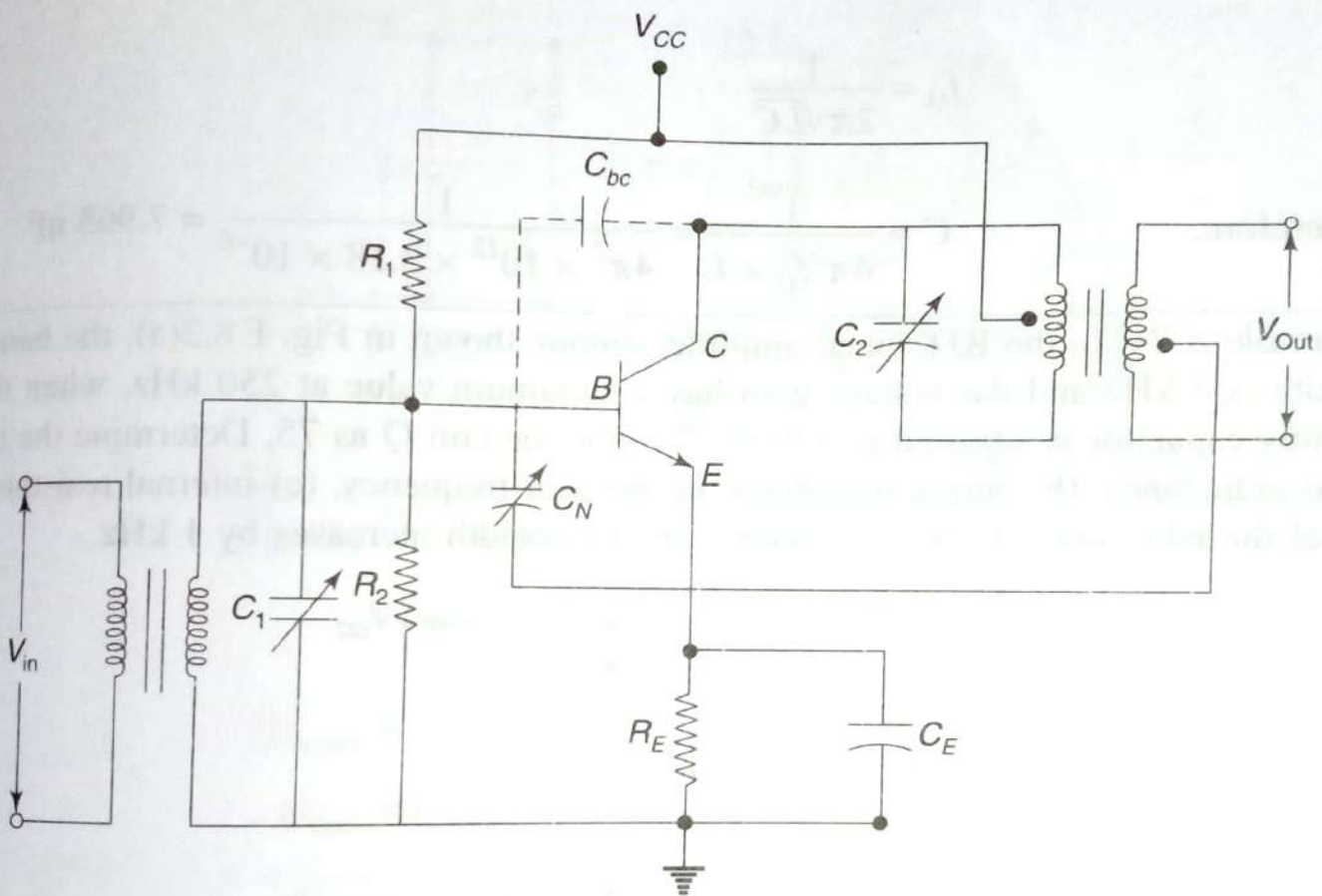


Fig. 8.30 Tuned RF amplifier with Hazeltine neutralization ( $C_N$  = neutralization capacitance)

In the modified version of Hazeltine neutralization called Neutrodyne neutralization technique,  $C_N$  is connected to the lower end of the secondary coil of the next stage. Hence it is connected with  $V_{CC}$  which ensures that, it is insensitive to any variation in the supply voltage  $V_{CC}$  and provides higher stabilization for the tuned amplifier. The circuit for the same is shown in Fig. 8.31.





**Fig. 8.31** Modified Hazeltine—neutrodyne neutralization technique