


NATIONAL BOARD OF ACCREDITATION

Data Capturing Points of the Program Applied for NBA Accreditation– Tier I/II UG (Engineering) Institute Programs

Note: To save Data Capturing Points as PDF Please click on print button and select destination as 'Save as PDF'. PLEASE SELECT LANDSCAPE MODE. 

Program Name : Electronics & Communication Engineering	Discipline : Engineering & Technology
Level : Under Graduate	Tier : 1
Application No : 10269	Date of Submission : 05-03-2025

PART A- Profile of the Institute

A1.Name of the Institute : ANNAMACHARYA INSTITUTE OF TECHNOLOGY AND SCIENCES	
Year of Establishment : 2007	Location of the Institute: Tirupati
A2. Institute Address :VENKATAPURAM (VILLAGE), MANGALAM ROAD, KARAKAMBADI(POST), RENIGUNTA(MANDAL),TIRUPATI-517520, CHITTOOR DIST	
City:Chittoor	State:Andhra Pradesh
Pin Code:517520	Website:www.aits-tpt.edu.in
Email:aitstpt@yahoo.com	Phone No(with STD Code):-
A3. Name and Address of the Affiliating University (if any):	
Name of the University : Jawaharlal Nehru Technological University Anantapu	City: Anantpur
State : Andhra Pradesh	Pin Code: 515002
A4. Type of the Institution : Self-Supported Institute	
A5. Ownership Status : Self financing	

A6. Details of all Programs being Offered by the Institution:

- No. of UG programs: **11**
- No. of PG programs: **10**

Table No. A6.1: List of all programs offered by the Institute.

Sr.No.	Discipline	Level of program	Name of the program	Year of Start	Year of Closed	Name of The Department
1	Computer Application	PG	Master of Computer Application	2022	--	Computer Application
2	Engineering & Technology	UG	Artificial Intelligence and Data Science	2020	--	Artificial Intelligence and Data Science
3	Engineering & Technology	UG	Artificial Intelligence and Machine Learning	2021	--	Artificial Intelligence and Machine Learning
4	Engineering & Technology	UG	Civil Engineering	2009	--	Civil Engineering
5	Engineering & Technology	PG	Computer Science	2012	2019	Computer Science and Engineering
6	Engineering & Technology	UG	Computer Science & Information Technology	2024	--	Computer Science and Information Technology
7	Engineering & Technology	UG	Computer Science and Engineering	2007	--	Computer Science and Engineering

8	Engineering & Technology	PG	Computer Science and Engineering	2011	--	Computer Science and Engineering
9	Engineering & Technology	UG	Computer Science and Engineering (Data Science)	2022	--	Computer Science and Engineering
10	Engineering & Technology	UG	Computer Science and Engineering (Internet of Things)	2020	--	Computer Science and Engineering
11	Engineering & Technology	PG	Digital Electronics & Communication Systems	2012	--	Electronics and Communication Engineering
12	Engineering & Technology	PG	Digital Systems & Computer Electronics	2011	2019	Electronics and Communication Engineering
13	Engineering & Technology	UG	Electrical & Electronics Engineering	2007	--	Electrical and Electronics Engineering
14	Engineering & Technology	UG	Electronics & Communication Engineering	2007	--	Electronics and Communication Engineering
15	Engineering & Technology	UG	Information Technology	2007	2013	Information Technology
16	Engineering & Technology	UG	Mechanical Engineering	2010	--	Mechanical Engineering
17	Engineering & Technology	PG	Power Electronics	2014	2019	Electrical and Electronics Engineering
18	Engineering & Technology	PG	Power Systems	2013	--	Electrical and Electronics Engineering
19	Engineering & Technology	PG	Production Engineering & Engineering Design	2014	--	Mechanical Engineering
20	Engineering & Technology	PG	Structural Engineering	2013	--	Civil Engineering
21	Management	PG	Masters in Business Administration	2008	--	Management

A7. Programs to be considered for Accreditation vide this Application:

Table No. A7.1: List of programs to be considered for accreditation.

Name of the Department	Having Allied Departments	Name of the Program	Program Level
Civil Engineering	No	Civil Engineering	UG
Electronics and Communication Engineering	No	Electronics & Communication Engineering	UG

Table No. A7.2: Allied Department(s) to the Department of the program considered for accreditation as above.
Cluster ID. Name of the Department (in table no. A7.1) Name of allied Departments/Cluster (for table no. A7.1)

No Record

PART-B: Program information

B1. Provide the Required Information for the Program Applied For:

Table No. B1: Program details.

A. List of the Programs Offered by the Department:

SR.NO.	PROGRAM NAME	PROGRAM APPLIED LEVEL	YEAR OF START / YEAR OF CLOSED	SANCTIONED INTAKE	INCREASE/DECREASE INTAKE (if any)	YEAR OF INCREASE/DECREASE	CURRENT INTAKE	YEAR OF AICTE APPROVAL	AICTE/COMPETENT AUTHORITY APPROVAL DETAILS	ACCREDITATION STATUS	FROM	TO	NO. OF TIMES PROGRAM ACCREDITED	PROGR/ DURATIC
1	Electronics & Communication Engineering	UG	2007 / --	60	Yes	2014	180	2014	F.No. South-Central/1-2019789104/2014/EOA	Granted accreditation for 3 years for the period (specify period)	2016	2025	3	4

Sanctioned Intake for Last Five Years for the Digital Electronics & Communication Systems

Academic Year	Sanctioned Intake
2024-25	180
2023-24	180
2022-23	180
2021-22	180
2020-21	180
2019-20	180

List of the Allied Departments/Cluster and Programs:

B2. Detail of Head of the Department for the program under consideration:

A. Name of the HoD :	Dr.N.Pushpalatha
B. Nature of appointment:	Regular
C. Qualification:	ME/M. Tech and PhD

B3. Program Details

Table No.B3.1: Admission details for the program excluding those admitted through multiple entry and exit points.

Item (Information to be provided cumulatively for all the shifts with explicit headings, wherever applicable)	2024-25 (CAY)	2023-24 (CAYm1)	2022-23 (CAYm2)	2021-22 (CAYm3)	2020-21 (CAYm4)	2019-20 (CAYm5)	2018-19 (CAYm6)
N=Sanctioned intake of the program (as per AICTE /Competent authority)	180	180	180	180	180	180	180
N1=Total no. of students admitted in the 1st year minus the no. of students, who migrated to other programs/ institutions plus no. of students, who migrated to this program	176	154	152	133	124	179	115
N2=Number of students admitted in 2nd year in the same batch via lateral entry including leftover seats	0	40	30	28	45	19	53
N3=Separate division if any	0	0	0	0	0	0	0
N4=Total no. of students admitted in the 1st year via all supernumerary quotas	16	17	17	17	13	12	0
Total number of students admitted in the program (N1 + N2 + N3 + N4) - excluding those admitted through multiple entry and exit points.	192	211	199	178	182	210	168

CAY= Current Academic Year. CAYm1= Current Academic Year Minus 1 CAYm2= Current Academic Year Minus 2. LYG= Last Year Graduate. LYGm1= Last Year Graduate Minus 1. LYGm2= Last Year Graduate Minus 2.

B4. Enrolment Ratio in the First Year

Table No. B4.1: Student enrolment ratio in the 1st year.

Year of entry	N (From Table 4.1)	N1 (From Table 4.1)	N4 (From Table 4.1)	Enrollment Ratio [(N1/N)*100]
2024-25 (CAY)	180	16	0	106.67
2023-24 (CAYm1)	180	17	0	95.00
2022-23 (CAYm2)	180	17	0	93.89

Average [(ER1 + ER2 + ER3) / 3] = 98.52≡ 20.00

B5. Success Rate of the Students in the Stipulated Period of the Program

Table No.B5.1: The success rate in the stipulated period of a program.

Item	(2020-21) LYG	(2019-20) LYGm1	(2018-19) LYGm2
A*= (No. of students admitted in the 1st year of that batch and those actually admitted in the 2nd year via lateral entry, plus the number of students admitted through multiple entry (if any) and separate division if applicable, minus the number of students who exited through multiple entry (if any).	182.00	210.00	168.00
B=No. of students who graduated from the program in the stipulated course duration	155.00	182.00	126.00
Success Rate (SR)= (B/A) * 100	85.16	86.67	75.00

Average SR of three batches ((SR_1+ SR_2+ SR_3)/3): 82.28

B6. Academic Performance of the First-Year Students of the Program

Table No.B6.1: Academic Performance of the First-Year Students of the Program.

Academic Performance	CAYm1(2023-24)	CAYm2(2022-23)	CAYm3 (2021-22)
Mean of CGPA or mean percentage of all successful students(X)	7.46	7.45	7.12
Y=Total no. of successful students	126.00	130.00	130.00
Z=Total no. of students appeared in the examination	153.00	152.00	133.00
API [X*(Y/Z)]	6.14	6.37	6.96

Average API[(AP1+AP2+AP3)/3] : 6.49

B7: Academic Performance of the Second Year Students of the Program

Table No.B7.1: Academic Performance of the Second Year Students of the Program.

Academic Performance	CAYm1 (2023-24)	CAYm2 (2022-23)	CAYm3 (2021-22)
X=(Mean of 2nd year grade point average of all successful students on a 10-point scale) or (Mean of the percentage of marks of all successful students in 2nd year/10)	7.75	7.70	7.69
Y=Total no. of successful students	140.00	148.00	158.00
Z=Total no. of students appeared in the examination	160.00	158.00	181.00
API [X * (Y/Z)]	6.78	7.21	6.71

Average API [(AP1 + AP2 + AP3)/3] : 6.90

B8. Academic Performance of the Third Year Students of the Program

Table No.B8.1: Academic Performance of the Third Year Students of the Program

Academic Performance	CAYm1 (2023-24)	CAYm2 (2022-23)	CAYm3 (2021-22)
----------------------	-----------------	-----------------	-----------------

X=(Mean of 3rd year grade point average of all successful students on a 10-point scale) or (Mean of the percentage of marks of all successful students in 3rd year/10)	7.79	7.78	7.38
Y=Total no. of successful students	140.00	155.00	182.00
Z=Total no. of students appeared in the examination	148.00	158.00	182.00
API [$X*(Y/Z)$]:	7.37	7.63	7.38

Average API [(AP1 + AP2 + AP3)/3] : 7.46

B9. Placement, Higher Studies, and Entrepreneurship

Table No.B9.1: Placement, higher studies, and entrepreneurship details.

Item	LYG (2020-21)	LYGm1(2019-20)	LYGm2(2018-19)
FS*=Total no. of final year students	225.00	199.00	233.00
X=No. of students placed	127.00	143.00	146.00
Y=No. of students admitted to higher studies	6.00	7.00	0.00
Z=Total no. of students appeared in the examination	1.00	0.00	0.00
Placement Index(P) = $((X + Y + Z)/FS) * 100$:	59.56	75.38	62.66

Average Placement Index = $(P_1 + P_2 + P_3)/3$: 65.87 Placement Index Points:

PART C: Faculty Details in Department and Allied Departments

(Data to be filled in for the Department and Allied Departments)

C1. Faculty details of Department and Allied Departments

Table No.C1: Faculty details in the Department for the past 3 years including CAY

Sr.No	Name of the Faculty	PAN No.	Highest degree	University	Area of Specialization	Date of Joining in this Institution	Experience in years in current institute	Designation at Time Joining in this Institution	Present Designation	The date on which Designated as Professor/ Associate Professor if any	Nature of Association (Regular/ Contract/ Ad hoc)	Currently Associated (Y/N)	In case of NO, Date of Leaving	IS HOD?
1	Dr.N.Pushpalatha	XXXXXXXX93M	ME/M. Tech and PhD	JNTUH	Wireless sensor networks	02/06/2008	16.8	Assistant Professor	Professor	01/01/2020	Regular	Yes		Yes
2	Dr.S.Venkatesan	XXXXXXXX90H	ME/M. Tech and PhD	Sathyabama University	Signal processing	10/06/2022	2.8	Professor	Professor	10/06/2022	Regular	Yes		No
3	Dr.I.Suneetha	XXXXXXXX20M	ME/M. Tech and PhD	Sri Venkateswara University Tirupati	Digital Image Processing	28/09/2007	17.4	Associate Professor	Professor	13/12/2017	Regular	Yes		No
4	Dr.K.Murali Babu	XXXXXXXX72R	ME/M. Tech and PhD	Annamalai University	Communication Systems	10/08/2022	2.6	Professor	Professor	10/08/2022	Regular	Yes		No
5	Dr.R.Kalyan	XXXXXXXX52R	ME/M. Tech and PhD	JNTUA	UWB Microstrip Antennas	06/05/2019	5.2	Assistant Professor	Associate Professor	16/12/2020	Regular	No	19/07/2024	No

6	Dr.P.Harish	XXXXXXX65L	ME/M. Tech and PhD	Vel Tech Rangarajan Dr. Sagunthala R&D Institute of Science and Technology,	IMAGE PROCESSING	01/07/2014	10.7	Assistant Professor	Associate Professor	28/04/2022	Regular	Yes		No
7	Dr.R.Senthamil Selvan	XXXXXXX17C	ME/M. Tech and PhD	Bharath Institute of Higher Education and Research	Wireless Networks	10/06/2022	2.8	Associate Professor	Associate Professor	10/06/2022	Regular	Yes		No
8	Dr. D. Jithendra Reddy	XXXXXXX61M	ME/M. Tech and PhD	Kalasalingam University	IMAGE PROCESSING	14/06/2023	1.7	Assistant Professor	Assistant Professor		Regular	Yes		No
9	Dr.M.Dharani	XXXXXXX71C	ME/M. Tech and PhD	Sri Venkateswara University Tirupati	Image Processing	10/06/2022	1.1	Associate Professor	Associate Professor	10/06/2022	Regular	No	22/07/2023	No
10	Dr.K.Jansi Lakshmi	XXXXXXX40L	ME/M. Tech and PhD	Sri Venkateswara University Tirupati	VLSI	01/11/2023	1.3	Assistant Professor	Associate Professor	29/01/2025	Regular	Yes		No
11	Dr.S.Rohini	XXXXXXX85F	ME/M. Tech and PhD	Sri Venkateswara University Tirupati	Remote Sensing	01/11/2023	1.3	Assistant Professor	Associate Professor	29/07/2024	Regular	Yes		No
12	Dr.A.Rajani	XXXXXXX66B	ME/M. Tech and PhD	Sri Venkateswara University Tirupati	Remote Sensing and GIS	28/06/2010	13.4	Assistant Professor	Associate Professor	21/04/2023	Regular	No	02/11/2023	No
13	Mr.P.Rajesh	XXXXXXX87A	M.E/M.Tech	Jawaharlal Nehru Technological University Anantapuramu	VLSI-DESIGN	16/12/2010	12.7	Assistant Professor	Assistant Professor		Regular	No	15/07/2023	No
14	Mr.Y.Penchalaiah	XXXXXXX81P	M.E/M.Tech	Sri Venkateswara University Tirupati	LICS	19/09/2011	13.4	Assistant Professor	Assistant Professor		Regular	Yes		No
15	Mr.N.Dilip Kumar	XXXXXXX26G	M.E/M.Tech	SRM University	VLSI Design	12/06/2014	10.8	Assistant Professor	Assistant Professor		Regular	Yes		No
16	Mr.M.Lakshmi Narayana Reddy	XXXXXXX37P	M.E/M.Tech	Jawaharlal Nehru Technological University Anantapuramu	DECS	02/03/2015	8.4	Assistant Professor	Assistant Professor		Regular	No	01/07/2023	No
17	Mr. S. Thanoj Kumar	XXXXXXX16J	M.E/M.Tech	Jawaharlal Nehru Technological University Anantapuramu	Digital Systems Computer Electronics	02/03/2015	8.4	Assistant Professor	Assistant Professor		Regular	No	01/07/2023	No
18	Ms.K.Kalyani	XXXXXXX34B	M.E/M.Tech	JNTUA	Digital Electronics and Communication Systems	11/05/2015	9.9	Assistant Professor	Assistant Professor		Regular	Yes		No

19	Ms.T.Jyothi	XXXXXXX83F	M.E/M.Tech	JNTUA	VLSI Design	10/02/2016	9	Assistant Professor	Assistant Professor		Regular	Yes		No
20	Ms.R.Revathi	XXXXXXX94L	M.E/M.Tech	Jawaharlal Nehru Technological University Anantapuramu	VLSI Design	01/07/2017	7.7	Assistant Professor	Assistant Professor		Regular	Yes		No
21	Ms.V.Vijaya Lakshmi	XXXXXXX13A	M.E/M.Tech	Jawaharlal Nehru Technological University Anantapuramu	Digital systems and computer electronics	01/07/2017	7.7	Assistant Professor	Assistant Professor		Regular	Yes		No
22	Ms.A.Revathi	XXXXXXX31E	M.E/M.Tech	Jawaharlal Nehru Technological University Anantapuramu	Embedded Systems	01/07/2017	7.7	Assistant Professor	Assistant Professor		Regular	Yes		No
23	Mr.M.Nagaraj	XXXXXXX35J	M.E/M.Tech	SRM University	Communication Systems	01/07/2017	7.7	Assistant Professor	Assistant Professor		Regular	Yes		No
24	Ms.N.Latha	XXXXXXX82A	M.E/M.Tech	Jawaharlal Nehru Technological University Anantapuramu	Digital Electronics Communication Systems	07/07/2017	7.7	Assistant Professor	Assistant Professor		Regular	Yes		No
25	Ms.G.Chandini	XXXXXXX30H	M.E/M.Tech	Sri Padmavathi Mahila Visvavidyalayam	Digital electronics and Communication Systems	15/12/2017	7.1	Assistant Professor	Assistant Professor		Regular	Yes		No
26	Ms.A.Mounika	XXXXXXX02R	M.E/M.Tech	Jawaharlal Nehru Technological University Anantapuramu	VLSI SYSTEM DESIGN	18/06/2018	6.7	Assistant Professor	Assistant Professor		Regular	Yes		No
27	Ms.K.Vijaya Lakshmi	XXXXXXX29F	M.E/M.Tech	Jawaharlal Nehru Technological University Anantapuramu	Control systems	25/06/2018	6	Assistant Professor	Assistant Professor		Regular	No	19/07/2024	No
28	Ms.A.S.Lavanya	XXXXXXX35C	M.E/M.Tech	JNTUH	Embedded Systems	27/06/2019	5.7	Assistant Professor	Assistant Professor		Regular	Yes		No
29	Ms.S.Vidya Rani	XXXXXXX04F	M.E/M.Tech	JNTUA	VLSI Design	29/07/2019	5.6	Assistant Professor	Assistant Professor		Regular	Yes		No
30	Ms.K.S.Deveswari	XXXXXXX06M	M.E/M.Tech	Sathyabama University	Applied Electronics	25/07/2019	5.6	Assistant Professor	Assistant Professor		Regular	Yes		No
31	Ms.Shaik Benargee	XXXXXXX31J	M.E/M.Tech	Sri Padmavathi Mahila Visvavidyalayam	Digital Electronics Communication Systems	22/07/2019	3.4	Assistant Professor	Assistant Professor		Regular	No	19/12/2022	No
32	Ms.N.Alekya	XXXXXXX59F	M.E/M.Tech	Sri Venkateswara University Tirupati	Signal processing	22/07/2019	5.6	Assistant Professor	Assistant Professor		Regular	No	19/07/2024	No

33	Ms.M.Sreelakshmi	XXXXXXX63D	M.E/M.Tech	JNTUA	Digital Systems and Computer Electronics	16/07/2020	4.6	Assistant Professor	Assistant Professor		Regular	No	28/07/2023	No
34	Ms.P.B.Lavanya	XXXXXXX84Q	M.E/M.Tech	JNTUA	Digital Electronics and Communication Systems	10/08/2020	4.6	Assistant Professor	Assistant Professor		Regular	Yes		No
35	Ms.E.Devisri	XXXXXXX32F	M.E/M.Tech	JNTUA	VLSI SYSTEM DESIGN	10/08/2020	4.6	Assistant Professor	Assistant Professor		Regular	No	29/02/2024	No
36	Mr.G.Kishore	XXXXXXX17R	M.E/M.Tech	JNTUA	Digital Electronics and Communication Systems	10/08/2020	4.6	Assistant Professor	Assistant Professor		Regular	No	19/07/2024	No
37	Mr.P.Kiran	XXXXXXX96C	M.E/M.Tech	JNTUA	Digital systems and computer electronics	13/08/2020	4.5	Assistant Professor	Assistant Professor		Regular	Yes		No
38	Mr.A.Vadivelu	XXXXXXX54G	M.E/M.Tech	JNTUA	Embedded Systems	13/08/2020	4.5	Assistant Professor	Assistant Professor		Regular	No	31/10/2022	No
39	Mr.D.Vishnuvardhan Reddy	XXXXXXX13N	M.E/M.Tech	JNTUA	Digital systems and computer electronics	13/08/2020	4.5	Assistant Professor	Assistant Professor		Regular	No	31/07/2023	No
40	Ms.M.Suneetha	XXXXXXX71C	M.E/M.Tech	JNTUA	Digital Electronics and Communication Systems	13/08/2020	4.5	Assistant Professor	Assistant Professor		Regular	Yes		No
41	Ms.G.Anitha Rani	XXXXXXX79H	M.E/M.Tech	JNTUA	Digital electronics and Communication Systems	10/08/2020	4.6	Assistant Professor	Assistant Professor		Regular	No	12/07/2023	No
42	Ms.K.B.Meena Kumari	XXXXXXX73H	M.E/M.Tech	JNTUA	VLSI SYSTEM DESIGN	07/06/2021	3.8	Assistant Professor	Assistant Professor		Regular	No	30/12/2023	No
43	Mr.P.Praveen Kumar	XXXXXXX81R	M.E/M.Tech	JNTUK	Embedded Systems	07/06/2021	3.8	Assistant Professor	Assistant Professor		Regular	No	02/12/2023	No
44	Mr.R.Nagaraju	XXXXXXX46G	M.E/M.Tech	JNTUA	Digital electronics and Communication Systems	07/06/2021	3.8	Assistant Professor	Assistant Professor		Regular	Yes		No
45	Ms.S.Bhavani	XXXXXXX72B	M.E/M.Tech	Sri Padmavathi Mahila Visvavidyalayam	Digital electronics and Communication Systems	18/08/2022	2.5	Assistant Professor	Assistant Professor		Regular	Yes		No
46	Mr.J.Gurunadhan	XXXXXXX44N	M.E/M.Tech	JNTUA	Digital systems and computer electronics	26/09/2022	2.4	Assistant Professor	Assistant Professor		Regular	No	28/07/2024	No

47	Mr.P.Anil Kumar	XXXXXXX08B	M.E/M.Tech	JNTUA	Digital Electronics and Communication Systems	01/11/2022	2.3	Assistant Professor	Assistant Professor		Regular	No	27/07/2024	No
48	Mr.K.Manjunath	XXXXXXX53A	M.E/M.Tech	JNTUA	Digital electronics and Communication Systems	16/02/2023	1.11	Assistant Professor	Assistant Professor		Regular	No	29/07/2024	No
49	Ms.G.Haritha	XXXXXXX57A	M.E/M.Tech	Sri Padmavathi Mahila Visvavidyalayam	Digital Electronics and Communication Systems	16/06/2023	1.7	Assistant Professor	Assistant Professor		Regular	No	03/10/2023	No
50	Ms.O.Naga Damini	XXXXXXX43E	M.E/M.Tech	Sri Padmavathi Mahila Visvavidyalayam	Digital Electronics and Communication Systems	16/06/2023	1.7	Assistant Professor	Assistant Professor		Regular	Yes		No
51	Ms.S.Thejaswini	XXXXXXX57R	M.E/M.Tech	Sri Venkateswara University Tirupati	Signal processing	16/06/2023	1.7	Assistant Professor	Assistant Professor		Regular	Yes		No
52	Ms. C. H. Sai Yasaswini	XXXXXXX72E	M.E/M.Tech	Sri Venkateswara University Tirupati	Signal processing	16/06/2023	1.7	Assistant Professor	Assistant Professor		Regular	No	23/03/2024	No
53	Ms. M. Ramya	XXXXXXX08R	M.E/M.Tech	JNTUA	VLSI Design	02/09/2023	1.5	Assistant Professor	Assistant Professor		Regular	No	01/08/2024	No
54	Mr. D. Theja	XXXXXXX72M	M.E/M.Tech	Sri Venkateswara University Tirupati	Signal processing	17/01/2024	1	Assistant Professor	Assistant Professor		Regular	Yes		No
55	Ms. M. Hema	XXXXXXX89Q	M.E/M.Tech	Sri Venkateswara University Tirupati	Communication Systems	08/07/2024	0.7	Assistant Professor	Assistant Professor		Regular	Yes		No
56	Ms. K. Vasanthaxmi	XXXXXXX24Q	M.E/M.Tech	Sri Padmavathi Mahila Visvavidyalayam	Digital electronics and Communication Systems	08/07/2024	0.7	Assistant Professor	Assistant Professor		Regular	Yes		No
57	Ms. K. Manisha	XXXXXXX23D	M.E/M.Tech	JNTUA	Embedded Systems	08/07/2024	0.7	Assistant Professor	Assistant Professor		Regular	Yes		No
58	Mr. E. Satheesh Kumar	XXXXXXX17J	M.E/M.Tech	VIT	Sensor System Technology	08/07/2024	0.7	Assistant Professor	Assistant Professor		Regular	Yes		No
59	Mr. Y. Mahesh	XXXXXXX59D	M.E/M.Tech	JNTUA	Digital electronics and Communication Systems	09/07/2024	0.7	Assistant Professor	Assistant Professor		Regular	Yes		No
60	Ms. A. Priyanka	XXXXXXX87D	M.E/M.Tech	JNTUA	Digital electronics and Communication Systems	16/07/2024	0.6	Assistant Professor	Assistant Professor		Regular	Yes		No

61	Ms. B. Lekhya	XXXXXXXX29L	M.E/M.Tech	JNTUA	Communication Systems	23/09/2024	0.4	Assistant Professor	Assistant Professor		Regular	Yes		No
----	---------------	-------------	------------	-------	-----------------------	------------	-----	---------------------	---------------------	--	---------	-----	--	----

Table No.C2: Faculty details of Allied Departments for the past 3 years including CAY.

C2. Student-Faculty Ratio (SFR)

No. of UG(Engineering) programs in Department including allied departments/ clusters (UGn):

UG1=1st UG program

UGn=nth UG program

B= No. of Students in UG 2nd year (ST)

C= No. of Students in UG 3rd year (ST)

D= No. of Students in UG 4th year (ST)

No. of PG (Engineering) programs in Department including allied departments/ clusters (PGm):

PG1=1st PG program.

PGm=mth PG program

A= No. of Students in PG 1st year

B= No. of Students in PG 2nd year

Student Faculty Ratio (**SFR**) = S/F

S= No. of students of all programs in the Department including all students of allied departments/clusters.

No. of students (ST)=Sanctioned Intake (SA)+ Actual admitted students via lateral entry including leftover seats (L) if any (limited to 10 % of SA)

Students who admitted under supernumerary quotas (SNQ, EWS, etc) will not be considered in calculating SFR value. Those students are exempted.

F=Total no. of regular or contractual faculty members (Full Time) in the Department, including allied departments/clusters (excluding first year faculty (The faculty members who have a 100% teaching load in the first-year courses)).

No. of UG Programs in the Department1 No. of PG Programs in the Department1

Table No.C2.1: Student-faculty ratio.

Description	CAY(2024-25)	CAYm1 (2023-24)	CAYm2 (2022-23)
UG1.B	198	198	198
UG1.C	198	198	198
UG1.D	198	198	198
UG1: Electronics & Communication Engineering	594	594	594
PG1.A	12	12	12
PG1.B	12	12	12
PG1: Digital Electronics & Communication Systems	24	24	24
DS=Total no. of students in all UG and PG programs in the Department	618	618	618
AS=Total no. of students of all UG and PG programs in allied departments	0	0	0
S=Total no. of students in the Department (DS) and allied departments (AS)	S1= 618	S2= 618	S3= 618
DF=Total no. of faculty members in the Department	37	34	40
AF= Total no. of faculty members in the allied Departments	0	0	0
F=Total no. of faculty members in the Department (DF) and allied Departments (AF)	F1= 37	F2= 34	F3= 40
FF=The faculty members in F who have a 100% teaching load in the first-year courses	0	0	0
Student Faculty Ratio (SFR)=S/(F-FF)	SFR1= 16.70	SFR2= 18.18	SFR3= 15.45
Average SFR for 3 years	SFR= 16.78		

C3. Faculty Qualification

- Faculty qualification index (FQI) = $2.5 * [(10X + 4Y)/RF]$ where
- X=No. of faculty members with Ph.D. degree or equivalent as per AICTE/UGC norms.
- Y=No. of faculty members with M. Tech. or ME degree or equivalent as per AICTE/ UGC norms.
- RF=No. of required faculty in the Department including allied Departments to adhere to the 20:1 Student-Faculty ratio, with calculations based on both student numbers and faculty requirements as per section C2 of this documents: (RF=S/20).

Table No.C3.1: Faculty qualification.

Year	X	Y	RF	FQ = $2.5 \times [(10X + 4Y) / RF]$
2024-25(CAY)	8	29	30.00	16.33
2023-24(CAYm1)	7	27	30.00	14.83
2022-23(CAYm2)	8	32	30.00	17.33

C4. Faculty Cadre Proportion

- Faculty Cadre Proportion is 1(RF1): 2(RF2): 6(RF3)
- RF1= No. of Professors required = $1/9 * \text{No. of Faculty required to comply with 20:1 Student-Faculty ratio based on no. of students (S) as per C2 of this documents.}$
- RF2= No. of Associate Professors required = $2/9 * \text{No. of Faculty required to comply with 20:1 Student-Faculty ratio based on no. of students (S) as per section C2 of this documents.}$
- RF3= No. of Assistant Professors required = $6/9 * \text{No. of Faculty required to comply with 20:1 Student-Faculty ratio based on no. of students (S) as per section C2 of this documents.}$
- Faculty cadre and qualification and experience should be as per AICTE/UGC norms.

Table No.C4.1: Faculty cadre proportion details.

Year	Professors		Associate Professors		Assistant Professors	
	Required RF1	Available AF1	Required RF2	Available AF1	Required RF3	Available AF3
2024-25	3.00	4.00	6.00	3.00	20.00	30.00
2023-24	3.00	4.00	6.00	3.00	20.00	27.00
2022-23	3.00	4.00	6.00	4.00	20.00	32.00
Average	RF1=3.00	AF1=4.00	RF2=6.00	AF2=3.33	RF2=20.00	AF2=29.67

C5. Visiting/Adjunct Faculty/Professor of Practice

Table No. C5.1: List of visiting/adjunct faculty/professor of practice and their teaching and practical loads.

(CAYm1)

S.No	Name of the Person	Designation	Organization	Name of the Course	No. of hours handled
1	Mr.P Shahensha	Digital IT Manager	Conneqt Business Solutions	Basics of Cloud Computing(BCC)	75.00
2	Mr.B.Ramesh	Research Scholar	VIT,Vellore	Basics of Cloud Computing(BCC)	39.00

(CAYm2)

S.No	Name of the Person	Designation	Organization	Name of the Course	No. of hours handled
1	Ms.T.Ramya	Research Scholar	VIT,Vellore	Basics of Cloud Computing(BCC)	78.00
2	Mr.B.Ramesh	Research Scholar	VIT,Vellore	Basics of Cloud Computing(BCC)	36.00

(CAYm3)

S.No	Name of the Person	Designation	Organization	Name of the Course	No. of hours handled
1	Mr.B.Ramesh	Research Scholar	VIT,Vellore	Basics of Python Programming	60.00

C6. Academic Research

Table No. C6.1: Faculty publication details.

S.No.	Item	2023-24 (CAYm1)	2022-23 (CAYm2)	2021-22 (CAYm3)
1	No. of peer reviewed journal papers published	46	55	32
2	No. of peer reviewed conference papers published	50	51	22
3	No. of books/book chapters published	2	5	4

C7. Sponsored Research Project

Table No. C7.1: List of sponsored research projects received from external agencies.

(CAYm1)

(CAYm2)

(CAYm3)

Total Amount (Lacs) Received for the Past 3 Years: NIL

Note*:

- Only sponsored research projects will be considered. Infrastructure-based projects will not be considered here.

C8. Consultancy Work

Table No. C8.1: List of consultancy projects received from external agencies.

(CAYm1)

PI Name	Co-PI names if any	Name of the Dept., where project is sanctioned	Project Title*	Name of the Funding agency	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25
Dr.P.Harish	Mr.N.Dilip Kumar	Consultancy	Agriculture Spraying using Drone	Private Customer	2 Months	0.60
						Amount received (Rs.):0.60

(CAYm2)

PI Name	Co-PI names if any	Name of the Dept., where project is sanctioned	Project Title*	Name of the Funding agency	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25
Dr.P.Harish	Mr.N.Dilip Kumar	Consultancy	Agriculture Spraying using Drone	Private Customer	35 Days	0.40
						Amount received (Rs.):0.40

(CAYm3)

PI Name	Co-PI names if any	Name of the Dept., where project is sanctioned	Project Title*	Name of the Funding agency	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25
Dr.P.Harish	Mr.N.Dilip Kumar	Consultancy	Agriculture Spraying using Drone	Private Customer	35days	0.40
						Amount received (Rs.):0.40

Total amount (Lacs) received for the past 3 years: 1.40

Note*:

- Only consultancy projects will be considered. Infrastructure-based projects will not be considered here.

C9. Institution Seed Money or Internal Research Grant to its Faculty for Research Work

Table No. C9.1: List of faculty members received seed money or internal research grant from the Institution.

(CAYm1)

Faculty name	Project title/ Support for Activity	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25	Amount Utilized(Lacs) i.e. 15,25,000=15.25	Outcomes of the project
Dr.P.Harish	MOOT-AlexNet: A Versatile Framework for brain Tumor Detection	4 months	0.25	0.25	Enhances brain tumor detection accuracy
Dr.P.Harish	A Hybrid Segmentation method BCOT for Skin Lesion Detection	3 Months	0.30	0.30	BCOT enhances skin lesion detection
Dr.N.Pushpalatha	Development of a Multi Wi-band follow Me drone with camera and obstacle avoidance capability	4 Months	0.30	0.30	Obstacle avoidance for autonomous tracking.
Mr.N.Dilipkumar	Design and analysis of dual band microstrip	3 Months	0.20	0.20	efficient performance for 5G communications
			Amount received (Rs.): 1.05		

(CAYm2)

Faculty name	Project title/ Support for Activity	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25	Amount Utilized(Lacs) i.e. 15,25,000=15.25	Outcomes of the project
Dr.I.Suneetha	Accident Avoidance System	4 Months	0.35	0.35	The system prevents accidents by detecting obstacles in advance
Mr.N.Dilipkumar	Wireless notice board	2 Months	0.20	0.20	A wireless notice board allows digital messages to be displayed and updated remotely without the need for physical connections
			Amount received (Rs.): 0.55		

(CAYm3)

Faculty name	Project title/ Support for Activity	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25	Amount Utilized(Lacs) i.e. 15,25,000=15.25	Outcomes of the project
Dr.N.Pushpalatha	Advancements and applications in electronics and communication engineering	6 months	0.25	0.25	Public awareness
Dr.R.Kalyan	Compact ultra wide band microstrip antenna for satellite base station applications	4 Months	0.25	0.25	The compact UWB microstrip antenna delivers efficient performance for satellite base station applications
Ms.K.S.Deveswari	Automatic Hand Sanitization system	2 Months	0.20	0.20	An automatic hand sanitization system dispenses sanitizer when it detects the presence of a hand, promoting hygiene without physical contact.
			Amount received (Rs.): 0.70		

Total amount (Lacs) received for the past 3 years : 2.30

PART D: Laboratory Infrastructure in the Department

(Data to be filled in for the Department)

D1. Adequate and Well-Equipped Laboratories, and Technical Manpower

Table No.D1.1: List of laboratories and technical manpower.

Sr. No	Name of the Laboratory	Number of students per set up(Batch Size)	Name of the Important Equipment	Weekly utilization status(all the courses for which the lab is utilized)	Technical Manpower Support		
					Name of the Technical staff	Designation	Qualification
1	Electronic Devices and Circuits Lab	4	• Cathode Ray Oscilloscope • Function Generators • Regulated Power Supply • Breadboard Trainer System • IC Tester • Digital Multimeter • Cathode Ray Oscilloscope	36Hrs/Week	Ms.M. Hemavathi	Lab Technician	B.Tech
2	IC Applications Lab	4	Cathode Ray oscilloscope Function Generators • Analog IC Tester • Analog System Lab Starter Kits • Digital Multimeter • Cathode Ray Oscilloscope	36Hrs/Week	Ms.M. Hemavathi	Lab Technician	B.Tech
3	Microwave and optical Communications Lab	4	Cathode Ray Oscilloscope • Klystron Power Supply • Klystron tubes • VSWR Meters • Isolators • Variable Attenuators • Standing Wave Ratio Meter • Matched Terminations	18Hrs/ Week	Mr. Chunchu Mahith	Lab Technician	B.Tech
4	Unmanned Aerial Vehicles lab	4	Unmanned Aerial Vehicles, Battery and chargers	18 Hrs/Week	Mr.M.Tej kumar	Lab Technician	M.Tech
5	Analog and Digital Communications Lab	4	• Cathode Ray Oscilloscope • Amplitude Modulation and Demodulation Trainer kit • Frequency Modulation and Demodulation Trainer kit • Study of Resonance	18Hrs/Week	Mr. Chunchu Mahith	Lab Technician	M.Tech
6	Basics Electronics Engineering lab	4	Cathode Ray Oscilloscope • Regulated Power supply Ammeters • Voltmeters • Rheostats, Digital Multimeters	36 Hrs/Week	Ms.P.Satvika	Lab Technician	B.Tech
7	Signal Processing and Simulation Lab	1	Intel Core i3 7100/8GB DDR4 RAM • CC Studio • MATLAB2015b • Multisim–NI Circuit Design suite • D-Link	36Hrs/ Week	Ms.A.Poornima	Lab Technician	B.Com

8	VLSI and Embedded Systems lab	1	TIVA C-Series Launch pack • Xilinx 9.2ISE	36Hrs/Week	Mr.U.V.Prasad	Lab Technician	Diploma
9	Microprocessor and Microcontrollers Lab	1	Dual DAC kit • 8086 Micoprocessor Trainer • 8051 Microcontroller Trainer kit • Dual DAC Interface • Elevatedataface 8870studboard 8850studboard 8852	18 Hrs/Week	Mr.B.Vijay Kumar	Lab Technician	Diploma

D2. Safety Measures in Laboratories

Table No. D2.1: List of various safety measures in laboratories.

Sr. No	Laboratory Name	Safety Measures
1	Electronic Devices and Circuits Lab	<ul style="list-style-type: none"> • Keep Work Area Dry – Ensure the lab and hands are dry before handling electrical components. • Proper Ventilation - Work in well-ventilated areas when using soldering irons or chemicals. • Know Emergency Procedures – Familiarize yourself with fire exits, first-aid kits, and emergency shut-off switches. • Turn Off Power When Working on Circuits – Always power down before making modifications. • Use Proper Insulation – Ensure all wires and components are properly insulated. • Avoid Overloading Circuits – Never exceed the rated capacity of power supplies and components. • Use Proper Tools– Use insulated tools for handling live circuits. • Check Connections Before Powering On – Double-check circuit connections to prevent short circuits. • Handle ICs with Care – Avoid touching pins and store them in anti-static foam. • Label Wires and Components – Prevent mix-ups and incorrect connections. • Dispose of Batteries and Components Properly – Follow e-waste disposal guidelines. • Never Touch a Hot Soldering Iron – Always place it in a stand when not in use. • Use Fire Extinguishers – Ensure fire extinguishers are accessible.
2	IC Applications Lab	<ul style="list-style-type: none"> • Keep Work Area Dry – Ensure the lab and hands are dry before handling electrical components. • Proper Ventilation - Work in well-ventilated areas when using soldering irons or chemicals. • Know Emergency Procedures – Familiarize yourself with fire exits, first-aid kits, and emergency shut-off switches. • Turn Off Power When Working on Circuits – Always power down before making modifications. • Use Proper Insulation – Ensure all wires and components are properly insulated. • Avoid Overloading Circuits – Never exceed the rated capacity of power supplies and components. • Use Proper Tools– Use insulated tools for handling live circuits. • Check Connections Before Powering On – Double-check circuit connections to prevent short circuits. • Handle ICs with Care – Avoid touching pins and store them in anti-static foam. • Label Wires and Components – Prevent mix-ups and incorrect connections. • Dispose of Batteries and Components Properly – Follow e-waste disposal guidelines. • Never Touch a Hot Soldering Iron – Always place it in a stand when not in use. • Use Fire Extinguishers – Ensure fire extinguishers are accessible.
3	Microwave and optical Communications Lab	<ul style="list-style-type: none"> • Keep Work Area Dry – Ensure the lab and hands are dry before handling electrical components. • Proper Ventilation - Work in well-ventilated areas when using soldering irons or chemicals. • Know Emergency Procedures – Familiarize yourself with fire exits, first-aid kits, and emergency shut-off switches. • Turn Off Power When Working on Circuits – Always power down before making modifications. • Use Proper Insulation – Ensure all wires and components are properly insulated. • Avoid Overloading Circuits – Never exceed the rated capacity of power supplies and components. • Use Proper Tools– Use insulated tools for handling live circuits. • Check Connections Before Powering On – Double-check circuit connections to prevent short circuits. • Handle ICs with Care – Avoid touching pins and store them in anti-static foam. • Label Wires and Components – Prevent mix-ups and incorrect connections. • Dispose of Batteries and Components Properly – Follow e-waste disposal guidelines. • Never Touch a Hot Soldering Iron – Always place it in a stand when not in use. • Use Fire Extinguishers – Ensure fire extinguishers are accessible.
4	Unmanned Aerial Vehicles lab	<ul style="list-style-type: none"> • Keep Work Area Dry – Ensure the lab and hands are dry before handling electrical components. • Proper Ventilation - Work in well-ventilated areas when using soldering irons or chemicals. • Know Emergency Procedures – Familiarize yourself with fire exits, first-aid kits, and emergency shut-off switches. • Turn Off Power When Working on Circuits – Always power down before making modifications. • Use Proper Insulation – Ensure all wires and components are properly insulated. • Avoid Overloading Circuits – Never exceed the rated capacity of power supplies and components. • Use Proper Tools– Use insulated tools for handling live circuits. • Check Connections Before Powering On – Double-check circuit connections to prevent short circuits. • Handle ICs with Care – Avoid touching pins and store them in anti-static foam. • Label Wires and Components – Prevent mix-ups and incorrect connections. • Dispose of Batteries and Components Properly – Follow e-waste disposal guidelines. • Never Touch a Hot Soldering Iron – Always place it in a stand when not in use. • Use Fire Extinguishers – Ensure fire extinguishers are accessible.

5	Analog and Digital Communications Lab	<ul style="list-style-type: none"> • Keep Work Area Dry – Ensure the lab and hands are dry before handling electrical components. • Proper Ventilation - Work in well-ventilated areas when using soldering irons or chemicals. • Know Emergency Procedures – Familiarize yourself with fire exits, first-aid kits, and emergency shut-off switches. • Turn Off Power When Working on Circuits – Always power down before making modifications. • Use Proper Insulation – Ensure all wires and components are properly insulated. • Avoid Overloading Circuits – Never exceed the rated capacity of power supplies and components. • Use Proper Tools– Use insulated tools for handling live circuits. • Check Connections Before Powering On – Double-check circuit connections to prevent short circuits. • Handle ICs with Care – Avoid touching pins and store them in anti-static foam. • Label Wires and Components – Prevent mix-ups and incorrect connections. • Dispose of Batteries and Components Properly – Follow e-waste disposal guidelines. • Never Touch a Hot Soldering Iron – Always place it in a stand when not in use. • Use Fire Extinguishers – Ensure fire extinguishers are accessible.
6	Basics of Electrical and Electronics Engineering lab	<ul style="list-style-type: none"> • Keep Work Area Dry – Ensure the lab and hands are dry before handling electrical components. • Proper Ventilation - Work in well-ventilated areas when using soldering irons or chemicals. • Know Emergency Procedures – Familiarize yourself with fire exits, first-aid kits, and emergency shut-off switches. • Turn Off Power When Working on Circuits – Always power down before making modifications. • Use Proper Insulation – Ensure all wires and components are properly insulated. • Avoid Overloading Circuits – Never exceed the rated capacity of power supplies and components. • Use Proper Tools– Use insulated tools for handling live circuits. • Check Connections Before Powering On – Double-check circuit connections to prevent short circuits. • Handle ICs with Care – Avoid touching pins and store them in anti-static foam. • Label Wires and Components – Prevent mix-ups and incorrect connections. • Dispose of Batteries and Components Properly – Follow e-waste disposal guidelines. • Never Touch a Hot Soldering Iron – Always place it in a stand when not in use. • Use Fire Extinguishers – Ensure fire extinguishers are accessible.
7	Signals and Systems Lab	<ul style="list-style-type: none"> • Maintain Cleanliness – Keep the workstation tidy and free of clutter. • Follow Lab Rules – Use equipment only for academic purposes; unauthorized activities are prohibited. • Respect Lab Time & Equipment – Avoid unnecessary use of resources and adhere to time slots. • Check Power Cables & Connections – Ensure cables are in good condition and properly plugged in. • Avoid Overloading Power Sockets – Prevent circuit failures and overheating. • Shut Down Properly – Always log out and power down systems when done. • Report Faulty Equipment – Notify the lab supervisor if a device is malfunctioning. • Adjust Screen Brightness – Reduce eye strain by optimizing screen settings. • Keep Hands Clean – Prevent dirt buildup on keyboards and mice. • Use Strong Passwords – Protect your accounts with unique and secure passwords. • No Unauthorized Software Installation – Avoid downloading unapproved applications that may contain malware. • Beware of Phishing & Cyber Threats – Never share login credentials or open suspicious emails. • Backup Important Data – Save work regularly to prevent data loss. • Log Out from Shared Computers – Prevent unauthorized access to your files. • Know Emergency Exits – Be aware of fire exits and evacuation procedures. • Use Fire Extinguishers if Necessary – Understand how to use electrical fire extinguishers • Report Security Issues – Immediately inform lab staff about any cyber threats or technical problems.
8	VLSI and Embedded Systems lab	<ul style="list-style-type: none"> • Maintain Cleanliness – Keep the workstation tidy and free of clutter. • Follow Lab Rules – Use equipment only for academic purposes; unauthorized activities are prohibited. • Respect Lab Time & Equipment – Avoid unnecessary use of resources and adhere to time slots. • Check Power Cables & Connections – Ensure cables are in good condition and properly plugged in. • Avoid Overloading Power Sockets – Prevent circuit failures and overheating. • Shut Down Properly – Always log out and power down systems when done. • Report Faulty Equipment – Notify the lab supervisor if a device is malfunctioning. • Adjust Screen Brightness – Reduce eye strain by optimizing screen settings. • Keep Hands Clean – Prevent dirt buildup on keyboards and mice. • Use Strong Passwords – Protect your accounts with unique and secure passwords. • No Unauthorized Software Installation – Avoid downloading unapproved applications that may contain malware. • Beware of Phishing & Cyber Threats – Never share login credentials or open suspicious emails. • Backup Important Data – Save work regularly to prevent data loss. • Log Out from Shared Computers – Prevent unauthorized access to your files. • Know Emergency Exits – Be aware of fire exits and evacuation procedures. • Use Fire Extinguishers if Necessary – Understand how to use electrical fire extinguishers • Report Security Issues – Immediately inform lab staff about any cyber threats or technical problems.
9	Microprocessor and Microcontrollers Lab	<ul style="list-style-type: none"> • Maintain Cleanliness – Keep the workstation tidy and free of clutter. • Follow Lab Rules – Use equipment only for academic purposes; unauthorized activities are prohibited. • Respect Lab Time & Equipment – Avoid unnecessary use of resources and adhere to time slots. • Check Power Cables & Connections – Ensure cables are in good condition and properly plugged in. • Avoid Overloading Power Sockets – Prevent circuit failures and overheating. • Shut Down Properly – Always log out and power down systems when done. • Report Faulty Equipment – Notify the lab supervisor if a device is malfunctioning. • Adjust Screen Brightness – Reduce eye strain by optimizing screen settings. • Keep Hands Clean – Prevent dirt buildup on keyboards and mice. • Use Strong Passwords – Protect your accounts with unique and secure passwords. • No Unauthorized Software Installation – Avoid downloading unapproved applications that may contain malware. • Beware of Phishing & Cyber Threats – Never share login credentials or open suspicious emails. • Backup Important Data – Save work regularly to prevent data loss. • Log Out from Shared Computers – Prevent unauthorized access to your files. • Know Emergency Exits – Be aware of fire exits and evacuation procedures. • Use Fire Extinguishers if Necessary – Understand how to use electrical fire extinguishers • Report Security Issues – Immediately inform lab staff about any cyber threats or technical problems.

D3. Project Laboratory/Research Laboratory

1.Skill Development Labs (APSSDC Skills Lab)

- Equipped with industry-standard tools and software for hands-on training in areas like **IoT, Artificial Intelligence, Machine Learning, Embedded Systems**, and **3D printing**.
- **Designed to bridge the skill gap between academic learning and industry requirements.**

2. Centre of Excellence (CoE)

- APSSDC partners with companies like **Siemens, Dassault Systèmes, Autodesk**, etc., to set up **Centres of Excellence**.
- These CoEs focus on advanced domains such as **Automation, Mechatronics, Product Lifecycle Management (PLM), and CAD/CAM**.
- They provide R&D facilities, simulation tools, and prototyping capabilities.

3. Innovation & Incubation Centers

- Dedicated spaces within the labs support **student innovation, entrepreneurship, and early-stage startups**.
- Facilities include **mentor support, funding guidance, ideation spaces**, and access to technical experts.
- Startups can prototype and test their ideas using available equipment.

4. Project & Research Support

- Labs support **UG/PG student projects and faculty research**.
- Access to software tools like **MATLAB, LabVIEW, Ansys**, and hardware platforms like **Arduino, Raspberry Pi**, and **FPGA kits**.
- Collaboration opportunities with industries for applied research.

PART E: First Year faculty and financial Resources

(Data to be filled in for the first year course faculty and budget allocation and utilization)

E1. First Year Student-Faculty Ratio (FYSFR)

Table No. E1.1: FYSFR details.

Year	Sanctioned intake of all UG programs (S4)	No. of required faculty (RF4= S4/20)	No. of faculty members in Basic Science Courses & Humanities and Social Sciences including Management courses (NS1)	No. of faculty members in Engineering Science Courses (NS2)	Percentage= No. of faculty members ((NS1*0.8) + (NS2*0.2))/(No. of required faculty (RF4)); Percentage= ((NS1*0.8) +(NS2*0.2))/RF
2022-23(CAYm2)	180	9	7	11	87
2023-24(CAYm1)	180	9	9	8	98
2024-25(CAY)	180	9	12	11	131

E2. Budget Allocation, Utilization, and Public Accounting at Institute Level

Table No. E2.1: Budget and actual expenditure incurred at Institute level.

Items	Budgeted in 2024-2025	Actual Expenses in 2024-2025 till	Budgeted in 2023-2024	Actual Expenses in 2023-2024 till	Budgeted in 2022-2023	Actual Expenses in 2022-2023 till	Budgeted in 2021-2022	Actual Expenses in 2021-2022 till
Infrastructure Built-Up	230000000	223518686	150000000	141861934	75000000	59449290	0	0
Library	2400000	2215840	2000000	1667205	2600000	2022595	2200000	1610430

Laboratory equipment	1850000	1352317	2485000	2236627	2990000	2627956	8650000	7771046
Teaching and non-teaching staff salary	120000000	115845575	100000000	95183372	90000000	83362693	80000000	74340311
Outreach Programs	515000	314950	465000	381000	430000	365000	430000	340000
R&D	1630000	1360000	1517000	1407000	1030000	841000	685000	568000
Training, Placement and Industry linkage	2500000	1475636	2500000	2406100	2500000	2269106	2000000	1701948
SDGs	6423200	5813204	4721000	4515013	3678600	3500388	3617600	3370692
Entrepreneurship	50000	40000	40000	35000	30000	25000	25000	25000
Others, specify	26225000	24833872	25177500	23510795	26108000	25924336	16582500	15563222
Total	391593200	376770080	288905500	273204046	204366600	180387364	114190100	105290649

E3. Budget Allocation, Utilization, and Public Accounting at Program Specific Level

Table No. E3.1: Budget and actual expenditure incurred at program level.

Items	Budgeted in 2024-2025	Actual Expenses in 2024-2025 till	Budgeted in 2023-2024	Actual Expenses in 2023-2024 till	Budgeted in 2022-2023	Actual Expenses in 2022-2023 till	Budgeted in 2021-2022	Actual Expenses in 2021-2022 till
Laboratory equipment	200000	156232	1200000	1150500	1700000	1610600	900000	836561
Software	50000	0	50000	0	50000	0	50000	0
SDGs	424200	388125	69000	60100	214600	180700	374600	331802
Support for faculty development	40000	20000	30000	19227	30000	15982	15000	11613
R & D	250000	170000	250000	220000	200000	125000	200000	140000
Industrial Training, Industry expert, Internship	100000	86095	200000	175515	80000	63000	50000	30000
Miscellaneous Expenses*	100000	76128	200000	162617	150000	107298	300000	235519
Total	1164200	896580	1999000	1787959	2424600	2102580	1889600	1585495